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USER MANUAL FOR ZIP,  
A Z80 ASSEMBLY LANGUAGE  
INTERPRETER PROGRAM

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**User Manual for ZIP,  
a Z80 Assembly Language  
Interpreter Program**

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## Introduction

ZIP (Z80 Interpreter Program) was written by G. D. Buzzard and W. MacLeod based on a design by T. N. Mudge. Designed as a teaching/debugging tool, ZIP is presently used to aid in the teaching of an introductory microcomputer course. Following the User Manual is an assembly listing of ZIP.

ZIP disassembles and interprets segments of memory containing Z80 machine code and/or data. The disassembled code is displayed on the left side of the CRT screen and the CPU registers, top four words of the stack, and 32 contiguous bytes of memory are displayed on the right side. This configuration provides a visual relationship between the assembly language source code and the dynamic state of the CPU registers, stack, and memory locations. Commands can be entered to control the interpretation of the program, modify register or memory values, and to control the display of information.

## Program Overview

In order to display the disassembled code in an intelligible format the areas of executable code must be distinguished from data areas and unused memory. This is accomplished by searching the target program code for jump, call, and return statements. The location of these statements, along with their targets, when necessary, are used to develop a table of origin and end point (ORG-END) pairs. This table of ORG-END pairs is then used to determine which segments of memory will be disassembled and displayed as source code, and which will be displayed as data.

Upon completion of the ORG-END table the user is prompted for commands. After the execution of any command which affects the target program PC (program counter), a segment of memory is disassembled and displayed. Within the constraints mentioned later, this dynamic disassembly allows the effects of self-modifying code to be observed. Following the execution of commands which change the state of any of the CPU registers, displayed memory locations, or the stack, the right side of the display is updated.

The user is prompted for new commands until program control by ZIP is terminated by:

- 1) The user entering the quit command (QU).
- 2) ZIP interpreting a reset, jump to the operating system, or an unreturned call to the operating system.
- 3) A recognized (interrupts enabled) mode 0 interrupt which supplies an instruction which changes the PC.
- 4) A recognized mode 1 interrupt.

## Operating Instructions

The file containing the load module for ZIP is available on the ECE 365 system diskette. ZIP is loaded in memory at  $C800_{16}$ . The target program stack is initialized at  $C7FF_{16}$  and proceeds towards low memory. Since no address checking is performed on the target program, the user is cautioned against interpreting programs which occupy or modify memory locations near or above  $C800_{16}$ .

The suggested method for running ZIP is to load both ZIP and the target program from SPDS (i.e., !A R,D,MYPGM<cr> !R<cr> !A R,D,ZIP<cr> !R<cr> ), then issue the GO command for address C800<sub>16</sub> (!G 0C800) to begin program execution. From this point on, program flow is controlled by ZIP and the user's interactive commands. Immediately, the CRT screen is reformatted and the user is prompted for a "START ADDRESS?". The starting address of the target program code is to be entered as a four digit hexadecimal number followed by a carriage return. This enables ZIP to track the target program's origins and endpoints. The disassembled text, CPU register contents, top four stack words, and 32 memory locations are then displayed and the user prompted for a command.

### Screen Layout

The layout of the screen is shown in Figure 1. The column on the left shows memory locations in hex (4 hex digits). Alongside these are one to four byte instruction codes also in hex (Z80 instructions can be from one to four bytes in length). Further to the right, the instruction codes are shown in their disassembled form. For example, consider the line covered by the shaded rectangle in the left center. At the left is a memory location (90EE<sub>16</sub>). This location and the subsequent one contain the bytes 10<sub>16</sub> and F7<sub>16</sub> respectively (the Z80 has byte oriented addressing). These disassemble to the Z80 instruction: "DJNZ 90E7" — decrement register B and jump if B is non-zero to location 90E7<sub>16</sub>. Notice that addresses of operands or targets of jumps are not disassembled but are left as absolute addresses. To disassemble these would require access to the symbol table created when the program was assembled. In order to keep the first version of ZIP simple the ability to recover symbolic address was omitted.

As noted above, ZIP automatically determines data areas in memory by examining jumps, subroutine calls, and returns, and when necessary their targets. Memory locations that contain data rather than instructions have their contents displayed as one to four hex digits in the same column as the symbolic instruction codes. Further to the right, in the same column as the disassembled instruction, the contents of the memory is displayed in its ASCII character form.

The right hand side of the screen displays the contents of the Z80's CPU registers, the top four items on the stack, 32 bytes of memory, and the command line.

There are eight 1 byte CPU registers: A, F, B, C, D, E, H, L. These are displayed at the top right of the screen. For example, the second row at the top right shows the contents of A in hex (88), the contents of B in hex (33), followed by the contents of A in binary (10001000), and the contents of F in binary (00110011). The binary display is useful for checking bit operations, shifts, and rotations. The F register is not a general purpose register, instead it holds six 1 bit flags that show the condition codes. Their position is shown in the binary display of F by the header "SZ\*H\*PNC" at the extreme top right (see [2] for their meaning). Immediately below the 1 byte CPU registers are the 16 bit CPU registers: IX, IY, SP, PC. IX and IY are index registers, SP is the stack pointer (points to the top of the stack), and PC is the program counter. The register pairs BC, DE, and HL can also be regarded as 16 bit registers and the format of the display has been set up to allow this view. To the right of the 16 bit registers appears the two special 1 byte registers I and R. Below the 16 bit registers appears the top four stack items. These items are one word, or two bytes each, thus in Figure 1, for example the top of the stack is at location F3F8<sub>16</sub> (see contents of SP)

and the top item is the 16 bit quantity ED08<sub>16</sub>. The bytes of the top four words of the stack are shown in the reverse order from which they appear in memory; left-to-right within each word corresponds to high-to-low memory addresses. The stack grows towards low memory. The orientation of the bytes displayed in each word is consistent with the orientation of the bytes displayed in the 18 bit registers and the register pairs BC, DE, and HL. In all cases, 16 bit words are stored with their most significant byte at the higher memory location.

Below the stack display a user selected 32 byte area of memory is displayed in hex. Finally, below that the command currently being entered by the user is shown.

The shaded rectangles in Figure 1 indicate reverse video. Thus the instruction to be executed next is the DJNZ mentioned above. In addition the contents of the H and L registers are shown in reverse video indicating that the most recently executed instruction -- "INC HL" -- caused their contents to be altered. If any of the memory locations already displayed on the screen had been altered they would also be shown in reverse video.

The command line is shown with an reverse video square alongside it to distinguish it. The particular command shown in figure 1 reads: beginning with the current instruction (the DJNZ) execute the program until the contents of registers A and B have been equal three times. The command is terminated with a carriage return; the return initiates ZIP's interpretation of the command line. The left side of the display scrolls so that the next instruction to be interpreted (i.e., the instruction displayed in reverse video) is always kept in the middle of the screen.

### Command Syntax

Figure 1 shows ZIP's command syntax in standard BNF (Backus Naur Form) notation. The current version of ZIP enforces rather severe spacing restrictions:

- 1) Exactly one space is required between the command words GO, SE, DI and the productions which follow them. The command words which are not followed by any productions may be followed by any combination of other characters.
- 2) Repetition factors (i.e., :12) must be preceded by one or more spaces.
- 3) No spaces other than those mentioned above are allowed.
- 4) All commands must be terminated by carriage returns.

Note: only the first two letters of the command word are interpreted. Therefore, GO\_BLUE 5000 is interpreted the same as GO 5000.

The modification to ZIP to accommodate arbitrary spacing is straight forward and will be implemented at a later date.

### Command Descriptions

GO --

causes the contents of the target program PC to be replaced by the specified value. The disassembled code is updated to reflect this change, and all reverse video on the right side of the screen, with the exception of

the PC, unless it is left unchanged, is reset to normal video.

**SET --**

causes the contents of the indicated register or memory location to be replaced by the specified value. Unless the specified value equals the previous value, the indicated register or memory location will be displayed in reverse video. It is possible to change the contents of any memory location regardless of whether or not it is displayed.

**DISPLAY --**

displays 32 contiguous bytes of memory, beginning with the specified address, in the memory display area of the screen. All resulting memory display screen locations with values differing from their previous ones are displayed in reverse video, while those screen locations remaining unchanged are displayed in normal video. This feature facilitates a quick byte by byte comparison of different memory locations.

**Trigger Conditions --**

cause the target program to be interpreted until the specified condition is met. All displayed values which have changed since the last command are shown in reverse video, while those which have not changed are shown in normal video. The <tail> production specifies the number of target program instructions to be interpreted, if the number is omitted a default of one is assumed. The second alternative of the <tail> production specifies an optional repetition factor. And, the <memory><relation><rhs\_memory> construct, when used, operates on comparisons of 8 bits in length.

**ON --**

fills the screen with 256 contiguous bytes of memory beginning with the 32 bytes which were displayed in the normal screen format.

**OFF --**

is used in conjunction with ON. OFF returns the screen to its normal format. All reverse video which appeared in the memory display area of the screen previous to the ON command is reset to normal video.

**AU --**

swaps the alternate A and F registers for the present ones. This command has a toggling action, and may be repeated several times in succession. Any resulting change (with respect to the values which were displayed at the end of the preceding command) is shown in reverse video.

**UA --**

performs the same function as AU on the remaining CPU general registers (i.e., B,C,D,E,H,L).

**OLD --**

displays the right side of the screen exactly as it appeared prior to the last trigger condition command.

**NEW --**

is used in conjunction with OLD. NEW restores the right side of the screen to reflect the current state of the registers and memory. All reverse video which appeared prior to the OLD command is reset to normal video.

**QUIT --**

terminates execution of ZIP and returns control to SPDS.

### System Routines

Operating system routines are not interpreted. Therefore, the execution of all operating system routines appear transparent to the user. The single step interpretations of some common operating system call statements are described below:

CALL CO (console output) --

The ASCII character representing the contents of the C register is flashed briefly near the lower left corner of the screen, and the user is prompted for the next command.

CALL CICO (console input console output) --

Program execution enters a wait loop until an input from the keyboard is received. The character entered is flashed briefly near the lower left corner of the screen, and the corresponding ASCII value is displayed in the A register.

CALL CI (console input) --

Program execution enters a wait loop until an input from the keyboard is received. Upon receiving an input the ASCII value corresponding to the entry is displayed in the A register.

### Self Modifying Code

The target program is tracked only once, establishing the ORG-END table prior to any part of the target program being executed. Hence, if the target program dynamically modifies a memory area which was executable code into a data field, or vice versa, the display on the left side of the screen will periodically become unintelligible. This, however, should not affect the correct execution of the target program. Self modifying code which does not change executable code into data, or vice versa, is interpreted without any adverse affects.

### Computed Jumps

Another result of the program being tracked only once is that the run-time targets of computed jumps (i.e., JP(HL), JP(DX), and JP(IX)) cannot be determined. When the tracking routine encounters a computed jump the tracking is aborted and the user queried to provide the entries for the ORG-END table.

The format required for user entry of the ORG-END table is as follows:

- 1) All entries must appear as four digit hex numbers followed by a carriage return.
- 2) All entries must be entered as ORG-END pairs, with END's being entered immediately after their corresponding ORG's.
- 3) The entries must be entered in ascending numerical order of ORG values.
- 4) The last two entries must be FFFF and 0000 respectively.



### Interrupts

Mode 0 interrupts (see [2] for descriptions) cannot be detected by ZIP. But, provided that the instruction supplied by the peripheral device does not change the PC, any resulting changes in the CPU registers, top for stacks words, or displayed memory will be shown.

As stated earlier, mode 1 interrupts result in program control being returned to the operating system. This action is effected via the equivalent of a "RST 38H" instruction.

For mode 2 interrupts the current version of ZIP will not dynamically trace the execution of an interrupt service routine during the interpretation of the target program. The final CPU register status, top four stack values, and displayed memory values will be shown, but, the execution of the interrupt service routine will appear transparent. However, ZIP can be coerced to request the user to make entries to the ORG-END table -- SPDS users can do this by entering C800 for the "START ADDRESS?". Then, ORG-END pairs can be entered which encompass only the interrupt service routine, thus allowing the routine to be interpreted as a separate entity. This independent interpretation of the interrupt service routine is analogous to the testing of an external subroutine of a structured computer program before the main (calling) program is tested as a whole.

By interpreting the target program up to the point where the interrupt would occur the pertinent register and memory values can be obtained. These values can then be loaded into the appropriate places at the beginning of the interpretation of the interrupt service routine by using the SET command.

### Refresh Register

The R (refresh) register exhibits some unique characteristics during program interpretations by ZIP. While the R register display does indeed reflect the actual value of the CPU R-register at the beginning of an instruction simulation, the R-register display may not reflect the actual value of the CPU R-register after any occurrence of the following simulation events:

- 1) Calls to the operating system.
- 2) Interrupts which are handled during instruction simulation.

These discrepancies are possible because the CPU R-register display value is computed by ZIP, and not merely taken from the CPU R-register. This is done in an effort to closely approximate the decrementing of the R-register during the execution of the target program alone, without reflecting the refresh cycles which occur during the execution of ZIP program code. However, the number of memory refresh cycles cannot be computed for operating system subroutines, interrupt service routines, or mode 0 interrupt instructions because their execution is not dynamically traced.

It is important to remember that the CPU R-register, which is initially set to  $00_{16}$ , is coerced by ZIP and **does** contain the value indicated in the display during the execution of the instruction which is shown in reverse video on the screen.

**References**

- [1] T. N. Mudge. "Teaching Assembly Language Using an Assembly Language Interpreter." Proc. 1981 ASEE Annual Conference, Univ. So. Cal., June 1981.
- [2] Microcomputer Data Book, Mostek, 1979.

Illustrations

90D7	05	PUSH	BC						SZ#H#PNC
90D8	05	PUSH	DE						A: 88 33: F 10001000 00110011
90D9	E5	PUSH	HL						B: 00 18: C 00000000 00011000
90DA	DD E5	PUSH	IX						D: 83 40: E 10000011 01000000
90DC	FD E5	PUSH	IY						H: [redacted] L 01100101 00111110
90DE	21 4F 92	LD	HL, 924F						IX: 1290 SP: F3F8 I: 11
90E1	FD 21 45 F8	LD	IY, F845						IY: 1413 PC: 90EE R: 4C
90E5	06 08	LD	B, 08						STACK: ED08 9024 0000 600F
90E7	7E	LD	A, (HL)						MEMORY:
90E8	FD 77 00	LD	(IY), A						7000: 73 03 00 20 39 31 30 43
90EB	FD 23	INC	IY						7008: 20 20 46 44 20 37 33 20
90ED	23	INC	HL						7010: 30 33 20 20 20 20 4C 44
									7018: 20 20 20 20 20 20 28 49
90F0	FD 21 82 F8	LD	IY, F882						A=B 3
90F4	DD 21 00 90	LD	IX, 9000						
90F8	06 04	LD	B, 04						
90FA	7E	LD	A, (HL)						
90FB	FD 77 00	LD	(IY), A						
90FE	3E 3A	LD	A, 3A						
9100	FB 77 01	LD	(IY+01), A						
9103	DD 7E 00	LD	A, (IX)						
9106	CD 5C 94	CALL	945C						
9109	FD 72 02	LD	(IY+02), B						
910C	FD 73 03	LD	(IY+03), E						

Figure 1. Screen layout

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<command> ::= GO<hex><hex><hex><hex>|<set>|<display>|
            <trigger_condition>|ON|OF|AU|UA|OL|NE|QU
<set> ::= SE<reg>=<hex><hex>|SE<double_reg>=<hex><hex><hex><hex>|
        SE<memory>=<hex><hex>
<display> ::= DI<memory>
<trigger_condition> ::= <condition><taill>|<tail>
<condition> ::= <reg><relation><rhs_reg>|
               double_reg<relation><rhs_double>|
               <memory><relation><rhs_memory>|
               F=<bit><bit><bit><bit><bit><bit>
<reg> ::= A|B|C|D|E|H|L
<relation> ::= =|<|>|<|>|
<rhs_reg> ::= <reg>|<hex><hex>|<memory>
<double_reg> ::= BC|DE|HL|SP|PC|IX|IY
<rhs_double> ::= <double_reg>|<hex><hex><hex><hex>|<memory>
<rhs_memory> ::= <hex><hex>|<memory>
<memory> ::= @<hex><hex><hex><hex>
<hex> ::= 0|1|2|3|4|...|F
<taill> ::= cr|<number>cr
<tail> ::= <number>cr|cr
<number> ::= <hex>|<hex><hex>|<hex><hex><hex>|<hex><hex><hex><hex>
<bit> ::= 0|1|X
    
```

Figure 2. ZIP's Syntax.

**Assembly Listing**

The following pages contain a commented Z80 assembly listing for ZIP.



00001 COMMENTS

00002

00003

00004

00005

BY G. D. BUZZARD

APRIL 1981

LAST UPDATED 08-21-81

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00056

THIS IS A SET OF ROUTINES TO FACILITATE AND  
DIRECT THE EXECUTION OF ZIP (Z-80 INTERPRETER  
PROGRAM).

RADIX 16

ASCII A  
ASCII F +1  
ASCII O  
ASCII ? +1  
COMMAND LINE LOCATION  
ASCII CR  
CURSOR HOME (HIGH)  
CURSOR HOME (LOW)  
CURSOR ERROR POSITION  
KEYBOARD INPUT ADDRESS  
KEYBOARD STATUS REGISTER  
QUESTION POSITION  
" " (LOW)  
RESPONSE POSITION  
QUESTION START ADDRESS  
SYSTEM ADDRESS  
ADDRESS FOR TITLE  
USER STACK LOCATION  
V-RAM IN CODE  
V-RAM OUT CODE

EXTRN ACONV  
EXTRN BANKST  
EXTRN BANKSW  
EXTRN CIOO  
EXTRN CURSES  
EXTRN CURSOR  
EXTRN FINTOP  
EXTRN KEYIN  
EXTRN NS1  
EXTRN MDISP  
EXTRN ORGEND  
EXTRN PRESAV  
EXTRN REGA  
EXTRN REGAF  
EXTRN REGB  
EXTRN REGC  
EXTRN REGD  
EXTRN REGE  
EXTRN REGF  
EXTRN REGH  
EXTRN REGL  
EXTRN REGIX  
EXTRN REGIY  
EXTRN REGSP

```

00057      EXTRN  REGPC
00058      EXTRN  RSTATE
00059      EXTRN  REVID
00060      EXTRN  REVMEM
00061      EXTRN  REGSAV
00062      EXTRN  SCREEN
00063      EXTRN  SAVIT
00064      EXTRN  SAVE
00065      EXTRN  SIMUL
00066      EXTRN  TEMP
00067      EXTRN  TEXTUP
00068      EXTRN  TRACK
00069      EXTRN  WIFE
00070      EXTRN  XRAF
00071      EXTRN  XRBC

00072      ENTRY  MAIN
00073      ENTRY  HEXIT
00074      ENTRY  SYN

00077 MAIN: LD      SP, STKP      ; SET OUR STACK
00078      CALL   BANKST    ; SET BANKSWITCH REG
00079      CALL   INIT      ; INITIALIZE STORAGE
00080      LD      A, VIDEO   ;
00081      CALL   BANKSW    ; SWITCH IN V-RAM
00082      CALL   QUERY     ; GET START ADDRESS
00083      LD      HL, (STRT) ; LOAD IT INTO HL
00084      CALL   TRACK     ; TRACK TARGET PROGRAM
00085      LD      HL, (ORGEND) ; ABSOLUTE START ADDR
00086      LD      (REGPC), HL ; AND INTO REGPC
00087      CALL   SCREEN    ;
00088      LD      A, CURSYN ;
00089      LD      (CURERR), A ; ERROR CURSOR POSITION
00090      LD      A, VIDEO   ;
00091      CALL   BANKSW    ; SWITCH IT IN AGAIN
00092 RPT: LD      HL, COMLOC ;
00093      CALL   CICO      ; GET INPUT
00094      CALL   DECIDE    ;
00095      JR      RPT      ;
00096      ;
00097      ; QUERY QUERIES THE USER FOR THE STARTING ADDRESS
00098      ; OF THE PROGRAM CODE. ONLY THE FIRST FOUR CHARACTERS
00099      ; OF THE RESPONSE ARE USED (45678=>4567).
00100      ;
00101 QUERY: PUSH   AF
00102      PUSH   BC
00103      PUSH   DE
00104      PUSH   HL
00105      ;
00106      CALL   WIPE      ; CLEAR SCREEN
00107      LD      DE, TITL
00108      LD      HL, ZIP
00109      LD      BC, 18
00110      LDIR     ; WRITE TITLE
00111      ;
00112      LD      DE, Q1

```

```

4A 01 000F 00113 LD BC,OF
4B ED 50 00114 LDIR ; WRITE "START ADDR"
00115 ;
4F 21 0000* 00116 LD HL,CURSES
52 23 00117 INC HL ; CHANGE
53 3E 00 00118 LD A,0CURSH ;
55 77 00119 LD (HL),A ; CURSOR
56 23 00120 INC HL ;
57 23 00121 INC HL ; ADDRESS
58 3E FF 00122 LD A,0CURSL
5A 77 00123 LD (HL),A
00124 ;
5B CD 0000* 00125 CALL CURSOR
5E EE 00126 EX DE,HL ; PUT SCR ADDR IN HL
5F CB 0000* 00127 CALL CICO
00128 ;
00129 ; GET INPUT, CONVERT AND CHECK SYNTAX
62 21 00E3* 00130 L22: LD HL,STR+1 ; LOCN FOR START ADDR
65 06 02 00131 LD B,2 ; COUNTER
67 11 0000* 00132 LD DE,KEYIN ; ASCII LOCN
00133 ;
6A CD 0126* 00134 L21: CALL HEXIT ; CONVERT TO HEX
6B CB 7F 00135 BIT 7,A ; ERROR?
6F 20 37 00136 JR NZ,SYN ; IF SO JUMP
00137 ;
71 ED 6F 00138 RLD ; STORE IT
73 13 00139 INC DE ; NEXT ASCII
00140 ;
74 CD 0126* 00141 CALL HEXIT ; CONVERT IT
77 CB 7F 00142 BIT 7,A ; ERROR?
79 20 2D 00143 JR NZ,SYN ; IF SO JUMP
00144 ;
7B ED 6F 00145 RLD ; STORE IT
7D 13 00146 INC DE ; NEXT ASCII
7E 2B 00147 DEC HL
7F 10 E7 00148 DJNZ L21
00149 ;
81 1A 00150 LD A,(DE)
82 FE 0B 00151 CP CR ; <CR> ?
84 C2 00A8* 00152 JP NZ,SYN
00153 ;
00154 ; MOVE CURSOR
87 21 0000* 00155 LD HL,CURSES
8A 23 00156 INC HL ; CHANGE
8B 3E 05 00157 LD A,CURSH ;
8D 77 00158 LD (HL),A ; CURSOR
8E 23 00159 INC HL ;
8F 23 00160 INC HL ; ADDRESS
90 3E 52 00161 LD A,CURSL
92 77 00162 LD (HL),A
00163 ;
93 E1 00164 POP HL
94 D1 00165 POP DE
95 C1 00166 POP BC
96 F1 00167 POP AF
97 C9 00168 RET

```



```

00169 ;
00170 ; SYNTAX ERROR HANDLER
00987 DD 21 00171 SYN1: LD IX, DECIDE+4 ; RETURN PAST PUSHES
00988 015D
00989 21 00E47 00172 LD HL, MESS2
00990 11 FDB2 00173 LD DE, COMLOC
00A27 FD 21 00174 LD IY, COMLOC
00A47 FDB2
00A67 15 0E 00175 JR L23
00176 ;
00A87 DD 21 00177 SYN: LD IX, L22 ; RETURN ADDRESS
00AA7 00627
00AC7 21 00E47 00178 LD HL, MESS2
00AF7 11 FBFF 00179 LD DE, QRESP
00B27 FD 21 00180 LD IY, QRESP
00E47 FBFF
00B67 01 0019 L23: LD BC, 19
00B97 ED 30 00182 LDIR ; WRITE MESSAGE
00183 ;
00BB7 21 0000* 00184 LD HL, CURSES
00BE7 23 00185 INC HL
00186 ;
00EF7 7E 00187 LD A, (HL) ; CHECK FOR SPECIAL
00C07 FE 00 00188 CP 00 ; CASE -- <COR> FOR
00C27 20 03 00189 JR NZ, L24 ; START ADDRESS --
00C47 3E 01 00190 LD A, 01
00C67 77 00191 LD (HL), A
00192 ;
00C77 23 00193 L24: INC HL ; THE LINKER DOES
00C87 23 00194 INC HL ; NOT ALLOW ARITH-
00C97 3A 01257 00195 LD A, (CURERR) ; METICS ON EXTERNALS
00CC 77 00196 LD (HL), A
00CD7 CD 0000* 00197 CALL CURSOR ; CURSOR
00198 ;
00DD7 DD E5 00199 PUSH IX ; PUT IT ON STACK
00200 ;
00DE7 FD E5 00201 PUSH IY
00DF7 E1 00202 POP HL
00E07 F5 00203 PUSH AF ; WE'RE SKIPPING
00E17 05 00204 PUSH BC ; THE FORMAL
00E27 05 00205 PUSH DE ; SUBROUTINE
00E37 05 00206 PUSH HL ; ENTRY POINT
00E47 DD E5 00207 PUSH IX
00E57 DD 21 00208 LD IX, KEYIN ; RESET POINTER
00E67 0000+
00E77 03 0000* 00209 JP N31 ; JUMP TO SUBR
00210 ; IN CICO
00211 ;
00E27 00212 STRT. DEFS 2
00E47 33 39 4E 00213 MESS2: DEFB 'SYNTAX ERROR RE-ENTER
00E77 34 41 58
00EA7 20 45 52
00ED7 52 4F 52
00F07 20 52 45
00F37 2D 45 4E
00F67 34 45 52

```

```

30 20 20 20
31 20 20
32 34 20 38 00214 ZIF: DEFB 0Z-30 INTERPRETER PROGRAM
33 30 20 49
34 4E 34 45
35 32 30 32
36 43 34 45
37 32 20 30
38 32 47 47
39 32 41 4D
40 33 34 41 00215 DEFB *START ADDRESS?
41 32 34 20
42 41 44 44
43 32 45 33
44 33 3F 20
5 13 00216 CURERR: DEFB 13
00217 ;
00218 ; HEXIT TAKES THE ASCII CONTENTS OF DE AND CONVERTS
00219 ; IT TO HEX. THE HEX VALUE IS STORED IN THE A-REG.
00220 ; IF A NON-NUMERIC ENTRY IS DETECTED, THE A-REG
00221 ; RETURNS OFF.
00222 ;
37 1A 00223 HEXIT: LD A,(DE) ; ASCII
38 FE 30 00224 CP ASC0 ; <0?
39 FA 0143 00225 JP M,ERR ; IF 30 ERROR
40 FE 3A 00226 CP ASC9F ; < 9?
41 F2 0134 00227 JP P,N21 ; IF 30 JUMP
00228 ;
41 E6 0F 00229 AND OF ; CONVERT TO HEX
42 09 00230 RET
00231 ;
44 FE 41 00232 N21: CP ASCA ; <A?
45 FA 0143 00233 JP M,ERR ; IF 30 ERROR
46 FE 47 00234 CP ASCFF ; <FF?
47 F2 0143 00235 JP P,ERR ; IF NOT ERROR
00236 ;
48 E6 0F 00237 AND OF ; CONVERT TO HEX
49 04 09 00238 ADD A,9
4A 09 00239 RET
00240 ;
4B 3E FF 00241 ERR: LD A,OFF ; ERROR CODE
4C 09 00242 RET
00243 ;
00244 ; INIT INITIALIZES THE STORAGE AREAS PRESAV,
00245 ; REGSAV, AND MDISP WITH ZEROS. ALSO THE USER
00246 ; STACK LOCATION IS SET.
00247 ;
46 01 0032 00248 INIT: LD BC,32 ; LENGTH
47 11 0000+ 00249 LD DE,PRESAV ; DESTINATION
48 21 0176 00250 LI: LD HL,ZERO ; SOURCE
49 ED A0 00251 LDI
50 2B 00252 DEC HL
51 EA 0140 00253 JP PE,L1 ; IF NOT DONE LOOP
00254 ;
55 21 07FF 00255 LD HL,USP ; USER STACK LOCATION
56 22 0000+ 00256 LD (REGSP),HL

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015E 21 0000 00257 LD HL, 00
015E 22 0000+ 00258 LD (MDISP), HL ; MEMORY DISP LOC'N
00259 ;
0161 01 0050 00260 LD BC, 50 ; LENGTH
0164 11 0000+ 00261 LD DE, SAVIT ; DESTINATION
0167 21 0175 00262 LZ LD HL, ZERO ; SOURCE
016A ED A0 00263 LDI
016C 2B 00264 DEC HL
016D EA 0167 00265 JP PE, LZ ; IF NOT DONE LOOP
00266 ;
0170 3E 18 00267 LD A, 18 ; INIT CURERR
0172 32 0125 00268 LD (CURERR), A
0175 C9 00269 RET
00270 ;
0176 00 00271 ZERO: DEFB 00
00272 ;
00273 ; COPY COPIES REGSAV INTO PRESAV
00274 ;
0177 05 00275 COPY: PUSH BC
0178 05 00276 PUSH DE
0179 05 00277 PUSH HL
017A 01 0012 00278 LD BC, 12
017D 11 0000+ 00279 LD DE, PRESAV
0180 21 0000+ 00280 LD HL, REGSAV
0183 ED E0 00281 LDIR
0185 E1 00282 POP HL
0186 01 00283 POP DE
0187 01 00284 POP BC
0188 C9 00285 RET
00286 ;
00287 ; DECIDE IS THE COMMAND INTERPRETER. IT'S INPUT
00288 ; IS TAKEN FROM KEYIN. THE CHARACTERS IN KEYIN ARE
00289 ; PARSED TO DETERMINE THE ACTION TO BE TAKEN.
00290 ; THIS ROUTINE IS STRUCTURED AS A BINARY DECISION
00291 ; TREE.
00292 ;
0189 F3 00293 DECIDE: PUSH AF
018A 05 00294 PUSH BC
018B 05 00295 PUSH DE
018C 05 00296 PUSH HL
00297 ;
018D 21 0000+ 00298 LD HL, KEYIN ; START OF BUFFER
018E 7E 00299 LD A, (HL)
0191 FE 4F 00300 CP 4F ; ASCII 0
0193 FA 01E1 00301 JP M, DQ ; < 0
0194 20 1A 00302 JR NZ, SQ ; > 0
00303 ;
0198 23 00304 INC HL ; NEXT CHARACTER
0199 7E 00305 LD A, (HL)
019A FE 4C 00306 CP 4C ; ASCII L
019C FA 01AA 00307 JP M, OFQ ; < 0L
019F CA 0A22 00308 JP Z, OL ; = 0L
00309 ;
01A2 FE 4E 00310 CP 4E ; ASCII N (ON?)
01A4 CA 09B7 00311 JP Z, ON ; = ON
01A7 03 0098 00312 JP SYN1 ; ERROR

```

00313 ;					
00314 0F0:	FE 46	CP	46	; ASCII F (0F?)	
00315	CA 0A04	JP	Z, 0F	; = 0F	
00316	CB 009E	JP	SYN1	; ERROR	
00317 ;					
00318 80:	FE 58	CP	58	; ASCII 8	
00319	FA 01D2	JP	M, 80	; < 8	
00320	Z0 0A	JR	NZ, 80	; > 8	
00321 ;					
00322	Z3	INC	HL	; NEXT CHARACTER	
00323	7E	LD	A, (HL)		
00324	FE 45	CP	45	; ASCII E (8E?)	
00325	CA 0208	JP	Z, 8E	; = 8E	
00326	CB 0435	JP	TRG	; ERROR OR TRIGGER	
00327 ;					
00328 90:	FE 55	CP	55	; ASCII U	
00329	CB 009B	JP	NZ, SYN1	; ^= U ERROR	
00330	Z3	INC	HL	; NEXT CHARACTER	
00331	7E	LD	A, (HL)		
00332	FE 41	CP	41	; ASCII A	
00333	CA 098C	JP	Z, UA	; = UA	
00334	CB 009B	JP	SYN1	; ERROR	
00335 ;					
00336 90:	FE 51	CP	51	; ASCII Q	
00337	C2 0435	JP	NZ, TRG	; ^= Q ERROR OR TRIGGER	
00338	Z3	INC	HL	; NEXT CHARACTER	
00339	7E	LD	A, (HL)		
00340	FE 55	CP	55	; ASCII U (0U?)	
00341	CA 0200	JP	Z, 0U	; = 0U	
00342	CB 009B	JP	SYN1	; ERROR	
00343 ;					
00344 90:	FE 44	CP	44	; ASCII D	
00345	FA 0213	JP	M, 80	; < D	
00346	Z0 0A	JR	NZ, 80	; > D	
00347 ;					
00348	Z3	INC	HL	; NEXT CHARACTER	
00349	7E	LD	A, (HL)		
00350	FE 49	CP	49	; ASCII I (DI?)	
00351	CA 0222	JP	Z, DI	; = DI	
00352	CB 0435	JP	TRG	; TRIGGER OR ERROR	
00353 ;					
00354 90:	FE 4E	CP	4E	; ASCII N	
00355	FA 0204	JP	M, 80	; < N	
00356	C2 009B	JP	NZ, SYN1	; > N ERROR	
00357 ;					
00358	Z3	INC	HL	; NEXT CHARACTER	
00359	7E	LD	A, (HL)		
00360	FE 45	CP	45	; ASCII E (NE?)	
00361	CA 0A10	JP	Z, NEE	; = NE	
00362	CB 009B	JP	SYN1	; ERROR	
00363 ;					
00364 90:	FE 47	CP	47	; ASCII G	
00365	C2 0435	JP	NZ, TRG	; TRIGGER OR ERROR	
00366	Z3	INC	HL	; NEXT CHARACTER	
00367	7E	LD	A, (HL)	; NEXT CHARACTER	
00368	FE 4F	CP	4F	; ASCII O (0O?)	

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0200 CA 028F 00369 JP Z, GO ; = GO
0210 CB 0098 00370 JP SYN1 ; ERROR
00371 ;
0211 FE 41 00372 ADL CP 41 ; ASCII A
0215 C2 0435 00373 JP NZ, TRG ; TRIGGER OR ERROR
0218 28 00374 INC HL ; NEXT CHARACTER
0219 7E 00375 LD A, (HL)
021A FE 35 00376 CP 55 ; ASCII U (AU?)
021C CA 091F 00377 JP Z, AU ; = AU
021F CB 0435 00378 JP TRG ; TRIGGER OR ERROR
00379 ;
00380 ; DI HANDLES THE MEMORY DISPLAY INSTRUCTION.
00381 ; ONLY THE FIRST FOUR NUMERICS ARE USED FOR
00382 ; DETERMINING THE ADDRESS OF THE MEMORY TO BE
00383 ; DISPLAYED.
00384 ;
0222 01 0012 00385 DI: LD BC, 12
0225 3E 20 00386 LD A, 20 ; ASCII <SP>
0227 28 00387 INC HL ; NEXT CHARACTER
0228 ED B1 00388 CPIR ; SEARCH FOR BLANK
00389 ;
022A E2 0098 00390 JP PO, SYN1 ; BLANK NOT FOUND
022D 7E 00391 LD A, (HL) ; CHAR AFTER <SP>
022E FE 40 00392 CP 40 ; ASCII @
0230 C2 0098 00393 JP NZ, SYN1
0233 28 00394 INC HL ; SHOULD POINT TO HHHH
0234 11 0000+ 00395 LD DE, MDISP ; TARGET LOCATION
0237 EB 00396 EX DE, HL
0238 28 00397 INC HL
00398 ;
0239 CD 025B 00399 CALL HEX4
00400 ;
023C 1A 00401 LD A, (DE) ; NEXT CHARACTER
023D FE 0E 00402 CP CR ; IS IT <CR> ?
023F C2 0098 00403 JP NZ, SYN1
00404 ;
0242 CD 0000+ 00405 CALL RSTATE ; PUT UP RHS OF SCREEN
0243 CD 027F 00406 CALL BLNK
0248 CD 0000+ 00407 CALL CURSOR
024B CD 0000+ 00408 CALL REVID
024E CD 0000+ 00409 CALL REVMEM
0251 CD 0000+ 00410 CALL SAVE
0254 CB 0AB8 00411 JP DONE
00412 ;
00413 ; HEX4 TAKES THE FOUR ASCII BYTES POINTED BY DE.
00414 ; CONVERTS THEM TO HEX, AND STORES THEM IN THE
00415 ; TWO BYTES POINTED BY HL.
00416 ; HEX2 FUNCTIONS SIMILARLY FOR TWO ASCII BYTES.
00417 ;
0257 06 01 00418 HEX2: LD B, 1
0259 1B 02 00419 JR AGAIN
00420 ;
025B 06 02 00421 HEX4: LD B, 2
025D CD 0126 00422 AGAIN: CALL HEXIT ; CONVERT TO HEX
0260 CB 7F 00423 BIT 7, A
0262 28 05 00424 JR Z, H1 ; IF NO ERROR JUMP

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640 DD E1      00425      POP      IX          ) STRIP TOP OF STACK
650 CB 009B    00426      JP       SYN1
        00427 ;
660 ED 8F      00428 H1:    RLD
670 13        00429      INC      DE          ) NEXT ASCII
680 CD 012B    00430      CALL    HEXIT       ) CONVERT IT
690 CB 7F      00431      BIT     7,A
700 23 05      00432      JR      Z,HZ        ) IF NO ERROR JUMP
710 DD E1      00433      POP      IX          ) STRIP TOP OF STACK
720 CB 009B    00434      JP       SYN1
        00435 ;
730 ED 8F      00436 H2:    RLD
740 13        00437      INC      DE          ) NEXT ASCII
750 2B        00438      DEC     HL          ) NEXT STORAGE BYTE
760 10 DF      00439      DJNZ   AGAIN
770 09        00440      RET
        00441 ;
        00442 ) BLNK BLANKS OUT THE COMMAND LINE.
        00443 ;
7F0 01 0019    00444 BLNK:  LD      BC,19
800 11 FD82    00445      LD      DE,COMLOC
810 21 028E    00446 BLNK1: LD      HL,SPCE
820 ED A0      00447      LDI
830 EA 0285    00448      JP     PE,BLNK1
840 09        00449      RET
        00450 ;
850 20        00451 SPCE:  DEFB   20
        00452 ;
        00453 ) GO CHANGES THE USER'S PC (REGPC)
        00454 ;
8F0 01 0012    00455 GO:   LD      BC,12
900 3E 20      00456      LD      A,20        ) ASCII <SP>
910 13        00457      INC     HL          ) NEXT CHARACTER
920 ED E1      00458      CPIR   ) SEARCH FOR BLANK
        00459 ;
930 E2 009B    00460      JP     PC,SYN1     ) BLANK NOT FOUND
940 11 0000*   00461      LD      DE,REGPC   ) TARGET LOCATION
950 EB        00462      EX     DE,HL
960 23        00463      INC     HL          ) HIGH BYTE REGPC
        00464 ;
9F0 CD 025B    00465      CALL   HEX4
        00466 ;
A20 1A        00467      LD      A,(DE)     ) NEXT CHARACTER
A30 FE 0D      00468      CP     CR          ) IS IT <CR> ?
A40 C2 009B    00469      JP     NZ,SYN1
        00470 ;
A80 CD 0000*   00471      CALL   FINTOP
AB0 CD 0000*   00472      CALL   WIPE
AE0 CD 0000*   00473      CALL   TEXTUP
B10 CD 0000*   00474      CALL   RSTATF
B40 CD 0000*   00475      CALL   CURSOR
B70 CD 0000*   00476      CALL   REVID
BA0 CD 0000*   00477      CALL   REVMEM
BD0 CB 0AB8    00478      JP     DONE
        00479 ;
        00480 ;

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0200 3E 12 00481 00. LD A,VIDOFF
0202 3D 0000+ 00482 CALL BANKSW ; SWITCH OUT V-RAM
0205 33 0111 00483 JP SYST ; END PROGRAM
00484
00485
00486 ; SE HANDLES THE SET INSTRUCTION (WHICH CHANGES
00487 ; MEMORY OR REGISTER VALUES). THE MAIN BODY OF
00488 ; THIS ROUTINE IS A BINARY DECISION TREE WHICH
00489 ; DETERMINES WHICH REGISTER TO SET AND STORES
00490 ; THAT INFO IN HL.
00491
0210 31 1011 00492 SE. LD BC,12
0213 3E 20 00493 LD A,20 ; ASCII 0000
0214 31 01 00494 INC HL ; NEXT CHARACTER
0215 31 01 00495 CP1R
00496
0217 3E 0000 00497 JP 00,SYN1 ; BLANK, NOT FOUND
0218 3E 0000 00498 LD A,0000 ; FIRST CHARACTER
0219 3E 00 00499 CP 40 ; ASCII 0
0220 34 0100 00500 JP M,SEB ; < 0
0221 31 007E 00501 JP NZ,SEL ; > 0
00502
022C 3E 00 00503 INC HL ; NEXT CHARACTER
022D 7E 00 00504 LD A,(HL)
022E 3E 30 00505 CP 30 ; ASCII =
022F 32 0090+ 00506 JP NZ,SYN1 ; != ERROR
00507
0228 3B 00 00508 EX DE,HL
0229 13 00 00509 INC DE ; NEXT CHARACTER
022A 21 0000+ 00510 LD HL,REGC ; DESTINATION
022B 03 040A+ 00511 JP REG1
00512
022E 3E 42 00513 SEB. CP 42 ; ASCII B
022F 3A 0300+ 00514 JP M,SEAT ; < B
02F0 20 5B 00515 JR NZ,SEC ; > B
00516
02F2 33 00 00517 INC HL ; NEXT CHARACTER
02F3 7E 00 00518 LD A,(HL)
02F4 3E 30 00519 CP 30 ; ASCII =
02F6 20 08 00520 JR NZ,SEBC
00521
02F8 3B 00 00522 EX DE,HL
02F9 13 00 00523 INC DE ; NEXT CHARACTER
02FA 21 0000+ 00524 LD HL,REGB ; DESTINATION
02FB 03 040A+ 00525 JP REG1
00526
0300 3E 43 00527 SEBC. CP 43 ; ASCII C
0302 32 0090+ 00528 JP NZ,SYN1
00529
0305 3B 00 00530 EX DE,HL
0306 13 00 00531 INC DE
0307 21 0000+ 00532 LD HL,REGB
030A 03 0410+ 00533 JP REG2
00534
030D 3E 40 00535 SEAT. CP 40 ; ASCII @
030F 3A 0090+ 00536 JP M,SYN1 ; < @

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012' 20 2A      00537      JR      NZ, BEA      ; > @ (A)
                00538 ;
                00539 ; SPECIAL CASE (SEE MEMORY)
                00540 ;
014' EB      00541      EX      DE, HL
015' 1B      00542      INC     DE
016' 21 033D'  00543      LD      HL, ATMEM-1 ; NEXT CHARACTER
019' 0B 025B'  00544      CALL   HEX4         ; STORAGE
010' EB      00545      EX      DE, HL
                00546 ;
01D' 7E      00547      LD      A, (HL)     ; NEXT CHARACTER
01E' FE 3D      00548      CP      3D         ; ASCII =
020' 02 0098'  00549      JP      NZ, SYN1
                00550 ;
023' EB      00551      EX      DE, HL
024' 1B      00552      INC     DE
025' 21 033D'  00553      LD      HL, WITH    ; NEXT CHARACTER
028' 0D 0257'  00554      CALL   HEX2         ; STORAGE
                00555 ;
02E' 2A 033B'  00556      LD      HL, (ATMEM) ; LOCATION
02E' 3A 033D'  00557      LD      A, (WITH)   ; VALUE
031' 77      00558      LD      (HL), A
                00559 ;
032' 1A      00560      LD      A, (DE)     ; NEXT CHARACTER
033' FE 0D      00561      CP      0D         ; <CR> ?
035' 02 0098'  00562      JP      NZ, SYN1
038' 03 041A'  00563      JP      SEDONE
                00564 ;
03E'      00565 ATMEM:  DEFS  2
03E'      00566 WITH:  DEFS  1
                00567 ;
                00568 ; RESUME TREE
                00569 ;
03E' 23      00570 BEA:   INC     HL
03F' 7E      00571      LD      A, (HL)
040' FE 3D      00572      CP      3D         ; ASCII =
042' 02 0098'  00573      JP      NZ, SYN1
                00574 ;
045' EB      00575      EX      DE, HL
046' 1B      00576      INC     DE
047' 21 0000+  00577      LD      HL, REGA   ; NEXT CHARACTER
04A' 03 040A'  00578      JP      REG1       ; DESTINATION
                00579 ;
04D' FE 43      00580 SEC:   CP      43         ; ASCII C
04F' 20 0F      00581      JR      NZ, SED
                00582 ;
051' 23      00583      INC     HL
052' 7E      00584      LD      A, (HL)
053' FE 3D      00585      CP      3D         ; ASCII =
055' 02 0098'  00586      JP      NZ, SYN1
                00587 ;
058' EB      00588      EX      DE, HL
059' 1B      00589      INC     DE
05A' 21 0000+  00590      LD      HL, REGC   ; NEXT CHARACTER
05D' 03 040A'  00591      JP      REG1       ; DESTINATION
                00592 ;

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0360	23	00593	SEDE	INC	HL	NEXT CHARACTER
0361	7E	00594		LD	A, (HL)	
0362	FE 3D	00595		CP	3D	ASCII =
0364	FA 0098	00596		JP	M, SYN1	< =
0367	20 03	00597		JR	NZ, SEDE	
		00598				
0369	EB	00599		EX	DE, HL	NEXT CHARACTER
036A	13	00600		INC	DE	DESTINATION
036B	21 0000*	00601		LD	HL, REGD	
036E	03 040A	00602		JP	REG1	
		00603				
0371	FE 45	00604	SEDE	CP	45	ASCII E
0373	02 0098	00605		JP	NZ, SYN1	
		00606				
0376	EB	00607		EX	DE, HL	
0377	13	00608		INC	DE	
0378	21 0000*	00609		LD	HL, REGD	
037B	03 0410	00610		JP	REG2	
		00611				
037E	FE 4C	00612	SEL	CP	4C	ASCII L
0380	FA 03C0	00613		JP	M, SEH	< L
0383	20 0F	00614		JR	NZ, SEP	> P
		00615				
0385	23	00616		INC	HL	NEXT CHARACTER
0386	7E	00617		LD	A, (HL)	
0387	FE 3D	00618		CP	3D	ASCII =
0389	02 0098	00619		JP	NZ, SYN1	
		00620				
038C	EB	00621		EX	DE, HL	
038D	13	00622		INC	DE	DESTINATION
038E	21 0000*	00623		LD	HL, REGD	
0391	03 040A	00624		JP	REG1	
		00625				
0394	FE 50	00626	SEP	CP	50	ASCII P
0395	FA 0098	00627		JP	M, SYN1	< P
0399	20 10	00628		JR	NZ, SES	> P
		00629				
039B	23	00630		INC	HL	NEXT CHARACTER
039C	7E	00631		LD	A, (HL)	
039D	FE 43	00632		CP	43	
039F	02 0098	00633		JP	NZ, SYN1	
		00634				
03A2	EB	00635		EX	DE, HL	NEXT CHARACTER
03A3	13	00636		INC	DE	DESTINATION
03A4	21 0000*	00637		LD	HL, REGPC	
03A7	23	00638		INC	HL	
03A8	03 0410	00639		JP	REG2	
		00640				
03AB	FE 53	00641	SES	CP	53	ASCII S
03AD	02 0098	00642		JP	NZ, SYN1	
		00643				
03B0	23	00644		INC	HL	NEXT CHARACTER
03B1	7E	00645		LD	A, (HL)	
03B2	FE 50	00646		CP	50	ASCII P
03B4	02 0098	00647		JP	NZ, SYN1	
		00648				

3B7	EB		00649	EX	DE, HL	
3B8	13		00650	INC	DE	; NEXT CHARACTER
3B9	21	0000+	00651	LD	HL, REG2P	
3BC	23		00652	INC	HL	
3BD	03	0410	00653	JP	REG2	
			00654			
3C0	FE	48	00655	CP	48	; ASCII H
3C2	FA	0098	00656	JP	M, SYN1	
3C5	20	18	00657	JR	NZ, SEI	
			00658			
3C7	23		00659	INC	HL	; NEXT CHARACTER
3C8	7E		00660	LD	A, (HL)	
3C9	FE	3D	00661	CP	3D	; ASCII =
3CB	FA	0098	00662	JP	M, SYN1	
3CE	20	08	00663	JR	NZ, SEHL	
			00664			
3D0	EB		00665	EX	DE, HL	
3D1	13		00666	INC	DE	; NEXT CHARACTER
3D2	21	0000+	00667	LD	HL, REGH	; DESTINATION
3D5	03	040A	00668	JP	REG1	
			00669			
3D8	FE	4C	00670	CP	4C	; ASCII L
3DA	02	0098	00671	JP	NZ, SYN1	
			00672			
3DD	EB		00673	EX	DE, HL	
3DE	13		00674	INC	DE	; NEXT CHARACTER
3DF	21	0000+	00675	LD	HL, REGH	; DESTINATION
3E1	03	0410	00676	JP	REG2	
			00677			
3E3	FE	49	00678	CP	49	; ASCII I
3E7	02	0098	00679	JP	NZ, SYN1	
			00680			
3EA	23		00681	INC	HL	
3EB	7E		00682	LD	A, (HL)	; NEXT CHARACTER
3EC	FE	58	00683	CP	58	; ASCII X
3EE	FA	0098	00684	JP	M, SYN1	
3F1	20	09	00685	JR	NZ, SEIY	
			00686			
3F3	EB		00687	EX	DE, HL	
3F4	13		00688	INC	DE	; NEXT CHARACTER
3F5	21	0000+	00689	LD	HL, REGIX	
3F8	23		00690	INC	HL	; DESTINATION
3F9	03	0410	00691	JP	REG2	
			00692			
3FC	FE	59	00693	CP	59	; ASCII Y
3FE	02	0098	00694	JP	NZ, SYN1	
			00695			
0401	EB		00696	EX	DE, HL	
0402	13		00697	INC	DE	; NEXT CHARACTER
0403	21	0000+	00698	LD	HL, REGIY	
0406	23		00699	INC	HL	; DESTINATION
0407	03	0410	00700	JP	REG2	
			00701			
			00702			; THE REGISTERS ARE CHANGED HERE.
040A	0D	0257	00703	REG1: CALL	HEX2	
040D	03	041A	00704	JP	SECDNE	

```

00705
0410 19 00706 REGE.  LD  A,(DE) ; NEXT CHARACTER
0411 FE 3D 00707 CP  ED ; ASCII =
0412 12 00708 JP  NZ,SYN1
00709
0413 1E 00710 INC  DE
0417 12 00711 CALL HEX4
00712
041A 19 00713 REGE.  LD  A,(DE) ; NEXT CHARACTER
041B FE 0D 00714 CP  CR ; CR=?
041D 02 00715 JP  NZ,SYN1
00716
0420 0D 0000+ 00717 CALL  RETATE ; PUT UP RHS OF SCREEN
0423 0D 027F 00718 CALL  BLNK
0424 0D 0000+ 00719 CALL  CURSOR
0425 0D 0000+ 00720 CALL  REVID
0426 0D 0000+ 00721 CALL  REVMEM
042F 0D 0000+ 00722 CALL  SAVE
0432 03 0A5B 00723 JP  DONE
00724
00725
00726 ; TRG HANDLES THE SIMULATION OF PROGRAM EXECUTION
00727 ; BY INTERPRETING THE INPUT STRING AND CALLING
00728 ; SIM THE APPROPRIATE NUMBER OF TIMES.
00729
043E 0D 0A5E 00730 TRG:  CALL  SAVOLD ; SAVE RHS OF SCREEN
043F 11 0946 00731 LD  DE,TPAD
043E 21 094A 00732 LD  HL,ZEROA
043E 31 0004 00733 LD  BC,4
0441 03 00 00734 TR:  LDI  ; ASCII ZERO OUT TPAD
0443 2B 00735 DEC  HL
0444 03 0441 00736 JP  RE,T3
00737
0447 21 0000 00738 LD  HL,00
0448 22 0944 00739 LD  (STEPS),HL ; ZERO OUT STEPS
00740
044D 22 05F3 00741 LD  (BIT7),HL ; RESET CODE
0450 22 05F7 00742 LD  (BIT6),HL
0453 22 05FB 00743 LD  (BIT4),HL
0456 22 05FE 00744 LD  (BIT2),HL
0459 22 05FD 00745 LD  (BIT1),HL
045C 22 05FF 00746 LD  (BIT0),HL
045F 22 063F 00747 LD  (BIT7A),HL
0462 22 063E 00748 LD  (BIT6A),HL
0465 22 063D 00749 LD  (BIT4A),HL
0468 22 063F 00750 LD  (BIT2A),HL
046B 22 0641 00751 LD  (BIT1A),HL
046E 22 0643 00752 LD  (BIT0A),HL
0471 22 0660 00753 LD  (BIT7B),HL
0474 22 0662 00754 LD  (BIT6B),HL
0477 22 0664 00755 LD  (BIT4B),HL
047A 22 0666 00756 LD  (BIT2B),HL
047D 22 0668 00757 LD  (BIT1B),HL
0480 22 066A 00758 LD  (BIT0B),HL
00759
0483 21 0000+ 00760 LD  HL,KEYIN ; INPUT BUFFER

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186 7E      00761      LD      A,(HL)      ; FIRST CHARACTER
187 FE 3A    00762      CP      3A          ; ASCII ? -1
188 FE 0A8A  00763      JP      P,TRIG     ;
00764 ;
189 FE 00    00765      CP      CR          ; ASCII CR
18E 20 08    00766      LR      NZ,T4      ;
190 3E 01    00767      LD      A,C        ;
192 32 0944  00768      LD      (STEPS),A  ; ONE SIMULATION
195 03 04A6  00769      LF      TNUM       ;
00770 ;
198 FE 30    00771 T4:    CP      30          ; ASCII 0
19A FA 0098  00772      LF      M,SYN1     ;
19D 11 0000* 00773      LD      DE,KEYIN   ;
00774 ;
1A0 CD 094B  00775      CALL   UNIFORM     ;
1A3 CD 015B  00776      CALL   HEX4        ;
00777 ;
1A6 ED 4B    00778 TNUM:   LD      EC,(STEPS) ; LOAD COUNTERS
1A8 0944 ;
1AA 04      00779      INC    B           ;
1AB CD 0000* 00780      CALL   SIMUL       ; RUN SIMULATION
1AE CD      00781      DEC    C           ; INNER COUNTER
1AF 37 F4    00782      JR     NZ,T2       ;
1B1 3A 0944  00783      LD      A,(STEPS)  ; RESET INNER
1B4 4F      00784      LD      C,A        ;
1B5 10 F4    00785      DJNZ   T2         ; DEC OUTER
1B7 03 0926  00786      JP     TDONE       ;
00787 ;
1BA 3E 01    00788 TREG:   LD      A,0        ;
1BC 32 091E  00789      LD      (LOOP),A   ; ZERO LOOP
1BE 32 0919  00790      LD      (CINFO),A  ; ZERO CINFO
00791 ;
1C2 3A 091E  00792 TREG:   LD      A,(LOOP)   ;
1C5 FE 01    00793      CP      1          ;
1C7 20 3E    00794      JR     NZ,T5       ; JUMP IF FIRST TIME
00795 ;
00796 ; THIS SECTION DETERMINE THE HEX VALUE OF AN ASCII
00797 ; NUMERIC INPUT STRING FOR THE RHS OF A TRIGGER
00798 ; CONDITION STATEMENT. NUMERIC ARE NOT VALID FOR
00799 ; THE LHS OF A TRIGGER CONDITION (WITH RELATION).
00800 ;
40F 7E      00801      LD      A,(HL)     ; NEXT CHARACTER
40A FE 3A    00802      CP      3A         ; ASCII ? -1
40C FE 0A8E  00803      JP      P,CKIT     ; CHECK FOR A-F HEX
40F FE 30    00804      CP      30         ;
4B1 FA 0098  00805      JP      M,SYN1     ; TOO LOW FOR COMMAND
00806 ;
4B4 EB      00807      PUSH   HL          ;
4B5 EB E1    00808      POP    IX          ; MOVE VALUE TO INDEXED
00809 ; REGISTER
4D7 EB 7E 02  00810 NUM:   LD      A,(IX-2)   ; 3RD CHARACTER
4DA 11 0920  00811      LD      DE,VAL     ; PUT VALUE IN VAL
4DB EB      00812      EX     DE,HL       ; (USED IN HEX2,4)
4DE FE 30    00813      CP      30         ; ASCII 0
4E0 FA 04E0  00814      JP      M,TNUM2    ; SHOULD BE TWO DIGITS
00815 ;

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00816 / ASSUME 4 DIGITS IN NUMBER. WE WILL GET THE NUMBER
00817 / AND STORE IT IN RHE.
00818 /
00819 / POINT TO HIGH BYTE
00819 11 00819 INC HL
00820 00 00820 CALL HEX4
00821 00 00821 EX DE,HL / POINT HL TO NEXT CHAR
00822 11 00822 INC DE / POINT DE TO VALUE
00823 11 00823 JP TREG16
00824 /
00825 / TWO DIGIT NUMBER
00825 00 00825 THUM1 CALL HEX2
00826 00 00826 EX DE,HL / POINT HL TO NEXT CHAR
00827 11 00827 INC DE / POINT DE TO VALUE
00828 11 00828 JP TREG8
00829 /
00830 / DECISION TREE
00831 /
00832 11 00832 LD A,(HL)
00833 00 00833 CP 46 / ASCII F
00834 00 00834 JP M,TB / < F
00835 11 00835 JP NZ,TL / > F
00836 /
00837 11 00837 INC HL
00838 11 00838 LD A,(HL) / NEXT CHARACTER
00839 00 00839 CP 3D / ASCII =
00840 00 00840 JP NZ,SYN1
00841 /
00842 / THIS SECTION HANDLES THE F=BBBBBB INSTRUCTION.
00843 /
00844 11 00844 LD A,(LOOP)
00845 00 00845 CP 0
00846 00 00846 JP NZ,SYN1
00847 /
00848 00 00848 LD B,0
00849 11 00849 INC HL / NEXT CHARACTER
00850 11 00850 LD A,(HL) / BIT 7
00851 00 00851 CP 58 / ASCII X
00852 10 11 00852 JR NZ,T5.3
00853 /
00854 00 11 00854 LD IX,0BFCB / MODIFY PROGRAM
00855 00 11 00855 LD (BIT7),IX
00856 00 11 00856 LD (BIT7A),IX
00857 00 11 00857 LD (BIT7B),IX
00858 10 0E 00858 JR T6
00859 00 30 00859 CP T5.5 / ASCII 0
00860 10 07 00860 JR Z,T6
00861 /
00862 00 31 00862 CP 31 / ASCII !
00863 00 00863 JP NZ,SYN1
00864 10 7E 00864 SET 7,B
00865 /
00866 10 00 00866 INC HL / NEXT CHARACTER
00867 11 00867 LD A,(HL) / BIT 6

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0868	FE	53	00868	CP	58		
0869	DE	11	00869	JR	NZ, T6, 5		
			00870				
0871	DE	11	00871	LD	IX, 0B70E		
0872	870E						
0873	DE	11	00873	LD	(BIT6), IX		
0874	0B70E						
0875	DE	11	00875	LD	(BIT4A), IX		
0876	0B70E						
0877	DE	11	00877	LD	(BIT6B), IX		
0878	0B70E						
0879	DE	0E	00879	JR	T7		
0880	FE	50	00880	CP	30	T6, 5,	
0881	DE	07	00881	JR	Z, T7		
			00882				
0883	FE	51	00883	CP	31		
0884	DE	00FE	00884	JP	NZ, SYN1		
0885	DE	50	00885	SET	6, B		
			00886				
0887	DE		00887	T7:	INC	HL	, NEXT CHARACTER
0888	7E		00888	LD	A, (HL)		, BIT 4
0889	FE	53	00889	CP	58		
0890	DE	11	00890	JR	NZ, T7, 5		
			00891				
0892	DE	11	00892	LD	IX, 0A70E		
0893	870E						
0894	DE	11	00894	LD	(BIT4), IX		
0895	0B70E						
0896	DE	11	00896	LD	(BIT4A), IX		
0897	0B70E						
0898	DE	11	00898	LD	(BIT4B), IX		
0899	0B70E						
0900	DE	0E	00900	JR	T8		
0901	FE	50	00901	CP	30	T7, 5,	
0902	DE	07	00902	JR	Z, T8		
			00903				
0904	FE	51	00904	CP	31		
0905	DE	00FE	00905	JP	NZ, SYN1		
0906	DE	50	00906	SET	4, B		
			00907				
0908	DE		00908	T8:	INC	HL	, NEXT CHARACTER
0909	7E		00909	LD	A, (HL)		, BIT 2
0910	FE	53	00910	CP	58		
0911	DE	11	00911	JR	NZ, T8, 5		
			00912				
0913	DE	11	00913	LD	IX, 0970E		
0914	870E						
0915	DE	11	00915	LD	(BIT2), IX		
0916	0B70E						
0917	DE	11	00917	LD	(BIT2A), IX		
0918	0B70E						
0919	DE	11	00919	LD	(BIT2B), IX		
0920	0B70E						
0921	DE	0E	00921	JR	T9		
0922	FE	50	00922	CP	30	T8, 5,	
0923	DE	07	00923	JR	Z, T9		

		00912			
08A1	FE 31	00913	CP	31	
08A2	01 009B	00914	JP	NZ, SYN1	
08A3	0B 00	00915	SET	0, B	
		00916			
08A4	16	00917	INC	HL	; NEXT CHARACTER
08A5	7E	00918	LD	A, (HL)	; BIT 1
08A6	FE 58	00919	CP	58	
08A7	16 11	00920	JR	NZ, TR. 3	
		00921			
08A8	0B 11	00921	LD	IX, 08FCB	
08A9	87FB				
08AA	1D 11	00923	LD	(BIT1), IX	
08AB	0BFB				
08AC	0B 11	00924	LD	(BIT1A), IX	
08AD	0A41				
08AE	0B 11	00925	LD	(BIT1E), IX	
08AF	0A4B				
08B0	1B 0B	00926	JR	T10	
08B1	FE 30	00927	CP	30	
08B4	1B 07	00928	JR	Z, T10	
		00929			
08B6	FE 31	00930	CP	31	
08B7	01 009B	00931	JP	NZ, SYN1	
08B8	0B 0B	00932	SET	0, B	
		00933			
08B9	16	00934	INC	HL	; NEXT CHARACTER
08BA	7E	00935	LD	A, (HL)	; BIT 0
08BB	FE 58	00936	CP	58	
08BC	16 11	00937	JR	NZ, T10. 3	
		00938			
08BD	0B 11	00939	LD	IX, 087CB	
08BE	87CB				
08BF	0B 11	00940	LD	(BIT0), IX	
08C0	0BFF				
08C1	0B 11	00941	LD	(BIT0A), IX	
08C2	0A4B				
08C3	0B 11	00942	LD	(BIT0B), IX	
08C4	0A2A				
08C5	1B 0B	00943	JR	T11	
08C6	FE 30	00944	CP	30	
08C7	1B 07	00945	JR	Z, T11	
		00946			
08C8	FE 31	00947	CP	31	
08C9	01 009B	00948	JP	NZ, SYN1	
08CA	0B 0B	00949	SET	0, B	
		00950			
08CB	7B	00951	LD	A, B	
08CC	11 009F	00952	LD	(BPAT), A	; STORE BIT PATTERN
08CD	16	00953	INC	HL	
08CE	7E	00954	LD	A, (HL)	
08CF	FE 0D	00955	CP	CR	; IS IT <CR> ?
08D0	01 0097	00956	JP	NZ, T13	
		00957			
08E5	1D 0000*	00958	CALL	SIMUL	; RUN SIMULATION
08E6	1A 0000*	00959	LD	A, (REGF)	

804	02	AF	00960	RES	B, B	
805	02	AF	00961	RES	B, B	
806	00		00962 BIT7:	NOP		
807	00		00963	NOP		
808	00		00964 BIT6:	NOP		
809	00		00965	NOP		
810	00		00966 BIT4:	NOP		
811	00		00967	NOP		
812	00		00968 BIT2:	NOP		
813	00		00969	NOP		
814	00		00970 BIT1:	NOP		
815	00		00971	NOP		
816	00		00972 BIT0:	NOP		
800	00		00973	NOP		
801	55		00974	CP	B	/ CHECK FLAG
802	10	E7	00975	JR	NZ, T12	
803	03	0025	00976	JF	TDONE	
			00977			
			00978			
807	3E	10	00979 T13:	LD	A, 20	/ ASCII 13FD
808	50		00980	LD	D, B	/ BIT PATTERN
809	01	000A	00981	LD	BC, 0A	
810	5E		00982	CP	(HL)	/ CHECK FOR 13FD
811	03	0093	00983	JF	NZ, SYN1	
			00984			
812	5E	A1	00985 T14:	CFI		
813	5E	0093	00986	JF	PO, SYN1	/ NO NUMBERS FOUND
814	0A	0611	00987	JF	Z, T14	
			00988			
819	2E		00989	DEC	HL	/ FOUND SOMETHING
81A	3E	3A	00990	LD	A, 3A	/ ASCII :
81B	5E		00991	CP	(HL)	
81C	03	0093	00992	JF	NZ, SYN1	
			00993			
81D	1E		00994	INC	HL	/ NEXT CHARACTER
81E	5E		00995	PUSH	HL	
81F	01		00996	POP	DE	
			00997			
823	0D	094B	00998	CALL	UNFORM	
824	0D	025B	00999	CALL	HEX4	
			01000			
81A	3A	091F	01001	LD	A, (BFAT)	/ GET BIT PATTERN
82C	57		01002	LD	D, A	
82D	ED	4B	01003	LD	BC, (STEPS)	/ LOAD COUNTERS
82F	0944					
831	04		01004	INC	B	
			01005			
832	3A	0000*	01006 T17:	LD	A, (REGF)	/ UNTIL F=BBBBBB
833	0E	AF	01007	RES	B, A	/ RUN SIMULATION
837	0E	AF	01008	RES	B, A	
839	00		01009 BIT7A:	NOP		
83A	00		01010	NOP		
83B	00		01011 BIT6A:	NOP		
83C	00		01012	NOP		
83D	00		01013 BIT4A:	NOP		
83E	00		01014	NOP		



0617	00		01015	BIT1A:	NCF		
0618	00		01016		NCF		
0619	00		01017	BIT1A:	NCF		
0620	00		01018		NCF		
0621	00		01019	BIT0A:	NCF		
0622	00		01020		NCF		
0623	0A		01021		CF	D	; CHECK FLAG
0624	1E	05	01022		JR	Z, T17, 5	
			01023				
0625	0E	0000*	01024		CALL	SIMUL	; RUN SIMULATION
0626	1E	05	01025		JR	T17	
			01026				
0627	0E		01027	T17, 5:	DEC	C	; INNER COUNTER
0628	20	0A	01028		JR	NZ, T18	
0629	1A	0A44	01029		LD	A, (STEPS)	; RESET INNER COUNTER
0630	4F		01030		LD	C, A	
0631	10	0E	01031		DJNZ	T18	; OUTER COUNTER
0632	13	0A2E	01032		JP	TDONE	
			01033				
0634	3A	0000*	01034	T18:	LD	A, (REGP)	; UNTIL FF=EEEEEE
0635	0E	AF	01035		RES	3, A	; RUN SIMULATION
0636	0E	AF	01036		RES	3, A	
0637	00		01037	BIT7B:	NCF		
0638	00		01038		NCF		
0639	00		01039	BIT6B:	NCF		
0640	00		01040		NCF		
0641	00		01041	BIT4B:	NCF		
0642	00		01042		NCF		
0643	00		01043	BIT2B:	NCF		
0644	00		01044		NCF		
0645	00		01045	BIT1B:	NCF		
0646	00		01046		NCF		
0647	00		01047	BIT0B:	NCF		
0648	00		01048		NCF		
0649	0A		01049		CF	D	; CHECK FLAG
0650	10	0E	01050		JR	NZ, T17	
			01051				
0652	0E	0000*	01052		CALL	SIMUL	; RUN SIMULATION
0653	1E	05	01053		JR	T18	
			01054				
			01055				; DECISION TREE (RESUMED).
			01056				
0674	FE	42	01057	TE:	CF	42	; ASCII E
0675	FA	068A	01058		JP	M, TAT	; < E
0676	10	1E	01059		JR	NZ, TD	; > E
			01060				
0678	1E		01061		INC	HL	; NEXT CHARACTER
0679	7E		01062		LD	A, (HL)	
0680	FE	42	01063		CF	42	; ASCII C
0681	11	0000*	01064		LD	DE, REG8	; 8 BIT LOCATION
0682	11	074A	01065		JP	NZ, TREG8	; 8 BIT COMP. (B=, C=)
			01066				
0685	23		01067		INC	HL	; NEXT CHARACTER
0686	1E		01068		DEC	DE	; POINT TO BC
0687	0E	0740	01069		JP	TREG16	; 16 BIT CP. (B=, C=)
			01070				

868A	FE	40	01071	TAT:	CP	40		ASCII @
868C	FA	008B	01072		JP	M, SYN1		
868F	1B	07	01073		JR	Z, TAT1		
			01074					
8691	1B		01075		INC	HL		NEXT CHAR (A=,C,...)
8693	11	0000+	01076		LD	DE, REGA		3 BIT LOCATION
8695	03	074A	01077		JP	TREG8		
			01078					
8698	1B		01079	TAT1:	INC	HL		NEXT CHAR
869A	11	091E	01080		LD	DE, REGMEM-1		TARGET
869C	1E		01081		EX	DE, HL		
869E	0B	025B	01082		CALL	HEX4		GET INPUT ADDRESS
86A0	1E		01083		EX	DE, HL		HL POINTS NEXT CHAR
86A2	1E	5B	01084		LD	DE, (REGMEM)		DE POINTS 3 BIT LOC'N
86A4	0A	0917						
86A6	03	073A	01085		JP	TRMEM		
			01086					
86A8	FE	44	01087	TD:	CP	44		ASCII D
86AA	FA	04BE	01088		JP	M, TC		MUST BE 0
86AD	20	16	01089		JR	NZ, TE		MUST BE 2
			01090					
86AF	1B		01091		INC	HL		NEXT CHAR
86B0	FE		01092		LD	A, (HL)		
86B2	FE	45	01093		CP	45		ASCII E
86B4	11	0000+	01094		LD	DE, REGD		3 BIT LOCATION
86B6	11	074A	01095		JP	NZ, TREG8		3 BIT CP. (D=,C,...)
			01096					
86B9	1B		01097		INC	HL		NEXT CHAR
86BB	1B		01098		DEC	DE		POINT TO DE
86BD	03	07A0	01099		JP	TREG16		16 BIT CP. (DE=,C,...)
			01100					
86BF	1B		01101	TC:	INC	HL		NEXT CHAR (C=,C,...)
86C1	11	0000+	01102		LD	DE, REGC		
86C3	03	074A	01103		JP	TREG8		3 BIT
			01104					
86C5	1B		01105	TE:	INC	HL		NEXT CHAR (E=,C,...)
86C7	11	0000+	01106		LD	DE, REGE		
86C9	03	074A	01107		JP	TREG8		
			01108					
86CB	FE	4C	01109	TL:	CP	4C		ASCII L
86CD	FA	06DA	01110		JP	M, TH		< L
86CF	20	3E	01111		JR	NZ, T3		> L
			01112					
86D1	1B		01113		INC	HL		NEXT CHAR (L=,C,...)
86D3	11	0000+	01114		LD	DE, REGL		
86D5	03	074A	01115		JP	TREG8		
			01116					
86D8	FE	48	01117	TH:	CP	48		ASCII H
86DA	FA	0098	01118		JP	M, SYN1		< H
86DC	20	0F	01119		JR	NZ, T1		> H
			01120					
86DE	1B		01121		INC	HL		NEXT CHAR
86E0	7E		01122		LD	A, (HL)		
86E2	FE	4C	01123		CP	4C		ASCII L
86E4	11	0000+	01124		LD	DE, REGH		3 BIT LOC'N
86E6	03	074A	01125		JP	NZ, TREG8		

Address	Op	Opnd	Label	Comment	Op	Opnd	Comment
06EB	LD		01126		INC	HL	NEXT CHAR (HL=>C...)
06ED	LD		01127		DEC	DE	POINT TO HL
06EE	LD		01128		JP	TREG18	
06EE	LD	07A0	01129				
06FF	LD		01130				ASCII I
0700	FE	48	01131	TI	CP	48	
0701	LD	0098	01132		JP	NZ, SYN1	NEXT CHAR
0702	LD		01133		INC	HL	
0703	FE		01134		LD	A, (HL)	ASCII X
0704	FE	78	01135		CP	58	
0705	FE	78	01136		JP	M, SYN1	
0706	FE	0098	01137		JP	NZ, TIY	
0707	LD	07	01138				NEXT CHAR (IX=>C...)
0708	LD		01139		INC	HL	
0709	LD	0000*	01140		LD	DE, REGIX	
070A	LD	07A0	01141		JP	TREG16	
070B	FE	58	01142				ASCII Y
070C	FE	58	01143	TIY	CP	58	
070D	LD	0098	01144		JP	NZ, SYN1	NEXT CHAR (IY=>C...)
070E	LD		01145		INC	HL	
070F	LD	0000*	01146		LD	DE, REGIY	
0710	LD	07A0	01147		JP	TREG18	
0711	FE	58	01148		CP	58	ASCII B
0712	FE	0727	01149	TS	JP	M, TPC	< B
0713	LD	0098	01150		JP	NZ, SYN1	> B
0714	LD		01151				NEXT CHAR
0715	LD		01152		INC	HL	
0716	FE		01153		LD	A, (HL)	ASCII F
0717	FE	70	01154		CP	50	
0718	LD	0098	01155		JP	NZ, SYN1	
0719	LD		01156				NEXT CHAR (BP=>C...)
071A	LD		01157		INC	HL	
071B	LD	0000*	01158		LD	DE, REGBP	
071C	LD	07A0	01159		JP	TREG16	
071D	LD		01160				ASCII F
071E	FE	50	01161	TPC	CP	50	
071F	LD	0098	01162		JP	NZ, SYN1	NEXT CHAR
0720	LD		01163		INC	HL	
0721	LD		01164		LD	A, (HL)	ASCII C
0722	FE		01165		CP	43	
0723	FE	43	01166		JP	NZ, SYN1	
0724	LD	0098	01167				NEXT CHAR (PC=>C...)
0725	LD		01168		INC	HL	
0726	LD	0000*	01169		LD	DE, REGPC	
0727	LD	07A0	01170		JP	TREG16	
0728	LD		01171				
0729	LD		01172				
072A	LD		01173				FOR (MEMD+CRELD)CRHS+MEMD ASSUME 8 BIT COMPARE
072B	LD	091E*	01174	TRMEM:	LD	A, (LOOP)	FIRST TIME
072C	LD	00	01175		CP	0	THRU LOOP?
072D	LD	08	01176		JP	Z, TREG8	IF 80 JUMP
072E	LD		01177				
072F	LD		01178				CHECK LENGTH BIT
0730	LD	0919*	01179		LD	A, (CINFO)	
0731	LD	7F	01180		BIT	7, A	
0732	LD	02	01181		JP	Z, TREG8	

0748	18	38	01182	JR	TREG18		
			01183				
0749	1A	091E	01184	TREG8	LD	A, (LOOP)	FIRST TIME
074B	FE	00	01185	CP	0		THRU LOOP ?
074F	20	41	01186	JR	NZ, TB. 1		IF NOT JUMP
			01187				
0751	3E	01	01188	TR18A	LD	A, 1	
0753	32	091E	01189	LD	(LOOP), A		SET LOOP FLAG
0756	3A	091F	01190	LD	A, (CINFO)		GET INFO BYTE
0758	47		01191	LD	B, A		PUT IT IN B
075A	ED	3E	01192	LD	(LHS), DE		STORE LHS OF COMPARE
075C	091C						
			01193				
075E	7E		01194	LD	A, (HL)		RELATIONAL CHAR
075F	FE	7E	01195	CP	7E		ASCII ?
0761	20	04	01196	JR	NZ, TB. 2		
			01197				
0763	0E	ED	01198	SET	4, B		SET NOT FLAG
0765	23		01199	INC	HL		NEXT CHAR
0768	7E		01200	LD	A, (HL)		
			01201				
076F	FE	3D	01202	TB. 2	CP	3D	ASCII =
076P	20	0A	01203	JR	NZ, TB. 3		
076B	0E	00	01204	SET	0, B		SET = FLAG
076D	78		01205	LD	A, B		SAVE IT
076E	32	091F	01206	LD	(CINFO), A		
0771	23		01207	INC	HL		NEXT CHAR
0772	0B	04C2	01208	JP	TRPT		CHECK RHS
			01209				
0775	FE	3E	01210	TB. 3	CP	3E	ASCII >
0777	20	0A	01211	JR	NZ, TB. 4		
077F	0E	08	01212	SET	1, B		SET > FLAG
077E	78		01213	LD	A, B		
077C	32	091F	01214	LD	(CINFO), A		SAVE IT
077F	23		01215	INC	HL		NEXT CHAR
0780	0B	04C2	01216	JP	TRPT		CHECK RHS
			01217				
0783	FE	3C	01218	TB. 4	CP	3C	ASCII <
0783	C1	0098	01219	JP	NZ, SYN1		
0788	0E	00	01220	SET	2, B		SET < FLAG
078A	78		01221	LD	A, B		
078E	32	091F	01222	LD	(CINFO), A		SAVE IT
078E	23		01223	INC	HL		NEXT CHAR
078F	0B	04C2	01224	JP	TRPT		
			01225				
0791	3A	091F	01226	TB. 1	LD	A, (CINFO)	CHECK LENGTH FLAG
0793	0E	7F	01227	BIT	7, A		
0797	C1	0098	01228	JP	NZ, SYN1		JUMP IF 16 (11)
			01229				
079A	ED	3E	01230	LD	(RHS), DE		RHS OF COMPARE
079C	7812						
079E	18	11	01231	JR	FINEND		
			01232				
07A0	3A	091E	01233	TREG18	LD	A, (LOOP)	FIRST TIME
07A3	FE	00	01234	CP	0		THRU LOOP ?
07A5	20	4E	01235	JR	NZ, TB. 1		IF NOT JUMP

0770	10 00	01238	LD	A, (CINFD)	GET INFO BYTE
0771	12 00	01239	SET	Z, A	SET 16 BIT FLAG
0772	12 00	0123A	LD	(CINFD), A	SAVE IT
0773	13 0700	0123B	JP	TRIGR	
		0123C			
0774	10 00	01241	LD	A, (CINFD)	CHECK LENGTH FLAG
0775	12 00	01242	BIT	Z, A	
0776	01 0700	01243	JP	Z, SYN1	JUMP IF 8 BIT (100)
		01244			
0777	0E 00	01245	LD	(RHS), DE	RHS OF COMPARE
0778	0010	01246			
0779	03 0700	01247	JP	FINEND	
		01248			
		01249			FINEND FINDS THE END OF THE INPUT LINE (CR) OR-
		01250			(###CR) AND LOADS THE APPROPRIATE VALUE IN
		01251			THE LOOP COUNTER.
		01252			
0781	0E	01253	FINEND: LD	A, (HL)	BETTER BE (SPD), (CR)
0782	0E 00	01254	CP	CR	ASCII CR
0783	10 00	01255	JR	NZ, F1	
		01256			
0786	0E 01	01257	LD	A, 1	ONCE THRU
0787	02 0944	01258	LD	(STEPS), A	
0788	03 0700	01259	JP	DOIT	
		01260			
078E	0E 20	01261	F1: CP	20	ASCII (SPD)
078F	02 0098	01262	JP	NZ, SYN1	
		01263			
0793	0E 20	01264	LD	A, 20	
0794	06 00	01265	LD	B, 3	MAX 3 SPACES
0797	13	01266	F2: INC	HL	NEXT CHAR
0798	0E	01267	CP	(HL)	(SPD) ?
0799	10 01	01268	JR	NZ, F3	IF NOT JUMP
079E	10 0A	01269	DJNZ	F2	
		01270			
079D	0E	01271	F3: LD	A, (HL)	
079E	0E 3A	01272	CP	3A	ASCII :
079F	02 0098	01273	JP	NZ, SYN1	
		01274			
07E3	13	01275	INC	HL	NEXT CHAR
07E4	0E	01276	PUSH	HL	
07E5	01	01277	POP	DE	SAVE START LOC IN
		01278			
07E6	0D 094B	01279	CALL	UNFORM	
07E9	0D 025B	01280	CALL	HEX4	# IN STEPS (IN HEX)
		01281			
07EC	0D 5B	01282	DOIT: LD	DE, (LHS)	POINT TO LHS VALUE
07ED	0910				
07F0	2A 091A	01283	LD	HL, (RHS)	POINT TO RHS VALUE
07F3	3A 0919	01284	LD	A, (CINFD)	GET INFO BYTE
07F6	0E 67	01285	BIT	4, A	CHECK > FLAG
07F8	10 4D	01286	JR	NZ, DNOT	JUMP IF < (100)
		01287			
07FA	0E 47	01288	BIT	0, A	CHECK = FLAG
07FC	13 13	01289	JR	Z, DGL	JUMP IF > OR <

07FE	3E	CA	01290		LD	A, OCA	
0800	3E	089F	01291		LD	(JP1), A	CC=ZERO (=)
0801	3E	08DC	01292		LD	(JP1A), A	PUT IT IN JP1
0802	3E	02	01293		LD	A, OC2	
0803	3E	08E5	01294		LD	(JP2), A	CC=NON-ZERO (=)
0804	3E	08F3	01295		LD	(JP2A), A	
0805	03	0891	01296		JP	DCONT	
			01297				
0811	3A	091F	01298	DGL	LD	A, (CINFC)	GET INFO BYTE
0812	0B	4F	01299		BIT	1, A	CHECK C FLAG
0813	2B	14	01300		JR	Z, DL	JUMP (C)
0814	0B		01301		EX	DE, HL	SWITCH RHB&LHS
0815	3E	FA	01302		LD	A, OFA	CC=MINUS (C)
0816	3E	089F	01303		LD	(JP1), A	
0817	3E	08DC	01304		LD	(JP1A), A	
0818	3E	F2	01305		LD	A, OF2	CC=PLUS (C)
0819	3E	08E5	01306		LD	(JP2), A	
0820	3E	08F3	01307		LD	(JP2A), A	
0821	03	0891	01308		JP	DCONT	
			01309				
082C	3A	091F	01310	DL	LD	A, (CINFC)	GET INFO BYTE
082D	0B	57	01311		BIT	Z, A	CHECK C FLAG
082E	0A	0098	01312		JP	Z, SYN1	NOT C.C. OR =
082F	3E	FA	01313		LD	A, OFA	CC=MINUS (C)
0830	3E	089F	01314		LD	(JP1), A	
0831	3E	08DC	01315		LD	(JP1A), A	
0832	3E	F2	01316		LD	A, OF2	CC=PLUS (C)
0833	3E	08E5	01317		LD	(JP2), A	
0834	3E	08F3	01318		LD	(JP2A), A	
0835	03	0891	01319		JP	DCONT	
			01320				
			01321				
			01322				
0847	0B	47	01323	DNCT	BIT	0, A	CHECK = FLAG
0848	1B	18	01324		JR	Z, INGL	JUMP IF D OR C
0849	1B	02	01325		LD	A, OC2	CC=NON-ZERO (=)
084A	3E	089F	01326		LD	(JP1), A	PUT IT IN JP1
084B	3E	08DC	01327		LD	(JP1A), A	
084C	3E	CA	01328		LD	A, OCA	CC=ZERO (=)
084D	3E	08E5	01329		LD	(JP2), A	
084E	3E	08F3	01330		LD	(JP2A), A	
084F	03	0891	01331		JP	DCONT	
			01332				
0857	3A	091F	01333	DNGL	LD	A, (CINFC)	GET INFO BYTE
0858	0B	4F	01334		BIT	1, A	CHECK C FLAG
0859	2B	14	01335		JR	Z, DNL	JUMP (C)
085A	0B		01336		EX	DE, HL	SWITCH RHB&LHS
085B	3E	F2	01337		LD	A, OF2	CC=PLUS (C)
085C	3E	089F	01338		LD	(JP1), A	
085D	3E	08DC	01339		LD	(JP1A), A	
085E	3E	FA	01340		LD	A, OFA	CC=MINUS (C)
085F	3E	08E5	01341		LD	(JP2), A	
0860	3E	08F3	01342		LD	(JP2A), A	
0861	03	0891	01343		JP	DCONT	
			01344				
0877	3A	091F	01345	DNL	LD	A, (CINFC)	GET INFO BYTE

0870	0B	F7	01346	BIT	Z, A	) CHECK C FLAG
0872	0A	00FB	01347	LP	Z, SYN1	) NOT (C, D), CR =
0874	0E	F2	01348	LD	A, OF2	) CC=PLUS (C)
0876	01	0BFF	01349	LD	(JP1), A	
0878	01	1B01	01350	LD	(JP1A), A	
087A	0E	FA	01351	LD	A, OFA	) CC=MINUS (C)
087C	02	0BEB	01352	LD	(JP2), A	
087E	02	0BFB	01353	LD	(JP2A), A	
			01354			
0881	ED	4B	01355	DOUNT	BC, (STEPS)	) LOAD LOOP COUNTERS
0883		0944				
0885	04		01356	INC	B	
0886	3A	0919	01357	LD	A, (CINFO)	) GET INFO BYTE
0888	0B	7F	01358	BIT	7, A	
088A	20	20	01359	JR	NZ, D016	) JUMP IF 16 BIT
			01360			
088D	1A		01361	D1:	LD	A, (DE)
088E	BE		01362	CP	(HL)	) COMPARE VALUES
088F	02	08A7	01363	JP1:	JP	NZ, D2
			01364			) JUMP IF CORRECT
			01364			) NZ IS MODIFIABLE
08A1	0D	0000*	01365	CALL	SIMUL	) RUN SIMUL IF NOT
08A5	1E	F6	01366	JR	D1	) CHECK AGAIN
			01367			
08A7	0D		01368	D2:	DEC	C
08A8	20	09	01369	JR	NZ, D3	) INNER COUNTER
08AA	3A	0944	01370	LD	A, (STEPS)	) RESET INNER COUNTER
08AD	4F		01371	LD	C, A	
08AE	10	03	01372	DUNZ	D3	) OUTER COUNTER
			01373			
08B0	0B	0926	01374	JP	TDONE	
			01375			
08B3	1A		01376	D3:	LD	A, (DE)
08B4	BE		01377	CP	(HL)	) COMPARE VALUES
08B5	0A	08FD	01378	JP2:	JP	Z, D1
			01379			) JUMP IF CORRECT
			01379			) Z IS MODIFIABLE
08B8	0D	0000*	01380	CALL	SIMUL	) RUN SIMUL IF NOT
08BB	1B	F6	01381	JR	D3	
			01382			
			01383			
08BD	ED	5B	01384	D016:	LD	(DEREG), DE
08BF		0922				) SAVE ADDRESS OF VALUE
08C1	22	0924	01385		LD	(HLREG), HL
			01386			) SAVE ADDR OF RHS VAL
08C4	3A	08DC	01387		LD	A, (JP1A)
08C7	0B	AF	01388	RES	5, A	) CHANGE MDL OR POND
08C9	32	08DC	01389		LD	(JP1A), A
			01390			
08CC	3A	08FB	01391		LD	A, (JP2A)
08CF	0B	AF	01392	RES	5, A	) CHANGE MDL OR POND
08D1	32	08FB	01393		LD	(JP2A), A
			01394			
08D4	0D	0900	01395	D1A:	CALL	GETEM
			01396			) LOAD DE, HL WITH (DE
			01396			) AND (HL) RESPECTIVELY
08D7	E5		01397	PUSH	HL	) SAVE IT
08D8	BF		01398	CP	A	) RESET CARRY FLAG
08D9	ED	52	01399	SBC	HL, DE	) SET FLAGS

08DE	E1	01400	POP	HL		
08E0	02 08E4	01401	JP	NZ, D2A		/ GET IT BACK
		01402				/ JUMP IF CORRECT
		01403				/ NZ IS SELF-MODIFIABLE
08E4	05 0000*	01404	CALL	SIMUL		
08E8	13 F0	01405	JR	D1A		/ RUN SIMUL IF NOT
		01406				/ CHECK AGAIN
08E4	0E	01407	DEC	C		
08E8	10 08	01408	JR	NZ, D3A		/ INNER COUNTER
08E7	14 0944	01409	LD	A, (STEP8)		
08EA	4F	01410	LD	C, A		/ RESET INNER COUNTER
08EE	10 03	01411	DNJZ	D3A		
08EF	01 0926	01412	JP	TDONE		/ OUTER COUNTER
		01413				
08F0	0D 0900*	01414	CALL	GETEM		
		01415				/ LOAD DE, HL WITH (DE)
08F3	EE	01416	PUSH	HL		/ AND (HL) RESPECTIVELY
08F4	BF	01417	CP	A		/ SAVE IT
08F7	ED E2	01418	SBC	HL, DE		/ RESET CARRY FLAG
08F7	E1	01419	POP	HL		/ SET FLAGS
08F8	0A 08D4	01420	JP	Z, D1A		
		01421				/ JUMP IF CORRECT
		01422				/ Z IS SELF MODIFIABLE
08F8	0D 0000*	01423	CALL	SIMUL		
08FE	13 F0	01424	JR	D3A		/ RUN SIMUL IF NOT
		01425				/ CHECK AGAIN
		01426				
		01427				/ GETEM LOADS DE WITH (DE), AND HL WITH (HL).
		01428				
0907	ED EE	01429	GETEM: LD	DE, (DEREG)		/ POINT TO LHS VALUE
0917	0922					
0947	EA 0924*	01430	LD	HL, (HLREG)		/ POINT TO RHS VALUE
		01431				
097	DE	01432	PUSH	DE		/ SAVE IT
098	DE	01433	LD	E, (HL)		/ GET LOW HALF OF (HL)
099	E1	01434	INC	HL		/ POINT TO HIGH 1/2
09A	E6	01435	LD	D, (HL)		/ GET HIGH HALF OF (HL)
		01436				
09B	ED E1	01437	POP	IX		/ GET OLD DE
09D	ED 0E 00	01438	LD	L, (IX)		/ GET LOW HALF
09E	ED E3	01439	INC	IX		/ POINT TO HIGH 1/2
09F	ED 0E 00	01440	LD	H, (IX)		/ GET HIGH HALF
		01441				
09F	09	01442	RET			
		01443				
09A7		01444	FLG:	DEFS	1	
09A7		01445	REGMEM:	DEFS	2	
09A7		01446	CINFO:	DEFS	1	
09A7		01447	RHS:	DEFS	2	
09A7		01448	LHS:	DEFS	2	
09A7		01449	LOOP:	DEFS	1	
09A7		01450	SPAT:	DEFS	1	
09A7		01451	VAL:	DEFS	2	
09A7		01452	DEREG:	DEFS	2	
09A7		01453	HLREG:	DEFS	1	
09A7		01454				



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0926* CB 0000* 01455 TDONE. CALL FINTOP
0927* CB 0000* 01456 CALL WIFE
0928* CB 0000* 01457 CALL TEXTOP
0929* CB 0000* 01458 CALL RSTATE
0930* CB 0000* 01459 CALL CURSOR
0931* CB 0000* 01460 CALL REVID
0932* CB 0000* 01461 CALL REVMEM
0933* CB 0000* 01462 CALL SAVE
0934* CB 0177* 01463 CALL COPY
0941* CB 0AEE* 01464 JP DONE
01465 ;
0944* 01466 STEPS: DEFB 2
0946* 01467 TPAD: DEFB 4
094A* 30 01468 ZEROA: DEFB 30
01469 ;
01470 ; UNFORM
01471 ; DETERMINE HEX VALUE FROM UNFORMATTED ASCII.
01472 ;
094E* 08 05 01473 UNFORM: LD B,3
094D* 3E 0D 01474 LD A,CR ; ASCII -CORO
094F* 23 01475 T15. INC HL
0950* 10 05 01476 DJNZ T15. ; SYNTAX CHECK
0952* 0D E1 01477 POP IX ; PEEL TOP OF STACK
0954* 03 0099* 01478 JP SYN1
0957* EE 01479 T16: CP (HL) ; SEARCH FOR -CORO
0958* 20 F5 01480 JR NZ,T15
01481 ;
095A* EE 01482 PUSH HL
095B* EF 01483 CP A ; CLEAR CARRY
095C* ED 52 01484 SBC HL,DE ; L = # OF #'S
095E* 06 00 01485 LD B,0
0960* 4D 01486 LD C,L ; BC IS LOOP COUNTER
0961* E1 01487 POP HL
0962* 2E 01488 DEC HL ; POINTS TO LAST #
0963* 11 0949* 01489 LD DE,TPAD-3 ; DESTINATION
0966* ED E8 01490 LDDR ; PUT # IN TPAD
01491 ;
0968* 21 0945* 01492 LD HL,STEPS+1
096E* 11 0946* 01493 LD DE,TPAD
097E* CF 01494 RET
01495 ;
01496 ;
01497 ; AU SWAPS THE AF REGISTER WITH AF. NO RECORD IS
01498 ; KEPT TO INDICATE WHICH OF THE VALUES WAS THE SUCCESSOR
01499 ; OF THE ORIGINAL PRIMARY AF REGISTER.
097F* 1A 0000* 01500 AU. LD HL,(XRAF) ; AF
0972* ED 5B 01501 LD DE,(REGAF) ; AF
0974* 0000* 01502 LD (XRAF),DE ; EXCHANGE THEM
0976* ED 5B
0978* 0000* 01503 LD (REGAF),HL
097A* 22 0000* 01504 ;
097E* CB 0000* 01505 CALL RSTATE
0980* CB 0000* 01506 CALL REVID
0983* CB 027F* 01507 CALL BLNK
0986* CB 0000* 01508 CALL CURSOR

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0839 CB 0A8B 01509 JP DONE
01510
01511
01512 CA SWAPS THE REMAINING GENERAL REGISTERS (BC, DE, HL)
01513 WITH THEIR ALTERNATES. AGAIN NO RECORD IS KEPT OF THE
01514 ORIGINAL PRIMARY REGISTER SET.
083C 11 0000+ 01515 DA LD DE, TEMP ; TEMPORARY STORAGE
083F 21 0000+ 01516 LD HL, XREG ; SOURCE
0841 01 0005 01517 LD BC, 5
0845 ED 80 01518 LDIR
01519
0847 13 01520 INC DE ; SKIP OVER XRAF
0848 13 01521 INC DE
0849 21 01522 INC HL ; SKIP OVER REGAF
084A 13 01523 INC HL
01524
084E 0E 06 01525 LD C, 6
084F ED 80 01526 LDIR
01527
084F 21 0000+ 01528 LD HL, TEMP ; SOURCE
0842 13 01529 INC DE ; SKIP OVER REGAF
0843 13 01530 INC DE
0844 0E 06 01531 LD C, 6
0846 ED 80 01532 LDIR
01533
084E CD 0000+ 01534 CALL RSTATE
084E CD 0000+ 01535 CALL REVID
084E CD 027F 01536 CALL BLNK
0841 CD 0000+ 01537 CALL CURSOR
0844 CB 0A8B 01538 JP DONE
01539
01540
01541 ON FILLS THE SCREEN WITH AN EXPANDED VERSION OF
01542 THE MEMORY DISPLAY AREA. TWENTY FOUR ROWS OF
01543 SIXTEEN BYTES EACH ARE DISPLAYED. BEGINNING WITH
01544 THE ADDRESS STORED IN MDISP.
0874 CD 0000+ 01545 ON: CALL WIPE
08A7 FD 21 01546 LD IY, 0F800 ; TOP LEFT OF V-RAM
08C0 F800
08E7 2A 0000+ 01547 LD HL, (MDISP) ; GET MEMORY ADDRESS
01548 LD C, 10 ; OUTER LOOP COUNTER
01549
0837 7C 01550 ON3: LD A, H ; WRITE MEM-ADDR TO VRAM
0847 CD 0000+ 01551 CALL ACONV
0877 FD 72 00 01552 LD (IY), D
08A7 FD 73 01 01553 LD (IY+1), E
08D7 7D 01554 LD A, L
08E7 CD 0000+ 01555 CALL ACONV
0817 FD 72 02 01556 LD (IY+2), D
0847 FD 73 03 01557 LD (IY+3), E
0877 FE 3A 01558 LD A, 0
08A7 FD 77 04 01559 LD (IY+4), A ; END WRITE MEM-ADDR
01560
08C7 06 10 01561 LD B, 10 ; LOAD INNER LOOP COUNT
08E7 11 0005 01562 LD DE, 5

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09E1 FD 19 01563 ADD IY, DE ; UPDATE IY LOOPN
01564
09E3 FD 28 01565 INC IY ; SKIP A SPACE
09E4 FD 28 01566 LD A, (HL) ; WRITE MEMORY CONTENTS
09E5 FD 28 01567 INC HL ; TO V-RAM
09E6 CD 0000+ 01568 CALL ACCNV
09E7 FD 72 00 01569 LD (IY), D
09E8 FD 28 01570 INC IY
09E9 FD 73 00 01571 LD (IY), E
09EA FD 28 01572 INC IY ; END WRITE MEM-CONTENT
09EB FD 28 01573 DJNZ ON4 ; RETURN FOR NEXT MEMOR
01574
09ED CD 00 01575 DEC C ; DEC OUTER LOOP COUNTN
09EE FD 11 001B 01576 LD DE, 1B
09EF FD 19 01577 ADD IY, DE ; SKIP TO NEXT LINE
09F0 FD 20 03 01578 JR NZ, ON3 ; RETURN TO WRITE NEXT
01579 ; LINE
09F2 CD 0000+ 01580 CALL CURSOR
09F3 CD 03 0AB8 01581 JP DONE
01582
01583
01584 ; OF RESTORES THE SCREEN TO ITS NORMAL CONFIGURATION
01585 ; WHICH REFLECTS THE CURRENT STATE OF THE PROGRAM.
09F4 CD 0000+ 01586 OF: CALL WIPE
09F5 CD 0000+ 01587 CALL FINTOP
09F6 CD 0000+ 01588 CALL TEXTUP
09F7 CD 03 0A10 01589 JP NEE ; CONT AT NEE
01590
01591 ; NEE RESTORES THE RIGHT HAND SIDE OF THE SCREEN TO
01592 ; ITS NORMAL CONFIGURATION AND CURRENT VALUES
09F8 CD 0000+ 01593 NEE: CALL RSTATE
09F9 CD 0000+ 01594 CALL CURSOR
09FA CD 0000+ 01595 CALL REVID
09FB CD 0000+ 01596 CALL REVMEM
09FC CD 027F 01597 CALL BLNK
09FD CD 03 0AB8 01598 JP DONE
01599
01600
01601 ; OL DISPLAYS THE RIGHT HAND SIDE OF THE SCREEN AS
01602 ; IT APPEARED IMMEDIATELY AFTER THE PRECEDING TRIGGER
01603 ; CONDITION WAS MET.
09FE FD 21 0ABD 01604 OL: LD HL, OLDSOR ; SAVE AREA
09FF FD 11 FB80 01605 LD DE, OF880 ; START OF 8-BIT REGS
0A00 FD 3E 04 01606 LD A, 4 ; WRITE 4 LINES
0A01 CD 0A4E 01607 CALL WRTE
01608
0A02 FD 11 FA10 01609 LD DE, OFA10 ; START OF 16-BIT REGS
0A03 FD 3E 02 01610 LD A, 2 ; WRITE 2 LINES
0A04 CD 0A4E 01611 CALL WRTE
01612
0A05 FD 11 FE00 01613 LD DE, OFB00 ; START OF STACK AREA
0A06 FD 3E 01 01614 LD A, 1 ; WRITE 1 LINE
0A07 CD 0A4E 01615 CALL WRTE
01616
0A08 FD 11 FBFD 01617 LD DE, OFBFD ; START OF MEMORY AREA
0A09 FD 3E 04 01618 LD A, 4 ; WRITE 4 LINES

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0442 00 044E 01619 CALL WRITE
0443 00 027F 01620 CALL BLNK
0444 00 0000 01621 CALL CURSOR
0445 03 04B8 01622 JP DONE
01623 ;
01624 ; WRITE WRITES THE DATA STORED IN OLDSOR ON THE SCREEN
044E 01 001D 01625 WRITE: LD BC, 1D ; INNER LOOP
0451 ED E0 01626 LDIR ; WRITE TO V-RAM
0452 3D 01627 DEC A ; DEC OUTER COUNTER
0454 01 0033 01628 LD BC, 33
0457 EB 01629 EX DE, HL ; SKIP TO NEXT LINE
0458 09 01630 ADD HL, BC
0459 EB 01631 EX DE, HL
045A 20 F2 01632 JR NZ, WRITE ; WRITE NEXT LINE
045C 09 01633 RET
01634 ;
01635 ;
01636 ; SAVOLD SAVES THE CURRENT RNS OF THE SCREEN
045D 11 04BD 01637 SAVOLD: LD DE, OLDSOR ; SAVE AREA
0460 21 FB80 01638 LD HL, OFB80 ; START OF 8-BIT REGS
0461 3E 04 01639 LD A, 4 ; WRITE 4 LINES
0463 00 0481 01640 CALL WRITE1
01641 ;
0463 21 FA10 01642 LD HL, OFA10 ; START OF 16-BIT REGS
0464 3E 02 01643 LD A, 2 ; WRITE 2 LINES
0465 00 0481 01644 CALL WRITE1
01645 ;
0470 21 FB00 01646 LD HL, OFB00 ; START OF STACK AREA
0471 3E 01 01647 LD A, 1 ; WRITE 1 LINE
0473 00 0481 01648 CALL WRITE1
01649 ;
0473 21 FBFO 01650 LD HL, OFBFO ; START OF MEMORY AREA
0475 3E 04 01651 LD A, 4 ; WRITE 4 LINES
0476 00 0481 01652 CALL WRITE1
0480 09 01653 RET
01654 ;
01655 ; WRITE1 WRITES THE DATA STORED ON THE SCREEN ON OLDSOR
0481 01 001D 01656 WRITE1: LD BC, 1D ; INNER LOOP
0484 EB E0 01657 LDIR ; WRITE TO OLDSOR
0486 3D 01658 DEC A ; DEC OUTER COUNTER
0487 01 0033 01659 LD BC, 33
0489 EB 01660 EX HL, BC ; SKIP TO NEXT LINE
048B 20 F4 01661 JR NZ, WRITE1 ; WRITE NEXT LINE
048D 09 01662 RET
01663 ;
01664 ; CKIT CHECKS TO DETERMINE IF THE RNS CHARACTER STRING
01665 ; IS A REGISTER OR A HEX NUMBER.
048E FE 41 01666 CKIT: CP 41
0490 FA 0098 01667 JP M, SYN1 ; < ASCII A
0491 FE 47 01668 CP 47
0493 F2 04F4 01669 JP P, T5 ; > ASCII F
01670 ;
0494 EB 01671 PUSH HL ; MOVE POINTER TO
0495 DD E1 01672 POP IX ; INDEX REGISTER
0496 BA 0919 01673 LD A, (CINFO) ; LENGTH OF COMPARE
0498 CB 7F 01674 BIT 7, A

```

0A90	28	0E	01675		JR	Z, 08	/ 5 BIT LENGTH
			01676				
0A92	00	7E	01677		LD	A, (IX+2)	/ GET THIRD CHARACTER
0A93	7E	30	01678		CP	30	
0A97	70	04F4	01679		JP	M, T5	/ TWO CHARACTERS LONG
0A9A	03	04E7	01680		JP	NUM	/ RETURN
			01681				
0A9D	00	7E	01682	03	LD	A, (IX-1)	/ GET 2ND CHARACTER
0A9E	7E	30	01683		CP	30	
0A9F	7A	04F4	01684		JP	M, T5	/ ONE CHARACTER LONG
0AA3	03	04E7	01685		JP	NUM	/ RETURN
			01686				
0AA3	81		01687	DONE	POP	HL	
0AA7	01		01688		POP	DE	
0AA8	01		01689		POP	BC	
0AAE	71		01690		POP	AF	
0AB0	0F		01691		RET		
			01692				
0ABD			01693	OLDSOR:	DEFS	13F	/ HOLDS OLD SCREEN (AHS)
			01694				
0BFC			01695		DEFS	4F	/ OUR STACK AREA
0C4E			01696	STKP:	DEFS	1	
			01697				
			01698		END		

ACADE.

AMBLE	0000	AGAIN	025D	AC	021B	ABCO	0080	ABOOP	003A
ANV	0041	ABOFP	0047	ATMEM	033B	AU	093F	BANNET	0004*
ARREW	0203*	BIT0	05FF	BIT0A	0643	BIT0B	063A	BIT1	05FD
ATIA	0641	BIT1B	0683	BIT2	05FB	BIT2A	063F	BIT1B	0688
ATA	05FF	BIT4A	063D	BIT4B	0684	BIT4	05FF	BIT4A	063B
ATSE	0682	BIT7	05FB	BIT7A	063F	BIT7B	0680	BLNF	027F
AWFL	0253	BRAT	091F	CB	0AAD	CICO	0080*	CINFO	091F
AWT	0A8E	COMLOC	F082	COPY	0177	CR	000D	CURERR	0123
AXEE	008C*	CURSH	0005	CURSL	0082	CURSOR	0A4F*	CURSYN	009B
	089D	DIA	08D4	D2	06A7	D2A	08E4	D3	08E3
A	08F0	DOONT	0891	DECIDE	0189	DEREG	0922	DGL	0811
	0222	DL	0820	DNGL	085E	DNL	0879	DNOT	0847
16	03ED	DOIT	07E0	DONE	0A85	DO	01E1	ERR	0143
	070E	F2	07D7	F3	07DD	FINEND	0701	FINTOP	0A08*
2	0918	GETEM	0900	GO	028F	GO	0204	H1	0289
	0273	HEX2	0257	HEX4	025B	HEXIT	0129	HLREG	0924
IT	0146	JP1	089F	JP1A	08DC	JP2	08E3	JP2A	08F8
YIN	049E*	KYBD	000E	L1	014C	L2	0187	LZ1	006A
1	0082	L23	00B6	L24	00C7	LHS	0910	LOOP	091E
IN	00001	MDISP	09BF*	ME882	00E4	N21	0134	N31	00E0*
3	0A10	NO	01F2	NUM	04D7	OF	0A04	OFQ	01AA
	0A22	OLDESCR	0ABD	ON	09B7	ON3	09C3	ON4	09E3
SENE	0013*	POLL	0014	PRESAV	017E*	O1	F3F0	OCURSH	0000
UREL	00FF	QQ	01D2	QRESP	F3FF	QU	0200	QUERY	0035
31	040A	REG2	0410	REGA	0693*	REGAF	097B*	REGB	0680*
3C	0600*	REGD	06B4*	REGG	06C7*	REGF	063A*	REGH	06E8*
31X	0700*	REGIY	070C*	REGL	06D5*	REGMEM	0917	REGFC	0735*
38AV	0131*	REGSP	0722*	REVID	0A17*	REVMEM	0A1A*	RHS	091A
T	002A	RSTATE	0A11*	SAVE	093C*	SAVIT	0163*	SAVOLD	0A3D
REEM	001E*	SE	02C8	SEA	033E	SEAT	030D	SEB	025B
3L	0300	SEC	034D	SED	0360	SEDE	0371	SEDONE	041A
4	0300	SEHL	03D9	SEI	03E3	SEIY	03FC	SEL	037E
5	0394	SES	03AB	SIMUL	03FC*	SPCE	028E	SO	01E2
3FB	0944	STKP	0C4B	STRT	00E2	SYN	00A81	SYN1	0098
BT	0123	T10	05BD	T10. 5	05D5	T11	05E0	T12	05EB
1	0607	T14	0611	T15	094F	T16	0957	T16. 1	07B2
2	0632	T17. 3	064D	T19	0659	T2	04AB	T3	0441
3	0498	T5	04F4	T3. 3	0526	T4	0531	T6. 3	0549
4	0534	T7. 3	056C	T3	0577	T3. 1	0792	T3. 2	0757
5	0773	T8. 4	0783	T3. 3	058F	TP	039A	TP. 3	05B2
6	068A	TAT1	0698	TE	0674	TC	04BE	TD	06A8
ONE	0928	TE	06C5	TEMP	09A0*	TEXTUP	0A0E*	TH	06DA
2	06FC	TITL	F86C	TIY	0705	TL	0600	TNUM	04A6
MI	04ED	TFAD	0946	TPC	0727	TR14A	0751	TRACK	0013*
3	04BA	TREG13	07A0	TREG3	074A	TRG	0435	TRMEM	073A
4	04C2	T3	0711	UA	098C	UNFORM	094B	UD	01C3
5	07FF	VAL	0920	VIDEO	00ED	VIDOFF	0012	WIFE	0A03*
6	03ED	WRTE	0A4E	WRTEL	0A81	XRAF	0978*	XREC	0990*
7	0176	ZERGA	094A	ZIP	00FE				

FATAL ERROR(3)

```

00001 . COMMENT:
00002 BY G. BUZZARD
00003 AND W. MACLEOD
00004
00005
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THIS IS A SET OF SUBROUTINES TO FILL THE SCREEN  
 WITH A REPRESENTATION OF THE STATE OF THE DDC SYSTEM.  
 THE LEFT SIDE OF THE SCREEN WILL DISPLAY A DISASSEMBLED  
 CORE LISTING, WITH THE PROGRAM COUNTER POINTING TO  
 THE MIDDLE LINE. ON THE RIGHT SIDE ARE DISPLAYED THE  
 CONTENTS OF THE UNPRIMED REGISTER SET, AS WELL AS THE  
 TOP OF THE STACK AND SELECTED STORAGE LOCATIONS.  
 THE CALLING PROGRAM MUST INITIALIZE A BANKSWITCH  
 REGISTER BY CALLING SUBROUTINE BANKSW. THIS REFORMATS  
 THE SCREEN AND SETS UP A "BANK SWITCH REGISTER" IN  
 STORAGE. (FOR A.K.A. XYCOM D.B.A HARDHAT COMPUTER  
 CLAIMS THAT THE BANK SWITCH REGISTER IS READ/WRITE,  
 NOT SO: IT IS WRITE-ONLY.)  
 THE USER'S REGISTERS ARE SAVED IN THE AREA  
 BEGINNING AT REGSAV. EACH USER REGISTER MAY BE  
 ADDRESSED SEPARATELY AS REGR WHERE R IS ITS NAME  
 (E.G. THE ACCUMULATOR IS SAVED IN REGA.)

. RADIX	16		
EXTRN	DISASS		. DISASSEMBLER
EXTRN	HEX		. WRITES LCON AND HEX FIELDS
EXTRN	LENGTH		
EXTRN	LINE		. DISASSEMBLED TEXT
EXTRN	OPCODE		. LINE+12 OR 30
EXTRN	ORGEND		. TABLE OF ORGEND PAIRS
EXTRN	INFO		. CHARACTERIZES INSTRUCTIONS
EXTRN	REGPC		
EXTRN	REGSAV		. REGISTER SAVE AREA
EXTRN	REGSP		
EXTRN	PRESAV		. PREVIOUS REG SAVE AREA
ENTRY	ACONV		
ENTRY	BANKSW		
ENTRY	BANKST		
ENTRY	CICD		
ENTRY	CURSES		
ENTRY	CURSOR		
ENTRY	FINTOP		
ENTRY	KEYIN		
ENTRY	MDISP		
ENTRY	N31		
ENTRY	REVID		
ENTRY	REVMEM		
ENTRY	SAVE		
ENTRY	SAVIT		
ENTRY	RSTATE		
ENTRY	SCREEN		
ENTRY	TEXTUP		
ENTRY	WIPE		

0010

0000		00037	MD13P:	DEFB	2		/ HOLDS ADDRESS OF MEMORY
		00038					/ TO BE DISPLAYED
		00039					
0010		00040	BLANK:	EQU	10		/ ASCII BLANK
0011		00041	BEFSA	EQU	0F7E0		/ JUST BEFORE TOP OF SCREEN
0012		00042	BE	EQU	08		/ ASCII BACKSPACE
0013		00043	CF	EQU	0F		/ ASCII CCR0
0014		00044	CLREL	EQU	32		/ CURSOR HOME RELATIVE (LOW)
0015		00045	NULL	EQU	00		/ ASCII NULL
0016		00046	KYBD	EQU	0E		/ KEYBOARD DATA ADDRESS
0017		00047	MEMLOC	EQU	0FB42		/ V-RAM ADDRESS
0018		00048	NEL	EQU	0FFFF		/ URGENT SENTINEL MARKER
0019		00049	POLL	EQU	14		/ KEYBOARD STATUS ADDRESS
0020		00070	R1&BIT	EQU	0FA12		
0021		00071	SCRTOP	EQU	0F800		/ TOP OF SCREEN IN RAM
0022		00072	SFLAGS	EQU	0F845		/ FLAG NAME FIELD
0023		00073	ST&LOC	EQU	0FB02		/ STACK FIELD IN VIDEO RAM
0024		00074	SR&S	EQU	0F862		/ REGISTER FIELD IN VIDEO RAM
0025		00075	VIDEO	EQU	0ED		
0026		00076	VIDOFF	EQU	12		/ FOR USE WITH BANKSW
0027		00077					
0028		00078					/ THE FOLLOWING TABLE HOLDS THE ADDRESSES OF THE SCREEN
0029		00079					/ LOCATIONS OF THE REGISTER CONTENTS DISPLAY. THIS
0030		00080					/ THIS TABLE IS INDEXED INTO FROM THE REVID (REVERSE
0031		00081					/ VIDEO) ROUTINE.
0032		00082					
0033	87	78	88	00083	DEFB	37, 0F8, 38, 0F8, 37, 0F8, 38, 0F8	/ F REG
0034	84	78	88	00084	DEFB	34, 0F8, 35, 0F8, 34, 0F8, 35, 0F8	/ A REG
0035	87	78	88	00085	DEFB	0D7, 0F8, 0D8, 0F8, 0D7, 0F8, 0D8, 0F8	/ C REG
0036	84	78	88	00086	DEFB	0D4, 0F8, 0D5, 0F8, 0D4, 0F8, 0D5, 0F8	/ B REG
0037	27	79	28	00087	DEFB	27, 0F9, 28, 0F9, 27, 0F9, 28, 0F9	/ E REG
0038	24	79	28	00088	DEFB	24, 0F9, 25, 0F9, 24, 0F9, 25, 0F9	/ D REG
0039	77	79	78	00089	DEFB	77, 0F9, 78, 0F9, 77, 0F9, 78, 0F9	/ L REG
0040	74	79	78	00090	DEFB	74, 0F9, 75, 0F9, 74, 0F9, 75, 0F9	/ H REG
0041	15	7A	16	00091			
0042	14	7A	16	00092	DEFB	15, 0FA, 16, 0FA, 17, 0FA, 18, 0FA	/ IX REG
0043	14	7A	16	00093	DEFB	15, 0FA, 16, 0FA, 17, 0FA, 18, 0FA	/ IX REG



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0000 00 FA 00
0001 00 FA 00
0002 00 FA 00
0003 00 FA 00
0004 00 FA 00
0005 00 FA 00
0006 00 FA 00
0007 00 FA 00
0008 00 FA 00
0009 00 FA 00
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0115 00 FA 00
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0118 00 FA 00
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0120 00 FA 00
0121 00 FA 00
0122 00 FA 00
0123 00 FA 00
0124 00 FA 00
0125 00 FA 00
0126 00 FA 00
0127 00 FA 00
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0129 00 FA 00
0130 00 FA 00
0131 00 FA 00
0132 00 FA 00

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00102 ;
00103 SCREEN. PUSH AF
00104 CALL FINTOP ; FIND TOP OF SCREEN
00105 LD A, VIDEO
00106 CALL BANKSW ; SWITCH IN VIDEO RAM
00107 CALL WIPE ; WIPE IT CLEAN
00108 CALL TEXTUP ; PUT UP DISASSEMBLED TEXT
00109 CALL RSTATE ; PUT UP REGISTER STATE
00110 CALL CURSCR ; SEND IT TO USER'S AREA
00111 CALL REVID ; PUT REV. VIDEO IN REGS V-RAM
00112 CALL REVMEM ; REVERSE VIDEO OF MEMORY CHNG
00113 CALL SAVE ; SAVE MEMORY DISPLAYED
00114 LD A, VIDOFF
00115 CALL BANKSW ; SWITCH OUT VIDEO RAM
00116 POP AF
00117 RET
00118 ;
00119 ; WIPE BLANKS THE ENTIRE SCREEN.
00120 WIPE: PUSH HL
00121 PUSH DE
00122 PUSH EC
00123 LD HL, SCRTOP
00124 LD (HL), BLANK
00125 LD EC, 7FF
00126 PUSH HL
00127 POP DE
00128 INC DE
00129 LDIR
00130 POP EC
00131 POP DE
00132 POP HL

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001A 0F          00133      RET
                00134
                00135 ; CURSOR REPOSITIONS (AND CAN ALTER) THE CURSOR.
001B 08 04      00136 CURSOR. LD      B, 4
001C 21 00DA    00137          LD      HL, CURSES
001D 0E 00      00138 CURSOUT. LD     C, 00
001E 0E 41      00139          OUTI
001F 0C          00140          INC     C
0020 0E 41      00141          OUTI
0021 10 F7      00142          JR     NZ, CURSOUT
0022 18          00143          RET
0023 0E 0E 0F    00144 CURSES. DEFB   0E, 05, 0F, 02, 0A, 40
0024 0E 0A 40

00145 ; THE LAST TWO BYTES WILL MAKE THE CURSOR BLINK.
00146 ; MAKE LOOP COUNT = 6 TO INCLUDE THEM.
00147 ;
00148 ; COMMENT: TEXTUP PUTS THE DISASSEMBLED TEXT
00149 UP ON THE (MEMORY MAPPED AT F800-0FFFF) SCREEN.
00150 IT EXPECTS HL TO POINT TO THE LOCATION TO BE DISPLAYED
00151 AT TOP OF SCREEN. THE LINE POINTED TO BY USER PC WILL
00152 BE IN REVERSE VIDEO (BLACK ON WHITE.)
00153 TEXTUP: PUSH   DE
00154          PUSH   BC
00155          PUSH   HL
00156          PUSH   HL
00157          LD     HL, BEFOR ; JUST BEFORE SCREEN TOP
00158          LD     (HERE), HL ; SAVE SCREEN LOCATION
00159          LD     B, 18 ; LOOP COUNTER
00160          POP    HL ; BRING BACK REF POINTER
00161 TXTP:  PUSH   BC
00162          PUSH   HL ; SAVE OLD REFERENCE POINTER
00163          CALL  WITHIN
00164          JR     NC, TIN
00165          LD     IX, ORGEND
00166 TNEXT: LD     H, (IX-1)
00167          LD     L, (IX+0)
00168          POP    BC ; GET REF POINTER
00169          PUSH   BC ; BALANCE STACK
00170          CP     A ; CLEAR CARRY FLAG
00171          SBC   HL, BC ; FIND NEXT ORG
00172          LD     BC, 0004
00173          JR     NC, TFOUND
00174          ADD   IX, BC
00175          JR     TNEXT ; AFTER THE LAST ORG-END
00176 ; PAIR, THERE HAD BETTER
00177 ; BE THE SENTINEL:
00178 ; FFFF, 0000
00179 ;
00180 TFOUND: SBC   HL, BC ; HOW MANY BYTES?
00181          JR     NC, FORQFM ; FOUR OF THEM
00182          ADD   HL, BC ; OR FEWER
00183          PUSH   HL
00184          POP    BC
00185 FORQFM: POP    HL
00186          ADD   HL, BC

```

0115	EF	00187	PUSH	HL	, RESTORE REF POINTER
0116	ED 41	00188	LD	(LENGTH), BC	
0118	0020*				
0119	ED 41	00189	SBC	HL, BC	
0117	CD 0000*	00190	CALL	HEX	
011F	41	00191	LD	B, C	, LOOP COUNTER
0120	41 0000*	00192	LD	DE, 0F00DE	, POINT AT 0F00DE FIELD
0121	CD 042A	00193 FLOOR:	CALL	ASCII	
0122	10 FB	00194	DJNZ	FLOOR	
0123	13 23	00195	JR	NORMAL	
012A	3E 12	00196	LD	A, VIDOFF	
012C	CD 0319	00197	CALL	BANKSW	, SWITCH OUT V-RAM
012F	CD 0000*	00198 TIN:	CALL	DISEAS	, DISASSEMBLE THE INSTR AT (HL)
0132	3E ED	00199	LD	A, VIDEO	
0134	CD 0529	00200	CALL	BANKSW	, SWITCH IN V-RAM
0137	D1	00201	POP	DE	, GET BACK OLD REF PTR
0138	E5	00202	PUSH	HL	, SAVE NEW REFERENCE POINTER
0139	E8	00203	EX	DE, HL	, POINT AT INSTR JUST DISASSEMBLED
013A	ED 4E	00204	LD	BC, (REGPC)	
013E	0000*				
013F	EF	00205	CP	A	, CLEAR CARRY FLAG
0141	ED 42	00206	SBC	HL, BC	, AT USER PC?
0143	20 0A	00207	JR	NZ, NORMAL	
0145	06 23	00208	LD	B, 23	
0146	21 0000*	00209	LD	HL, LINE	
0148	DE FE	00210 REVERS.	SET	7, (HL)	, REVERSE VIDEO IF SO
0149	13	00211	INC	HL	
014E	10 FB	00212	DJNZ	REVERS	
014D	2A 0176	00213 NORMAL.	LD	HL, (HERE)	, GET SCREEN POINTER
0150	11 0030	00214	LD	DE, 30	, LINE INCREMENT
0153	1F	00215	ADD	HL, DE	, POINT TO NEXT LINE
0154	12 0176	00216	LD	(HERE), HL	, SAVE SCREEN POINTER
0157	E5	00217	PUSH	HL	, PUT SCREEN POINTER
0158	D1	00218	POP	DE	, IN DESTINATION REG
0159	21 0000*	00219	LD	HL, LINE	, POINT AT TEXT
015C	01 0032	00220	LD	BC, 32	, CHARACTER COUNT
015F	ED 50	00221	LDIR		, PUT IT ON SCREEN
		00222			
0161	21 0000*	00223	LD	HL, LINE	, BLANK OUT LINE
0164	0E 20	00224	LD	C, 20	, ASCII BLANK
0166	06 32	00225	LD	B, 32	
0168	71	00226 LOOP1:	LD	(HL), C	, C ALREADY = 0
0169	23	00227	INC	HL	
016A	10 FC	00228	DJNZ	LOOP1	
		00229			
016C	E1	00230	POP	HL	, RESTORE CURS POINTER
016D	01	00231	POP	BC	, RESTORE LOOP COUNTER
016E	05	00232	DEC	B	, DJNZ TXTP
016F	02 00ED	00233	JP	NZ, TXTP	, LOOP FOR 24 LINES
		00234			
0172	E1	00235 TXRET:	POP	HL	, RESTORE CALLER'S REGISTERS
0173	01	00236	POP	BC	
0174	D1	00237	POP	DE	
0175	0F	00238	RET		
0176		00239 HERE:	DEFS	2	
		00240			

```

00241 . RETATE PUTS UP THE RIGHT HAND SIDE OF THE SCREEN
00242 . (THE REGISTER STATES AND MEMORY LOCATIONS).
171 FF 00243 RETATE. PUSH AF . SAVE CALLER'S REGISTERS
172 DF 00244 PUSH BC
173 DF 00245 PUSH DE
174 DF 00246 PUSH HL
175 DF 00247 PUSH IX
176 DF 00248 PUSH IY
177 21 01FF 00249 LD HL, NAMEE . REGISTER NAME LIST
178 FD 21 00250 LD IY, SFLAG . REGISTER FIELD ADDR
179 FB43
180 06 03 00251 LD B, 3
181 7E 00252 NFLAG. LD A, (HL) . PUT UP FLAG LABELS
182 FD 77 00 00253 LD (IY), A
183 FD 23 00254 INC IY
184 23 00255 INC HL
185 06 F7 00256 DUNZ NFLAG
186 FD 21 00257 LD IY, SREGS . POINT TO REG FIELD
187 FB82
188 00258
189 DD 21 00259 LD IX, REGSAY . USER'S REGISTER SET
190 0000+
191 06 04 00260 LD B, 4 . LOOP COUNTER
192 7E 00261 REGS: LD A, (HL) . REGISTER NAME
193 FD 77 00 00262 LD (IY), A . TO VIDEO RAM
194 FE 3A 00263 LD A, 1
195 FD 77 01 00264 LD (IY+1), A
196 DD 7E 00 00265 LD A, (IX) . REGISTER CONTENTS
197 DD 04E2 00266 CALL ACONV . ONTO SCREEN
198 FD 72 05 00267 LD (IY+5), D
199 FD 73 06 00268 LD (IY+6), E
200 FE 03 00269 PUSH HL . SAVE NAME POINTER
201 FD FE 00270 PUSH IY
202 E1 00271 POP HL . 16-BIT LOAD
203 11 0013 00272 LD DE, 13
204 1F 00273 ADD HL, DE . POINT TO BINARY FIELD
205 DD 031E 00274 CALL FBITS
206 DD 7E 01 00275 LD A, (IX+1) . NEXT REG. CONTENTS
207 DD 04E2 00276 CALL ACONV
208 FD 72 02 00277 LD (IY+2), D . ONTO SCREEN
209 FD 73 03 00278 LD (IY+3), E
210 37 00279 SCF
211 3F 00280 CCF . CLEAR CARRY
212 11 0011 00281 LD DE, 11
213 DD ED 51 00282 SBC HL, DE . POINT TO NEXT BINARY FIELD
214 DD 031E 00283 CALL FBITS
215 E1 00284 POP HL . RESTORE NAME POINTER
216 23 00285 INC HL
217 FE 3A 00286 LD A, 1
218 FD 77 07 00287 LD (IY+7), A
219 7E 00288 LD A, (HL) . REGISTER NAME
220 FD 77 08 00289 LD (IY+8), A
221 23 00290 INC HL
222 11 0002 00291 LD DE, 02
223 DD 1F 00292 ADD IX, DE . NEXT REGISTER PAIR
224 11 0050 00293 LD DE, 50

```

01E:	FD 19	00294	ADD	IY, DE	, NEXT SCREEN LINE
01E1:	10 82	00295	DJNZ	REGS	
		00296			
		00297	COMMENT:		
		00298			
		00299	THIS SECTION WRITES THE TITLES AND CONTENTS OF		
		00300	THE 16-BIT REGISTERS, THE I REGISTER, AND THE R REGISTERS		
		00301	TO THE VIDEO RAM.		
		00302			
01E9:	FD 21	00303	LD	IY, R16BIT	
01E0:	FA12				
01EE:	06 02	00304	LD	B, 2	
01F0:	7E	00305	LD	A, (HL) ; WRITE TITLE (FOR IX)	
01F1:	FD 77 00	00306	LD	(IY), A ; (2ND TIME FOR IY)	
01F4:	23	00307	INC	HL	
01F5:	7E	00308	LD	A, (HL)	
01F6:	FD 77 01	00309	LD	(IY+1), A	
01F8:	23	00310	INC	HL	
01FA:	3E 3A	00311	LD	A, 01	
01FC:	FD 77 02	00312	LD	(IY+2), A ; END WRITE TITLE	
01FF:	0D 7E 00	00313	LD	A, (IX) ; WRITE CONTENTS OF IX	
0202:	0D 23	00314	INC	IX	
0204:	0D 0422	00315	CALL	ACONV ; CONVERT TO ASCII	
0207:	FD 72 05	00316	LD	(IY+5), D	
020A:	FD 73 06	00317	LD	(IY+6), E	
020E:	0D 7E 00	00318	LD	A, (IX)	
0210:	0E 23	00319	INC	IX	
0212:	0D 0422	00320	CALL	ACONV	
0215:	FD 72 03	00321	LD	(IY+3), D	
0218:	FD 73 04	00322	LD	(IY+4), E ; END WRITE IX	
021E:	7E	00323	LD	A, (HL) ; WRITE TITLE (FOR 3P)	
0220:	23	00324	INC	HL ; (2ND TIME FOR 3P)	
0222:	FD 77 09	00325	LD	(IY+9), A	
0225:	7E	00326	LD	A, (HL)	
0227:	23	00327	INC	HL	
0229:	FD 77 0A	00328	LD	(IY+0A), A	
022B:	3E 3A	00329	LD	A, 01	
022D:	FD 77 0E	00330	LD	(IY+0B), A	
022F:	0D 7E 00	00331	LD	A, (IX) ; WRITE CONTENTS OF 3P	
0232:	0D 23	00332	INC	IX	
0235:	0D 04E1	00333	CALL	ACONV	
0238:	FD 72 0E	00334	LD	(IY+0E), D	
023B:	FD 73 0F	00335	LD	(IY+0F), E	
023E:	0D 7E 00	00336	LD	A, (IX)	
0240:	0D 23	00337	INC	IX	
0243:	0D 04E1	00338	CALL	ACONV	
0246:	FD 72 0C	00339	LD	(IY+0C), D	
0249:	FD 73 0D	00340	LD	(IY+0D), E ; END WRITE 3P	
024B:	7E	00341	LD	A, (HL) ; WRITE TITLE (FOR I)	
024D:	23	00342	INC	HL ; (2ND TIME FOR I)	
024F:	FD 77 11	00343	LD	(IY+11), A	
0252:	3E 3A	00344	LD	A, 01	
0255:	FD 77 13	00345	LD	(IY+13), A	
0258:	0D 7E 00	00346	LD	A, (IX) ; WRITE CONTENTS OF I	
025B:	0D 23	00347	INC	IX	
025E:	0D 04E1	00348	CALL	ACONV	

```

00349 LD (IY-14),D
00350 LD (IY-15),E
00351 LD DE,50
00352 ADD IY,DE
00353 DJNZ 5F53
00354
00355 ; THIS SECTION WRITES THE TITLE AND TOP FOUR
00356 ; STACK LOCATION CONTENTS TO THE VIDEO RAM
00357 LD IX,(REGSP) ; USER STACK POINTER
00358 LD DE,STKLOC ; ADDRESS IN VIDEO RAM
00359 LD BC,6
00360 LDIR ; WRITE "STACK:" TO V-RAM
00361 PUSH DE
00362 POP IY ; USE IY FOR ADDRESSES
00363 LD B,4 ; LOOP COUNTER
00364 N1: INC IY ; SKIP SPACE
00365 LD A,(IX+1) ; GET ONE NIBBLE
00366 INC IX ; THIS IS AN UNROLLED
00367 CALL ACONV ; LOOP
00368 LD (IY),D
00369 INC IY
00370 LD (IY),E ; WRITE NIBBLE
00371 INC IY
00372 LD A,(IX-1) ; GET ONE NIBBLE
00373 INC IX ; THIS IS THE OTHER
00374 CALL ACONV ; HALF OF IT.
00375 LD (IY),D
00376 INC IY
00377 LD (IY),E ; WRITE NIBBLE
00378 INC IY
00379 DJNZ N1 ; RETURN FOR NEXT NIBBLE
00380
00381 ; THIS SECTION WRITES THE TITLE, ADDRESS, AND
00382 ; CONTENTS OF 32 CONTIGUOUS MEMORY LOCATIONS TO
00383 ; THE VIDEO RAM
00384 LD DE,MEMLOC ; LOAD V-RAM ADDRESS
00385 LD BC,7
00386 LDIR ; WRITE "MEMORY:" TO V-RAM
00387 PUSH DE
00388 POP IY ; USE IY FOR V-RAM ADDRESS
00389 LD DE,49 ; SKIP TO NEXT LINE
00390 ADD IY,DE ; " "
00391 LD HL,(MDISP) ; GET MEMORY DISPLAY
00392 ; LOCATION
00393 LD C,4 ; OUTER LOOP COUNTER
00394 N3: LD A,H ; WRITE MEM-ADDR TO V-RAM
00395 CALL ACONV
00396 LD (IY),D
00397 LD (IY+1),E
00398 LD A,L
00399 CALL ACONV
00400 LD (IY+2),D
00401 LD (IY+3),E
00402 LD A,L
00403 LD (IY+4),A ; END WRITE MEM-ADDR

```

02F0	DD	E1	00417	LD	DE, B	INNER LOOP COUNTER
02F1	E1		00418	LD	DE, B	
02F2	D1		00419	ADD	IX, DE	UPDATE IX LOCATION
02F3	D1		0041A	INC	IX	SKIP SPACE
02F4	C1		0041B	LD	A, VALD	WRITE MEMORY CONTENTS
02F5	F1		0041C	INC	IX	TO VIDEO RAM
02F6	C9		0041D	CALL	ACORV	
02F7	53	5A 2A	0041E	LD	VIY, E	
02FA	48	2A 51	0041F	INC	IX	
02FB	0E	43	00420	LD	VIY, E	
02FC	41	46 42	00421	INC	IX	
0302	43	44 45	00422	INC	IX	END WRITE MEMORY CONTENTS
0305	48	4C	00423	DNZ	IX	RETURN FOR NEXT MEMORY
0307	49	53 53	00424	DEC	IX	DEC OUTER LOOP
030A	50	49 49	00425	LD	DE, B8	
030D	59	50 43	00426	ADD	IX, DE	SKIP TO NEXT LINE
0310	52		00427	JR	NZ, NS	RETURN TO WRITE NEXT LINE
0311	53	54 41	00428	POP	IX	
0314	43	4B 3A	00429	POP	HL	
0317	4D	45 4D	00430	POP	DE	
031A	4F	52 59		POP	BC	
031D	3A			POP	AF	
			00431	RET		
			00432	DEFM	'SZ*H*PNC'	
			00433	DEFM	'AFBCDEHL'	
			00434	DEFM	'IXEPIYPCR'	
			00435	DEFM	'STACK: MEMORY:'	
			00436	DEFM		
031E	C5		00436	PUSH	BC	
031F	D5		00437	PUSH	DE	
0320	06	08	00438	LD	B, B	LOOP 8 TIMES
0321	0E	07	00439	RLC	A	LOOK AT MSB
0324	38	04	00440	JR	C, ONE	SELECT CHARACTER
0326	16	30	00441	LD	D, '0'	
0328	18	02	00442	JR	PUT	
032A	16	31	00443	LD	D, '1'	
032C	72		00444	LD	(HL), D	PUT CHAR. ON SCREEN
032D	13		00445	INC	HL	
032E	10	F1	00446	DJNZ	BITS	
0330	D1		00447	POP	DE	
0331	C1		00448	POP	BC	

```

00449 RET
00450
00451 FINTOP FINDE THE PLACE TO START DISASSEMBLY
00452 SO THAT THE USER'S PROGRAM COUNTER WILL POINT
00453 TO THE LINE IN THE MIDDLE OF THE SCREEN
00454 ON RETURN, HL POINTS TO THE LOCATION TO BE
00455 DISPLAYED AT TOP OF SCREEN.
00456
00457 IN THE CIRCULAR QUEUE THAT REMEMBERS THE
00458 LAST 12 LINES, WE WILL USE FFFF(HEX) TO MEAN
00459 NULL. SINCE THIS PROGRAM IS TO BE LOADED AT
00460 THE TOP OF MEMORY, THE USER'S PROGRAM WILL
00461 NEVER REACH FFKX.
00462
00463 FINTOP LD IX,SPOT ; FIRST SPOT IN QUEUE
00464 LD HL,(REGPC) ; USER PC
00465 IF A ; CLEAR CARRY FLAG
00466 LD BC,34
00467 SBC HL,BC ; GO BACK 32 BYTES
00468
00469 LD ;
00470 LD ;
00471 JUMP IF WITHIN ORGEND
00472 ; FILL QUEUE WITH ADDRESSES (4 AT A TIME) IF NOT WITHIN
00473 ; ORG-END PAIR
00474 IF ;
00475 LD ; PUT HL IN QUEUE
00476 LD ;
00477 INC HL ; NEXT ADDRESS (HL+4)
00478 INC HL
00479 INC HL
00480 INC HL
00481 CALL ORGEXT ; POINT TO NEXT SPOT
00482
00483 ;
00484 CALL WITHIN
00485 IF ;
00486 ; CHECK IF REG. POINTER (HL) IS = REGPC -- IF SO JUMP
00487 EX DE,HL ; SAVE HL
00488 LD HL,(REGPC)
00489 IF A ; ZERO CARRY FLAG
00490 SBC HL,DE
00491 EX DE,HL
00492 JR Z,FDONE ; JUMP IF =
00493 JR C,FDONE ; JUMP IF HL>REGPC
00494 JR F2
00495 ; IF WITHIN ORG-END START AT ORG, FILL QUEUE UNTIL
00496 ; END OR UNTIL HL=REGPC
00497 F1 LD HL,(IX+1) ; START AT ORG
00498 LD L,(IX)
00499 ; CHECK IF IX (HL) = (REGPC)
00500 PUSH HL ; SAVE HL
00501 OR A ; CLEAR CARRY FLAG
00502 LD BC,(REGPC)
00503 SBC HL,BC
00504 JR NZ,F1.5 ; JUMP IF NOT EQUAL
00505

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0375  E1          00505      POP      HL          ; BALANCE STACK
0376  13 17      00506      JR        FDCN
0377  E1          00507      POP      HL          ; BALANCE STACK
0378  FD 75 01    00508      LD       (IY),L      ; PUT HL IN QUEUE
0379  FD 76 01    00509      LD       (IY+1),H
037F  1E 0300*   0050F      CALL    INFO        ; FIND INSTR LENGTH
0382  E2 03      00510      AND     03
0384  3C          00511      INC     A
0385  02 00      00512      LD     B,0
0387  4F          00513      LD     C,A
0388  09          00514      ADD     HL,BC        ; INC REF. POINTER
0389  0D 03A9     00515      CALL   CNEXT        ; POINT TO NEXT SPOT
0390  EE          00516      CHECK IF REF. POINTER (HL) IS >= REGPC -- IF SO JUMP
0391  EA 0000*   00517      EX     DE,HL        ; SAVE HL
0392  1A 0000*   00518      LD     HL,(REGPC)
0393  EF          00519      CF     A            ; ZERO CARRY FLAG
0394  EE 51      00520      SBC    HL,DE
0395  EE          00521      EX     DE,HL
0396  3E 09      00522      JR     C,FDCN       ; JUMP IF HL>REGPC
0397  2E 07      00523      JR     Z,FDCN       ; JUMP IF =
0398  0D 03EC     00524      CALL   INSIDE
0399  38 A8      00525      JR     C,F2        ; IF OUTSIDE, JUMP
039A  13 5A      00526      JR     F3        ; ELSE, REPEAT
039B  0D 03A7     00527      FDCN: CALL   CNEXT
039C  FD 86 01    00528      FDCNE: LD     H,(IY+1) ; PUT STARTING LOC'N
039D  FD 8E 00    00529      LD     L,(IY)      ; IN HL
039E  1F          00530      RET
039F  05          00531      ;
03A0  05          00532      ;
03A1  05          00533      ;
03A2  05          00534      ;
03A3  05          00535      ;
03A4  05          00536      ;
03A5  05          00537      ;
03A6  05          00538      ;
03A7  05          00539      ;
03A8  05          00540      ;
03A9  05          00541      ;
03AA  05          00542      ;
03AB  01 03DC     00543      CNEXT: PUSH   HL
03AC  FD 13      00544      PUSH   BC
03AD  FD 13      00545      LD     BC,SPOT-18
03AE  FD 13      00546      INC    IY
03AF  FD 13      00547      INC    IY
03B0  FD 13      00548      PUSH   IY
03B1  FD 13      00549      PUSH   IY
03B2  EF          00550      CF     A            ; RESET CARRY FLAG
03B3  4F          00551      POP    HL
03B4  ED 42      00552      SBC    HL,BC
03B5  FD 13      00553      POP    IY
03B6  01          00554      POP    BC
03B7  E1          00555      POP    HL
03B8  08          00556      RET     C
03B9  FD 11      00557      LD     IY,SPOT
03BA  0301* 0304     00558      ;
03BB  09          00559      RET
03BC  13          00560      SPOT:  DEFB 13      ; CIRCULAR FIFO ADDRESS QUEUE

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-000-

00537 CNEXT TAKES THE IY REGISTER (ASSUMED TO POINT TO AN ADDRESS WITHIN SPOT) AND RETURNS THE NEXT SPOT. 00538 ADDRESSES WITHIN SPOT WRAP AROUND; IT IS A FIFO QUEUE WITH ONLY TWELVE MEMBERS.

```

00558
00559 /INSIDE RESETS THE CARRY FLAG IF THE HL ADDRESS LIES
00560 /WITHIN THE ORGEND PAIR POINTED TO BY IX.
00561 INSIDE. PUSH BC /SAVE CALLER'S REGS
00562 PUSH HL /HL HOLDS ADDR
00563 CP A /CLEAR CARRY FLAG
00564 LD B,(IX+1)
00565 LD C,(IX) /BC GETS ORG
00566 SBC HL,BC /SET CY IF ORG>ADDR
00567 POP HL
00568 POP BC
00569 RET C
00570 PUSH BC
00571 PUSH HL
00572 POP BC /BC GETS ADDR
00573 LD H,(IX+3)
00574 LD L,(IX+2) /HL GETS END
00575 DEC HL
00576 SBC HL,BC /SET CY IF ADDR>(END-1)
00577 PUSH BC
00578 POP HL
00579 POP BC
00580 RET
00581
00582 /WITHIN CHECKS THE HL REG AGAINST ALL ORGEND PAIRS.
00583 /IT RESETS CARRY FLAG IF HL LIES INSIDE A PAIR.
00584 /ON RETURN, IX POINTS TO THE ORG-END PAIR CONTAINING
00585 /THE INSTRUCTION (IF INSIDE AN ORGEND.)
00586 WITHIN. LD IX,ORGEND /START AT BEGINNING...
00587 PUSH BC
00588 PUSH HL /SAVE CALLERS REGS
00589 WNEXT. LD B,(IX+1)
00590 LD C,(IX) /BC GETS ORG
00591 CP A /CLEAR CARRY FLAG
00592 SBC HL,BC /SET CY IF ORG>ADDR
00593 JR C,WRET
00594 POP HL /RESTORE ADDR
00595 PUSH HL
00596 CALL INSIDE
00597 JR NC,WRET
00598 PUSH HL
00599 LD BC,0004
00600 ADD IX,BC
00601 LD H,(IX+1)
00602 LD L,(IX) /HL GETS ORG
00603 LD BC,0001
00604 ADD HL,BC /ORG = FFFF?
00605 POP HL
00606 JR Z,WRET
00607 JR WNEXT /ORGEND HAD BETTER HAVE FFFF
00608 /AFTER IT!
00609 WRET: POP HL
00610 POP BC
00611 RET
00612

```

0414	00		00613	ASCII	POBR	BC
0415	00		00614		POBR	HL
0416	00		00615		LD	A, (HL)
0417	00		00616		REB	7, A TREAT THEM ALL AS ASCII
0418	00		00617		CP	7F
0419	20	00	00618		LD	NZ, AEN
0420	21	04FE	00619		LD	HL, DLET
0421	22	10	00620		OR	MOV4
0422	23	20	00621	AREN	CP	20
0423	24	0E	00622		OR	C, NONFR
0424	25		00623		LD	(DE), A
0425	26		00624		INC	DE
0426	2E	20	00625		LD	A, BLANK
0440	06	03	00626		LD	B, 3
0441	12		00627	BLOOP:	LD	(DE), A
0442	13		00628		INC	DE
0443	10	FD	00629		DONZ	BLOOP
0444	21		00630		POP	HL
0445	23		00631		INC	HL
0446	01		00632		POP	BC
0447	02		00633		RET	
0448	03	00	00634	NONFR.	LD	B, 0
0449	0B	17	00635		SLA	A
044E	0B	17	00636		SLA	A
0450	4F		00637		LD	C, A
0451	11	045E	00638		LD	HL, CTRL
0454	09		00639		ADD	HL, BC
0455	01	0004	00640	MOV4.	LD	BC, 4
0456	2D	00	00641		LDIR	
045A	21		00642		POP	HL
045B	23		00643		INC	HL
045C	21		00644		POP	BC
045D	09		00645		RET	
045E	2E	33 40	00646	CTRL.	DEFM	^NUL SOH STX ETX EOT ENQ ACK BEL
0461	10	33 4F				
0464	23	20 33				
0467	34	33 20				
046A	43	34 33				
046D	20	43 4F				
0470	34	20 43				
0473	4E	31 20				
0476	41	43 4B				
0479	20	42 43				
047C	40	20				
047E	42	33 20	00647		DEFM	^BS HT LF VT FF CR SO SI
0481	20	43 34				
0484	20	20 40				
0487	43	20 20				
048A	36	34 20				
048D	20	46 43				
0490	20	20 43				
0493	32	20 20				
0496	33	4F 20				
0499	20	33 49				
049C	20	20				
049E	44	40 43	00648		DEFM	^DLE DC1 DC2 DC3 DC4 NAK SYN ETB

04B1 20 44 43  
 04B4 31 20 44  
 04B7 43 31 20  
 04B8 44 43 31  
 04B9 20 44 43  
 04BC 34 20 4E  
 04BE 41 4E 20  
 04B8 33 39 4E  
 04BF 20 45 34  
 04C1 42 20  
 04CE 43 41 4E 00649  
 04D1 20 4E 4E  
 04D4 20 20 33  
 04D7 33 42 20  
 04DA 43 33 43  
 04DB 20 46 33  
 04DE 20 20 47  
 04D3 33 20 20  
 04D6 31 33 20  
 04D9 20 36 33  
 04DC 20 20  
 04DE 44 45 4C 00650  
 04E1 20

DEFM 00649 CAN EM SUB 230 PS GS RS VS

00650 DLET: DEFM 00650 DEL

04E2 33  
 04E3 33  
 04E4 21 0500  
 04E7 37  
 04E9 3E 33  
 04EA 3E 3F  
 04EB 3E 3A  
 04ED 38 02  
 04E1 16 07  
 04E4 3F  
 04E5 3E  
 04E6 3E 3A  
 04E8 38 02  
 04FA 06 07  
 04FC 37  
 04FD 31  
 04FE 31  
 04FF 09  
 0500

00651  
 00652 ACONV: PUSH HL  
 00653 PUSH AF  
 00654 LD HL, ALOC  
 00655 LD (HL), A  
 00656 LD A, 33H  
 00657 RLD  
 00658 RLD  
 00659 CP 3AH  
 00660 JR C, ANAJ1  
 00661 ADD A, 7  
 00662 ANAJ1: LD E, A  
 00663 LD A, (HL)  
 00664 CP 3AH  
 00665 JR C, ANAJ2  
 00666 ADD A, 7  
 00667 ANAJ2: LD D, A  
 00668 POP AF  
 00669 POP HL  
 00670 RET  
 00671 ALOC: DEFS 1  
 00672  
 00673  
 00674



00675 ; BANKST INITIALIZES THE BANK SWITCHING REGISTER, AND  
 00676 ; SETS UP A SAVE FIELD (XYCOM'S OUTSTANDING DOCUMENTATION  
 00677 ; NOTWITHSTANDING), THE BANK SWITCHING REGISTER IS WRITE  
 00678 ; ONLY.)

00679 ; IT ALSO SETS UP THE CRT CONTROLLER FOR 24  
 00680 ; 80-CHARACTER LINES.

0501 35  
 0502 05  
 0503 35

00681 BANKST: PUSH AF ; SAVE ACCUMULATOR  
 00682 PUSH BC  
 00683 PUSH HL

```

0504 1E 01 00684 LD A, 03 ; DEFAULT SETTING
0505 12 051E 00685 LD (BANKS), A ; SAVE BANK STATUS
0506 03 2F 00686 OUT (2F), A ; WRITE TO BANK REGISTER
00687 ;
0508 11 051F 00688 LD HL, VIDSET ; CRT CONTROLLER SETTING
0509 06 15 00689 LD B, 5 ; # OF CRTG REGS TO FIX
0510 7E 00690 SETVID. LD A, (HL) ; GET REGISTER NUMBER
0511 03 00 00691 OUT (VREG), A ; SEND TO CRTG REG SELE
0512 13 00692 INC HL ; POINT TO NEXT ENTRY
0513 7E 00693 LD A, (HL) ; GET REGISTER CONTENTS
0514 03 01 00694 OUT (VWORD), A ; SEND TO CRTG REGISTER
0515 13 00695 INC HL ; POINT TO NEXT ENTRY
0516 10 F6 00696 DJNZ SETVID
0517 E1 00697 POP HL
0518 01 00698 POP BC
0519 F1 00699 POP AF
051E 09 00700 RET
00701 ;
051E 00702 BANKS: DEFB !
051F 04 15 05 00703 VIDSET: DEFB 04, 15, 05, 1A, 06, 15, 07, 15, 09, 0A
051E 1A 06 15
051E 07 15 09
051E 0A
0000 00704 VREG EQU 00
0001 00705 VWORD EQU 01
00706 ;
00707 ; COMMENT#
00708 ;
00709 ; BANKSW ALTERS THE STORED (SAVED) BANK SWITCH REGISTER
00710 ; AND SENDS AN ALTERED WORD TO THE HARDWARE BANK REG.
00711 ; IT EXPECTS THE ACCUMULATOR TO CONTAIN INFORMATION
00712 ; SPECIFYING THE CHANGE TO BE MADE.
00713 ; TO TURN ON A BIT OR BITS, SET BIT 4 OF THE ACC. AND
00714 ; SET THE DESIRED BIT/S (ALL OTHERS SHOULD BE ZERO.)
00715 ; TO TURN OFF A BIT OR BITS, RESET BIT 4 AND THE DESIRE
00716 ; BIT/S, WITH ALL OTHER BITS SET.
00717 ;
00718 #
0519 25 00719 BANKSW: PUSH HL
0520 21 051E 00720 LD HL, BANKS ; POINT TO STATUS BYTE
0521 0B 67 00721 BIT 4, A ; TURN ON OR OFF?
0522 18 03 00722 JR Z, OFF ;
0523 06 00723 OR (HL) ; TURN BIT(S) ON
0524 18 01 00724 JR GOBAK ;
0525 46 00725 OFF. AND (HL) ; TURN BIT(S) OFF
0526 32 051E 00726 GOBAK. LD (BANKS), A ; SAVE STATUS BYTE
0527 E1 00727 POP HL
0528 03 2F 00728 OUT (2F), A ; WRITE TO BANK REGISTER
0529 09 00729 RET
00730 ;
00731 ; THIS ROUTINE COMPARES THE VALUES OF THE USER'S
00732 ; REGISTER SET BEFORE AND AFTER THE SIMULATION OF
00733 ; THE PREVIOUS (USER'S) INSTRUCTION. IF A DIFFERENCE
00734 ; IS FOUND THE CORRESPONDING REGISTER VALUE IS
00735 ; DISPLAYED IN REVERSE VIDEO IN THE VIDEO RAM.
00736 ;

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05307 F5      00737 REVID:  PUSH    AF
05317 D5      00738      PUSH    BC
05327 D5      00739      PUSH    DE
05337 E5      00740      PUSH    HL
           00741 ;
05407 01 0012  00742      LD      BC,12      ; LENGTH OF COMPARE
05437 11 0000+ 00743      LD      DE,REGSAV ; OLD ONES
05467 21 0000+ 00744      LD      HL,REGSAV ; NEW ONES
           00745 ;
05477 1A      00746 L1:      LD      A,(DE)      ; OLD VALUE
05487 ED A1    00747      CPI
05497 13      00748      INC     DE          ; COMPARE TEM
054D7 04 0538  00749      CALL   NZ,REV      ; IF DIFF REVERSE IT
05507 EA 0549  00750      JP     PE,L1       ; LOOP IF NOT DONE
           00751 ;
05537 E1      00752      POP     HL
05547 D1      00753      POP     DE
05557 01      00754      POP     BC
05567 F1      00755      POP     AF
05577 09      00756      RET
           00757 ;
05587 F5      00758 REV:   PUSH    AF
05597 D5      00759      PUSH    BC
055A7 D5      00760      PUSH    DE
055B7 E5      00761      PUSH    HL
           00762 ;
055C7 11 0000+ 00763      LD      DE,REGSAV ; START ADDRESS
055F7 2B      00764      DEC     HL
05607 37      00765      SCF
05617 3F      00766      CCF
05627 2D 52    00767      SBC     HL,DE      ; ZERO CARRY FLAG
           00768 ;
           00769 ;
05647 0B 25    00769      SLA     L
05657 0B 25    00770      SLA     L
05667 0B 25    00771      SLA     L
056A7 11 0002  00772      LD      DE,SCRLOC ; MULTIPLY BY 3
056D7 19      00773      ADD     HL,DE      ; ADDRESS OF TABLE
           00774 ;
           00775 ;
056E7 06 04    00775      LD      B,4        ; LOOP COUNT
05707 5E      00776 LZ:      LD      E,(HL)
05717 23      00777      INC     HL
05727 56      00778      LD      D,(HL)
05737 23      00779      INC     HL
05747 EB      00780      EX     DE,HL      ; HL=(TABLE ENTRY)
05757 0B FE    00781      SET    7,(HL)    ; SET REV. VIDED BIT
05777 EB      00782      EX     DE,HL
05787 10 F6    00783      DJNZ   LZ
           00784 ;
057A7 E1      00785      POP     HL
057B7 D1      00786      POP     DE
057C7 01      00787      POP     BC
057D7 F1      00788      POP     AF
057E7 09      00789      RET
           00790 ;
00791 ; SAVE SAVES THE TOP FOUR STACK WORDS (2 BYTES EACH),
00792 ; AND THE 32 DISPLAYED MEMORY VALUES.  THESE VALUES

```

00796 , ARE STORED IN SAVIT.

00794 ;

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0878  FE  00795  SAVE.  PUSH  AF
0879  CF  00796          PUSH  BC
0880  DF  00797          PUSH  DE
0881  EF  00798          PUSH  HL
0882  EF  00799 ;
0883  BE  04  00800          LD    A, 4 ; OUTER COUNTER
0884  21  FB0A 00801          LD    HL, 37A100-7 ; FROM V-RAM
0885  11  08E0 00802          LD    DE, SAVIT ; TO SAVIT
0886  01  0004 00803  L3:   LD    BC, 4 ; MOVE TOP 4 OF STACK
0887  ED  B0  00804          LDIR ;
0888  23  00805          INC   HL ; SKIP SPACE
0889  3E  00806          DEC   A ; DEC COUNTER
0890  20  F7  00807          JR    NZ, L3
0891  00808 ;
0892  21  FBFB 00808          LD    HL, MEMLOC-56 ; START ADDRESS
0893  3E  04  00809          LD    A, 4 ; OUTER COUNTER
0894  01  0010 00810          LD    BC, 10
0895  ED  A0  00811  N7:   LDI ;
0896  ED  A0  00812  N7A:  LDI ;
0897  23  00813          INC   HL ; NEXT BYTE
0898  EA  089C  00814          JP   PE, N7A
0899  00815 ;
0900  DE  00816          PUSH  DE
0901  11  0038 00817          LD    DE, 38
0902  1F  00818          ADD  HL, DE ; POINT TO NEXT LINE
0903  D1  00819          POP  DE
0904  00820 ;
0905  3D  00821          DEC   A
0906  20  EC  00822          JR    NZ, N7 ; NEXT LINE
0907  00823 ;
0908  00824 ;
0909  21  08E0  00825          LD    HL, SAVIT
0910  06  B0  00826          LD    B, 50
0911  0E  BE  00827  FIXIT. RES  7, (HL) ; RESET VIDEO BIT
0912  23  00828          INC   HL
0913  10  FE  00829          DJNZ  FIXIT
0914  00830 ;
0915  E1  00831          POP  HL
0916  D1  00832          POP  DE
0917  01  00833          POP  BC
0918  F1  00834          POP  AF
0919  CF  00835          RET
0920  00836 ;
0921  00837  SAVIT.  DEFS  50
0922  00838 ;
0923  00839 ; REVMEM COMPARES THE NEW MEMORY DISPLAY WITH THE OLD
0924  00840 ; ONE. THE BYTES WHICH DIFFER ARE DISPLAYED IN REVER
0925  00841 ; VIDED. PLEASE NOTE THAT WHEN THE MEMORY DISPLAY
0926  00842 ; ADDRESS IS CHANGED THE BYTES IN THE SAME RELATIVE
0927  00843 ; SCREEN POSITION ARE COMPARED FOR DIFFERENCES. THIS
0928  00844 ; ALLOWS YOU TO COMPARE BLOCKS OF MEMORY FROM THE
0929  00845 ; KEYBOARD WITH THE DIFFERENCES BEING HIGHLIGHTED.
0930  00846 ;
0931  FE  00847  REVMEM.  PUSH  AF
0932  CE  00848          PUSH  BC
```

060E	DE	00849		PUSH	DE	
060F	EF	00850		PUSH	HL	
		00851				
0610	FD 21	00852		LD	IX, 00	/ DISPLACEMENT COUNT
0611	0000					
0612	3E 04	00853		LD	A, 4	
0613	32 0675	00854		LD	(CNT), A	/ COUNTER
0614	21 F80F	00855		LD	HL, STRLOC+7	/ START ADDRESS
0615	ED 21	00856		LD	IX, STRLOC+7	/ SAVE IT
0616	F80F					
0620	11 05E0	00857		LD	DE, SAVIT	/ OTHER ONE
		00858				
0623	01 0004	00859	L3:	LD	BC, 4	/ COMPARE FIRST WORD
0624	1A	00860	L4:	LD	A, (DE)	
0627	ED A1	00861		CPI		/ COMPARE TEM
0629	13	00862		INC	DE	
062A	20 3E	00863		JR	NZ, CHANG4	/ JUMP IF DIFF
062C	FD 13	00864	R1:	INC	IX	/ INC DISPLACEMENT
062E	EA 0623	00865		JP	PE, L4	
		00866				
0631	13	00867		INC	HL	
0632	3A 0675	00868		LD	A, (CNT)	
0635	3D	00869		DEC	A	/ DEC COUNTER
0636	32 0675	00870		LD	(CNT), A	
0639	20 3E	00871		JR	NZ, L5	/ CHECK NEXT WORD
		00872				
063B	3E 04	00873		LD	A, 4	
063D	32 0675	00874		LD	(CNT), A	
		00875				
0640	21 F8F3	00876		LD	HL, MEMLOC+56	/ MEM DISPLAY ADDR
0643	01 0010	00877	L3:	LD	BC, 10	
0646	3E	00878		PUSH	HL	
0647	ED E1	00879		POP	IX	/ START OF LINE
0649	FD 21	00880		LD	IX, 00	/ DISPLACEMENT COUNT
064E	0000					
064D	1A	00881	L7:	LD	A, (DE)	
064E	ED A1	00882		CPI		/ CHECK FIRST BYTE
0650	20 24	00883		JR	NZ, CHAN3A	/ JUMP IF DIFF
		00884				
0652	FD 13	00885		INC	IX	/ INC DISPLACEMENT
0654	13	00886		INC	DE	
0655	1A	00887		LD	A, (DE)	
0656	ED A1	00888		CPI		/ CHECK 2ND BYTE
0658	20 21	00889		JR	NZ, CHANG2	/ JUMP IF DIFF
		00890				
065A	13	00891	R2:	INC	DE	
065B	13	00892		INC	HL	/ SKIP SPACE
065C	FD 13	00893		INC	IX	/ INC COUNTER
065E	EA 064D	00894		JP	PE, L7	/ NEXT BYTE
		00895				
0661	D5	00896		PUSH	DE	
0662	11 0038	00897		LD	DE, 38	
0665	19	00898		ADD	HL, DE	/ POINT TO NEXT LINE
0666	D1	00899		POP	DE	
		00900				
0667	3A 0675	00901		LD	A, (CNT)	



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066A 3D 00902 DEC A ; DEC OUTER COUNTER
066E E2 067E 00903 LD (CNT), A
066E 10 D1 00904 JR NZ, L3
00905 ;
0670 E1 00906 POP HL
0671 D1 00907 POP DE
0672 C1 00908 POP BC
0673 F1 00909 POP AF
0674 CF 00910 RET
00911 ;
0675 00912 CNT. DEFB 1
00913 ;
00914 ; THIS ROUTINE CHANGES THE V-RAM BYTES TO REVERSE
00915 ; VIDEO.
00916 ;
0676 13 00917 CHANZA: INC DE
0677 FD 13 00918 INC IY
0678 ED A1 00919 CPI ; DUMMY STATEMENTS
00920 ;
067B FD 00921 CHANG2: PUSH AF
067C CB 00922 PUSH BC
067D CB FB 00923 SET 7, B ; FLAG
067F 1E 02 00924 JR N12
00925 ;
0681 FD 00926 CHANG4: PUSH AF
0682 CB 00927 PUSH BC
0683 DE 00928 N12: PUSH DE
0684 E5 00929 PUSH HL
00930 ;
0685 FD E5 00931 PUSH IY
0687 E1 00932 POP HL ; HL IS DISPLACEMENT
00933 ;
0688 CB 3D 00934 SRL L ; DIVIDE BY 2
068A CB 73 00935 BIT 7, B ; CHECK FLAG
068C 10 02 00936 JR NZ, N3
068E CB 3D 00937 SRL L ; DIVIDE BY 2
0690 7D 00938 N8: LD A, L ; STORE # SPACES
0691 CB 15 00939 SLA L ; MULTIPLY BY 2
0693 CB 73 00940 BIT 7, B ; CHECK FLAG
0695 10 02 00941 JR NZ, N9
0697 CB 15 00942 SLA L ; MULT BY 2
0699 E5 00943 N9: ADD A, L ; ADD IN SPACES
069A 6F 00944 LD L, A
069B DD E5 00945 PUSH IX ; GET START ADDRESS
069D D1 00946 POP DE
069E 1F 00947 ADD HL, DE ; ADDRESS IN V-RAM
069F CB 73 00948 BIT 7, B
06A1 10 0E 00949 JR NZ, N10
00950 ;
06A3 06 04 00951 LD B, 4
06A5 CB FE 00952 L6: SET 7, (HL) ; REVERSE IT
06A7 23 00953 INC HL
06A8 10 FB 00954 DJNZ L6
00955 ;
06AA E1 00956 POP HL
06AB D1 00957 POP DE

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06AC	01	00958	POP	BC	
06AD	F1	00959	POP	AF	
06AE	13 0620	00960	JP	R1	
		00961			
06B1	06 02	00962 N10.	LD	B, 2	
06B2	1B FE	00963 N11.	SET	7, (HL)	REVERSE IT
06B3	13	00964	INC	HL	
06B4	10 FE	00965	DJNZ	N11	
		00966			
06B8	E1	00967	POP	HL	
06B9	D1	00968	POP	DE	
06BA	D1	00969	POP	BC	
06BB	F1	00970	POP	AF	
06BC	1B 0C	00971	JR	R2	
		00972			
		00973	CIC0 READS CHARACTERS FROM THE KEYBOARD (IN POLLED		
		00974	FASHION) AND STORES THEM IN KEYIN. THIS ACTION IS		
		00975	TERMINATED UPON RECEIVING A WORD OF THE 25TH		
		00976	CHARACTER (WHICH CAUSES AN INPUT BUFFER OVERFLOW).		
		00977	THE CHARACTERS INPUTTED ARE ECHOED ON THE SCREEN		
		00978	AT THE LOCATION POINTED TO BY HL.		
		00979			
06BE	FB	00980 CIC0:	PUSH	AF	
06BF	CB	00981	PUSH	BC	
06C0	EB	00982	PUSH	DE	
06C1	EB	00983	PUSH	HL	
06C2	ED EB	00984	PUSH	IX	
		00985			
		00986			
06C4	DE 21	00987	LD	IX, KEYIN	
06C6	07BF				
06C8	1E 1A	00988	LD	E, 1A	OVERFLOW COUNTER
		00989			
06CA	EB 14	00990 CIC01:	IN	A, (POLL)	READ STATUS
06CB	0B 6F	00991	BIT	S, A	
06CC	20 FA	00992	JR	NZ, CIC01	IF SO CHECK AGAIN
06CD	EB 0E	00993	IN	A, (KYBD)	GET INPUT
06CE	FE 08	00994	CP	BS	BACKSPACE?
06D4	28 19	00995	JR	Z, BCKUP	IF SO JUMP
		00996			
06D8	FE 0D	00997 CIC02:	CP	CR	CR?
06D9	0A 077E	00998	JP	Z, DONE	IF SO DONE
06DB	47	00999	LD	B, A	SAVE INPUT
06DC	1B	01000	DEC	E	DEC OVERFLOW COUNT
06DD	2B 53	01001	JR	Z, OVER	JUMP IF OVERFLOW
		01002			
06DF	05	01003	PUSH	BC	
06E0	EB	01004	PUSH	HL	
06E1	3A 00BD	01005	LD	A, (COURSE+3)	
06E4	3C	01006	INC	A	INC CURSOR POSITION
06E5	32 00BD	01007	LD	(COURSE+3), A	
06E7	20 07	01008	JR	NZ, N34	
		01009			
06EA	3A 00BB	01010	LD	A, (COURSE+1)	
06EB	3C	01011	INC	A	INC CURSOR POSITION
06ED	32 00BB	01012	LD	(COURSE+1), A	(HIGH BYTE)

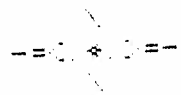
06F1	CB	000E	01013		CALL	CURSOR	
06F4	E1		01014	N34	POP	HL	
06F5	C1		01015		POP	BC	
			01017				
06F8	DD	70 00	01018		LD	(IX), B	PUT IN KEYIN
06F9	ED	13	01019		INC	IX	
06FE	70		01020		LD	(HL), B	PUT ON SCREEN
06FC	E3		01021		INC	HL	
06FD	1B	CB	01022		JR	CIC01	
			01023				
06FF	E3		01024	BACKUP:	PUSH	HL	
0700	C5		01025		PUSH	BC	
0701	AF		01026		XOR	A	ZERO CARRY FLAG
0702	DD	E3	01027		PUSH	IX	COPY IX TO BC
0704	C1		01028		POP	BC	
0705	11	078F	01029		LD	HL, KEYIN	START ADDRESS
0708	ED	42	01030		SBC	HL, BC	CHECK FOR UNDERFLOW
070A	20	04	01031		JR	NZ, BCK1	CONT. IF NZ
			01032				
070C	C1		01033		POP	BC	
070D	E1		01034		POP	HL	
070E	1B	BA	01035		JR	CIC01	RETURN
			01036				
0710	3A	00DD	01037	BCK1	LD	A, (CURSES+3)	
0713	FE	00	01038		CP	0	SET FLAGS
0715	20	09	01039		JR	NZ, N36	
			01040				
0717	47		01041		LD	B, A	
0718	3A	00DB	01042		LD	A, (CURSES+1)	
071B	3D		01043		DEC	A	DEC HIGH BYTE
071C	32	00DB	01044		LD	(CURSES+1), A	
071F	78		01045		LD	A, B	
			01046				
0720	3D		01047	N36	DEC	A	BACK-UP CURSOR
0721	32	00DD	01048		LD	(CURSES+3), A	
0724	3D	00DB	01049		CALL	CURSOR	
0727	C1		01050		POP	BC	
0728	E1		01051		POP	HL	
0729	3E	20	01052		LD	A, BLANK	BLANK-OUT CHAR
072E	77		01053		LD	(HL), A	BACK-UP
0730	3D	1B	01054		DEC	IX	POINTERS
073E	2E		01055		DEC	HL	INC COUNTER
073F	1C		01056		INC	E	
0730	1B	98	01057		JR	CIC01	
			01058				
0732	3D	E1	01059	OVER:	POP	IX	
0734	E1		01060		POP	HL	
0735	E3		01061		PUSH	HL	START OF TEXT
0738	3D	E3	01062		PUSH	IX	
0739	11	07AF	01063		LD	DE, MESS1	MESSAGE ADDRESS
073B	E3		01064		EX	DE, HL	LENGTH
073D	C1	0019	01065		LD	BC, 19	WRITE IT
073F	ED	B0	01066		LDIR		
0741	EE		01067		EX	DE, HL	
			01068				

0742	A7		01065	AND	A		ZERO CARRY FLAG
0743	11	0019	01070	LD	DE, 19		
0744	ED	52	01071	SBC	HL, DE		RESET SCREEN ADDRESS
0745	ED	31	01072	LD	IX, KEYIN		RESET BUFFER
0746	075F						
			01073				
0740	EE	14	01074	IN	A, (POLL)		READ STATUS
0742	EE	5F	01075	BIT	B, A		
0750	20	7A	01076	JR	NZ, N31		IF BC CHECK AGAIN
			01077				
0751	21	001F	01078	LD	BC, 1F		LENGTH
0753	11	070B	01079	LD	DE, BLNK		BLANKS
0755	EE		01080	EX	DE, HL		
0757	ED	40	01081	LDI			PUT IT ON SCREEN
0758	1E		01082	DEC	HL		NEXT BLANK
075C	EA	0759	01083	JF	PE, N32		IF NOT DONE JUMP
075F	EE		01084	EX	DE, HL		
			01085				
0760	A7		01086	AND	A		ZERO CARRY FLAG
0761	11	0019	01087	LD	DE, 19		
0764	ED	52	01088	SBC	HL, DE		RESET SCREEN ADDRESS
0766	1E	1A	01089	LD	E, 1A		RESET COUNTER
			01090				
0768	3A	00ED	01091	LD	A, (COURSE+3)		
076E	E6	19	01092	SUB	19		
076E	32	00ED	01093	LD	(COURSE+3), A		RESET CURSOR
0770	30	07	01094	JR	NC, N35		
			01095				
0772	3A	00DB	01096	LD	A, (COURSE+1)		
0775	3D		01097	DEC	A		RESET CURSOR (HIGH)
0776	32	00DB	01098	LD	(COURSE+1), A		
			01099				
077F	DE	0E	01100	IN	A, (KEYB)		GET INPUT
077E	23	04D6	01101	JP	C1002		
			01102				
077E	3E	31	01103	LD	A, CURSL		PUT CURSOR
0780	32	00ED	01104	LD	(COURSE+3), A		ADDRESS BACK
			01105				
0783	3E	0B	01106	LD	A, CR		
0785	ED	77 00	01107	LD	(IX), A		INSERT CURSOR
			01108				
078E	ED	E1	01109	POP	IX		
078A	E1		01110	POP	HL		
078E	31		01111	POP	DE		
0790	21		01112	POP	BC		
079E	F1		01113	POP	AF		
079E	09		01114	RET			
			01115				
079F			01116	DEFS	20		
07AF	45	4E 50	01117	DEFS	INPUT OVERFLOW RE-ENTER		
07B2	53	54 20					
07B3	4F	56 43					
07B8	52	46 4C					
07BB	4F	57 20					
07BE	52	43 2D					
07C1	45	4E 54					

0704 45 21 20  
0707 20  
0708 20

01118 BLK... DEFE  
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END



MACROS

SYMBOLS

ACONV	04E2E	ALOC	0500	ANAL1	04F4	ANAL2	04F0	ASCII	042A
AK	0438	BANKS	051E	BANKST	0501	BANKW	0529	BCK1	0710
BACKP	04FF	BEFOR	F7B0	BITS	0322	BLANK	0020	BLNK	0708
BLDF	0441	BS	0008	CHANZA	0676	CHANG2	067E	CHANG4	0681
CODE	06BE	C1001	060A	C1002	06D6	CNEXT	03A9	CNT	0675
CR	000D	CRSOUT	00D0	CTRL	045E	CURSES	00DA	CUREL	0082
CURSER	00CE	DIBASS	0130*	DLET	04DE	DONE	077E	F1	0363
FILE	0378	F2	0345	F3	0379	FDDN	039F	FDDNE	03A2
INTOP	0333	FIXIT	05E2	FLOOP	0123	FORDFM	0113	GOBAK	0335
ERE	0176	HEX	011D*	INFO	0380*	INSIDE	03DC	KEYIN	078F
YED	000E	L1	0549	L2	0570	L3	058B	L4	0626
S	0623	L6	06A5	L7	064D	L8	0443	LENGTH	0118*
LINE	0162*	LOOP1	0168	MDISP	0000	MEMLOC	FB82	MEB31	07AF
QV4	0455	N1	0276	N10	06B1	N11	06B3	N12	0683
Q	02E3	N31	0740	N32	0759	N34	06F1	N35	0779
Q6	0720	N4	02D3	N7	0599	N7A	059C	N8	0690
Q	0699	NAMES	02F7	NFLAG	0189	NIL	FFFF	NONFR	044A
ORMAL	014D	NULL	0000	OFF	0534	ONE	032A	OPCODE	0121*
RGEND	04FC*	OVER	0732	FBITS	031E	POLL	0014	PRESAV	0544*
VT	0320	R1	062D	R14BIT	FA12	R2	065A	REGPC	038E*
EGS	019C	REGSAV	055D*	REGSP	0267*	REV	0538	REVERB	0148
EVID	0E30	REVMEM	0600	RSTATE	0178	SAVE	057F	SAVIT	05BC
GREEN	0092	SRLOC	0002	SRTOP	F300	SETVID	0510	SFLAGS	F345
ROT	0304	SRES	01F0	SRESS	F382	STHLOC	FB02	TEXTUP	00E0
ROUND	0100	TIN	012F	TNEXT	00F8	TXRET	0172	TXTF	00ED
DEED	00ED	VIDOFF	0012	VIDSET	051F	VREG	0000	VWORD	0001
FE	00B7	WITHIN	03FA	WNEXT	0400	WRET	0427		

0 FATAL ERROR(S)

00001  
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--(0)--

DISASB IS A DISASSEMBLER SUBROUTINE. IT WORKS ON  
ONE INSTRUCTION PER CALL. IT EXPECTS THE ABSOLUTE  
ADDRESS OF THE INSTRUCTION TO BE PASSED IN THE HL  
REGISTER PAIR.  
THE DISASSEMBLED TEXT IS WRITTEN TO AN EXTERNALLY  
ACCESSIBLE BUFFER CALLED LINE.  
ON RETURN, HL WILL POINT TO THE NEXT INSTRUCTION.  
BY BILL MACLEOD 1980:X:30  
LAST UPDATED 08-08-81  
  
THIS FILE HAS THE UTILITY SUBROUTINES  
ORIGINALLY IN DAS, AS WELL AS THE INFO  
ROUTINE.

1010

RADIX

16  
ENTRY DISASB  
  
ENTRY BYTE  
ENTRY HEX  
ENTRY INSRET  
ENTRY INSTR  
ENTRY LENGTH  
ENTRY LINE  
ENTRY OPCODE  
ENTRY OPRNDS  
  
EXTRN BANKSW

00ED  
001Z

VIDEO  
VIDOFF

EQU OED  
EQU 12

; SWITCH IN V-RAM CODE  
; SWITCH V-RAM OUT CODE

NEXPT

MACRO  
POP HL  
PUSH HL  
INC HL

; NEXT POINT OF REF COUNTER

FUT

MACRO CHAR  
LD A, CHAR  
LD (DE), A  
INC DE  
ENDM

--(0)--

PAGE

```

0000
0004B -----
0004F -----
00050 DEFINITIONS AND DECLARATIONS
00051 -----
00052 -----
00053 BYTE: DEFS 1 ; LEAVE THE INSTRUCTION HERE
00054 LENGTH: DEFS 2 ; USABLE AS 1 OR 2 BYTES
00055 ;
00056 LINE: DEFS 1 ; OUTPUT LINE 50 CHARACTERS LONG
00057 LCON: DEFS 4 ; FIRST FIELD IN "LINE"
00058 SP1: DEFS 2 ;
00059 CODE: DEFS 00 ;
00060 OPCODE: DEFS 8 ;
00061 OPANDS: DEFS 17 ; LAST FIELD IN "LINE"
00062 ;
00063 HLMB: DEFM "HL, "
00064 DECM: DEFM "DEC "
00065 INCM: DEFM "INC "
00066 NOPM: DEFM "NOP "
00067 EXM: DEFM "EXAF, AF "
00068 ;
00069 DJNZM: DEFM "DJNZ "
00070 JRM: DEFM "JR "
00071 LTAB1: DEFM "RRCARRA CPL CCF "
00072 LTAB2: DEFM "RLCARLA DAA SCF "
00073 CALLM: DEFM "CALL "
00074 PUSHM: DEFM "PUSH "
00075 POPM: DEFM "POP "
00076 PLM: DEFM "RET EXX "
00077 JPM: DEFM "JP ."
00078 LDM: DEFM "LD ."
00079 HLIND: DEFM "(HL)SP, HL. "

```



0092	29	33	30				
0093	20	43	40				
0094	1E						
0095	31	33	34	00080	RETM.	DEFM	"RST"
0096	20	43		00081	INTERM.	DEFM	"I"
0097	43	4E	20	00082	INH.	DEFM	"IN"
0098	4F	33	34	00083	OUTM.	DEFM	"OUT"
0099	33	30	20	00084	SPHLM.	DEFM	"SP, (HL)"
009A	1F						
009B	44	4E	20	00085	DEHLM.	DEFM	"DE, HL"
009C	43	40					
009D	30			00086	DIBFX.	DEFB	00
009E	49	2E		00087	XBLF.	DEFM	"I-"
009F				00088	DBUF.	DEFS	3
00A0	0E	00		00089	CBBUF.	DEFB	0CB, 00
00A1	41	43	34	00090	TWITAB.	DEFM	"BIT RES SET"
00A2	20	32	43				
00A3	33	20	33				
00A4	43	34					
00A5	31	40	43	00091	BHTAB.	DEFM	"RLC RRC RL RR BLA BRA ****BRL"
00A6	20	32	32				
00A7	43	20	32				
00A8	40	20	20				
00A9	32	32	20				
00AA	20	33	40				
00AB	41	20	33				
00AC	32	41	20				
00AD	2A	2A	2A				
00AE	2A	33	32				
00AF	40						
00B0	43	41	40	00092	HLTM.	DEFM	"HALT"
00B1	34						
00B2				00093	NBUF.	DEFS	4
00B3	41	20		00094	AMES.	DEFM	"A"
00B4	41	44	44	00095	LOGTAB.	DEFM	"ADD ADC SUB SBC AND XOR OR CP"
00B5	20	41	44				
00B6	43	20	33				
00B7	33	42	20				
00B8	33	42	43				
00B9	20	41	4E				
00BA	44	20	33				
0101	4F	32	20				
0104	4F	32	20				
0107	20	43	30				
010A	20	20					
010C	42	43	44	00096	RNAMES.	DEFM	"BCDEHL"
010F	43	43	40				
0112				00097	RNAM.	DEFS	2, CAN BE "SP", "AF", OR "*A"
0114	4E	3A	3A	00098	CONDXM.	DEFM	"NZZ. NCC. POPEP. M."
0117	2E	4E	43				
011A	43	2E	30				
011D	4F	30	43				
0120	30	2E	4D				
0123	1E						
				00099		PAGE	

0124						
0124	FE	00100	DIEABS.	PUSH	AF	
0125	DF	00101		PUSH	BC	
0126	7E	00102		LD	A, (HL)	
0127	12 0000	00103		LD	(BYTE), A	
0128	CD 010E	00104		CALL	INFO	
012E	36 03	00105		AND	03	
012F	3C	00106		INC	A	LENGTH CAN BE USED AS
0130	12 0001	00107		LD	(LENGTH), A	EITHER EIGHT BITS
0133	AF	00108		XOR	A	
0134	32 0002	00109		LD	(LENGTH+1), A	OR SIXTEEN BITS
0137	CD 0137	00110		CALL	INSTR	
013A	CD 0145	00111		CALL	HEX	
013D	ED 4E	00112		LD	BC, (LENGTH)	
013F	0001					
0141	09	00113		ADD	HL, BC	
0142	01	00114		POP	BC	
0143	F1	00115		POP	AF	
0144	09	00116		RET		
		00117				
		00118				
		00119	PAGE			

--(0)--

```

0145
0145 DE 00120
0146 EF 00121 HEX. PUSH DE
0147 FF 00122 PUSH BC
0148 EF 00123 PUSH AF
0149 EF 00124 PUSH IX
014A EF 00125 PUSH HL
014B EF 00126
014C EF 00127 LD A,VIDOFF SWITCH OUT V-RAM
014D CB 0000+ CALL BANKSW
014E FC 00130 LD A,H
014F CB 072D CALL ACONVI
0150 ED 58 LD (LOCN),DE PUT ADDR IN LOCN FIELD:
0151 0004 LD A,L
0152 7D CALL ADDNVI
0153 CB 071D LD (LOCN+2),DE
0154 ED 58 LD A,L
0155 0004 LD IX,CODE OBJECT FIELD
0156 ED 21 00136 LD A,(LENGTH)
0157 000A LD B,A
0158 BA 0001 00137 LD A,(HL) WRITE OBJECT CODE
0159 CF 00138 CALL ACONVI
0160 FE 00139 CORE LD (IX),E
0161 FE 171E 00140 LD (IX-1),D
0162 FE 171E 00141 INC HL
0163 FE 171E 00142 INC IX
0164 FE 171E 00143 INC IX
0165 FE 171E 00144 INC IX
0166 FE 171E 00145 INC IX
0167 FE 171E 00146 INC IX
0168 FE 171E 00147 CORE COUNTING INSTRUCTION BYTES
0169 FE 171E 00148
0170 FE 171E 00149 LD A,VIDON SWITCH V-RAM IN
0171 FE 171E+ CALL BANKSW
0172 FE 171E
0173 FE 171E
0174 FE 171E
0175 FE 171E
0176 FE 171E
0177 FE 171E
0178 FE 171E
0179 FE 171E
0180 E1 00152 POP HL
0181 ED E1 00153 POP IX
0182 E1 00154 POP AF RESTORE CALLERS REGISTERS
0183 E1 00155 POP BC
0184 E1 00156 POP DE
0185 E1 00157 RET AND RETURN
0186 CF 00158
0187 00159
0188 00160 PAGE

```

-----  
THIS ROUTINE PUTS CHARACTERS  
INTO "LOCATION" AND "OBJECT"  
FIELDS OF THE OUTPUT LINE.  
-----

SAVE CALLERS REGISTERS

SWITCH OUT V-RAM

PUT ADDR IN LOCN FIELD:

OBJECT FIELD

WRITE OBJECT CODE

COUNTING INSTRUCTION BYTES

SWITCH V-RAM IN

RESTORE CALLERS REGISTERS

AND RETURN

--(0)--

PAGE

```

0187
0187 05      00161
0188 05      00162 INSTR.  PUSH  DE      , EXPECTS HL TO POINT AT
0189 05      00163      PUSH  BC      , INSTRUCTION IN CORE.
018A 05      00164      PUSH  AF      , WRITES TO "SOURCE" FIELD
018B 05      00165      PUSH  HL      , OF OUTPUT LINE.
018E 21 0003 00167      LD    HL, LINE
018E 3A 20    00168      LD    (HL), 20      , FIRST BLANK OUT
0190 11 0004 00169      LD    DE, LINE-1    , OUTPUT LINE
0193 01 0031 00170      LD    BC, 31
0195 ED 50    00171      LDIR
0198 11 0016 00172      LD    DE, OP00DE    , POINT AT SOURCE FIELD
019E 3A 0000 00173
019E 05 ED    00174      LD    A, (BYTE)
01A0 05 ED    00175      CP    0ED
01A0 05 CA 0F10 00176      JP    Z, SFECL
01A3 05 CB    00177      CP    0CE
01A3 05 CA 0E20 00178      JP    Z, TWIDL
01A8 05 DB    00179      CP    0DD
01AD 05 CA 0E44 00180      JP    Z, INDEXD
01AD 05 EB    00181      CP    0FD
01AF 05 CA 0E44 00182      JP    Z, INDEXD
00183
00184 :
00185 :
00186 :
00187 :

```

FALL THROUGH TO THE GARDEN VARIETY INSTR-  
UCTIONS. THESE ARE THE ONES WE CAN IDENTIFY  
BY LOOKING AT ONLY THE FIRST BYTE.  
PAGE

01E1

01E2 B4 20  
 01E4 FE 20  
 01E6 0A 01FE  
 01E9 FE 40  
 01EB 1A 0A3E  
 01EE FE 30  
 01F0 1A 0A40  
 01F3 1B 01F2

01C4 E1  
 01C7 F1  
 01C8 01  
 01C9 01  
 01CA 1F

001EE  
 001EF  
 001F0  
 001F1  
 001F2  
 001F3  
 001F4  
 001F5  
 001F6  
 001F7  
 001F8  
 001F9  
 00200  
 00201  
 00202  
 00203  
 00204  
 00205  
 00206  
 00207  
 00208  
 00209

INSRET.

HERE WE DEAL WITH BARDEN VARIETY INSTRUCTIONS  
 AND 000 ;CLASSIFY BY 1ST 2 BITS  
 JP 00  
 JP Z, ARITH  
 JP 40  
 JP Z, LOADS  
 JP 30  
 JP Z, LOGIC  
 JP HIQTR ;GO DO HIGH QUARTER OF MAP

POP HL  
 POP AF  
 POP BC  
 POP DE  
 RET

== (0) ==  
 ; JUMP HERE FOR UNIFORM RETURN

==(<\*>)==

PAGE

010B

```

00210 / INFO ROUTINE 30.VII.14
00211 / RETURNS A BYTE OF INFORMATION
00212 / ABOUT THE INSTRUCTION
00213 / POINTED TO BY THE HL REGISTER PAIR.
00214 / THE INFORMATION BYTE
00215 / IS RETURNED IN THE A REGISTER.
00216 /
00217 / ***** BITS RETURNED. *****
00218 /
00219 / BIT 7 : I/O FLAG
00220 / BIT 6 : AFFECTS P REGISTER
00221 / BIT 5 : CONDITIONAL INSTRUCTION
00222 / BIT 4 : CHANGES PROGRAM COUNTER
00223 / BIT 3 : CHANGES SOME REGISTER
00224 / BIT 2 : CHANGES MEMORY
00225 / BIT 1 : HIGH BIT OF DIMINISHED LENGTH
00226 / BIT 0 : LOW BIT OF DIMINISHED LENGTH
00227 / (LENGTH-1)
00228 / *****
00229 /
00230 / ENTRY INFO
00231 / RADIX 16
00232 / INFO: LD A,VIDOFF ; SWITCH OUT V-RAM
00233 / CALL BANKSW
00234 /
00235 / LD A,(HL)
00236 / PUSH HL
00237 / CP OCB ; SEE IF IT'S A BIT TWIDDLE
00238 / JP Z,TWIDDL
00239 / CP ODD ; INDEXED?
00240 / JP Z,NDEXD
00241 / CP OFD
00242 / JP Z,NDEXD
00243 / CP OED
00244 / JP Z,SPECIAL
00245 / *** TO GET HERE WE ARE POINTING AT ***
00246 / *** A GARDEN VARIETY INSTRUCTION ***
00247 / CALL LOOK
00248 / BACK: POP HL
00249 /
00250 / PUSH AF ; SAVE A
00251 / LD A,VIDEO ; SWITCH V-RAM IN
00252 / CALL BANKSW
00253 / POP AF
00254 /
00255 / RET
00256 / #EJECT

```

```

0010
010B 3E 12
010D CD 0000*
010E 7E
010F EE
0110 FE CE
0111 0A 0780*
0112 FE ED
0113 0A 0700*
0114 FE FD
0115 0A 0700*
0116 FE ED
0117 0A 07EE*
0118
0119 CD 0821*
011A E1
011B FB
011C FE ED
011D CD 0000*
011E F1
011F CP

```

```

01FE 01FE 0000 00267 HIGH  LD A,(BYTE) ; HERE DO HIGH QUARTER
01FF 0E 00 00268  OF INSTRUCTION SET.
0200 13 7E 00269  LR Z,CALLO ; CO=1=1ST BYTE OF FF
0201 0E 00 00270  AND 03 ; CLASSIFY BY LOW 2 BITS
0202 0E 00 00271  OF 00
0203 13 4F 00272  LR Z,CALRET
0204 0E 01 00273  OF 01
0205 0A 0263 00274  JP Z,POPUSH
0206 0E 03 00275  OF 03
0207 0A 02DC 00276  JP Z,RETJNK
0208 00277  FALL THROUGH TO IMMEDIATE ARITHMETIC & JPS
0209 0A 0000 00278  LD A,(BYTE)
0210 0E 57 00279  BIT Z,A
0211 0E 26 00280  JR Z,JPS
0212 00281  FALL THRU TO IMMEDIATE ARITHMETIC
0213 0E 67 00282  RES 6,A
0214 0E 0000 00283  LD (BYTE),A
0215 0E 0137 00284  CALL INSTR
0216 0E 28 00285  LD A,( )
0217 0E 001E 00286  LD HL,OPRND3
0218 0E 0017 00287  LD BC,17
0219 0E 81 00288  OFIR
0220 0E 0106 00289  JP NZ,INSRET ; ERROR RETURN; NO MATC
0221 0E 00290  DEC HL
0222 0E 00291  PUSH HL
0223 0E 00292  POP DE
0224 0E 00293  NEXPT
0225 0E 00294  POP HL
0226 0E 00295  PUSH HL
0227 0E 00296  INC HL
0228 0E 04EA 00297  CALL NUMB3
0229 0E 10 00298  LD A,( )
0230 0E 00299  LD (DE),A
0231 0E 00300  INC DE
0232 0E 00301  LD (DE),A
0233 0E 0106 00302  JP INSRET
0234 00303  --(0)--
0235 0E 0087 00304  LD HL,JPM
0236 0E 0002 00305  LD BC,2
0237 0E 50 00306  LDIR
0238 0E 0766 00307  CALL CONDX
0239 00308  PUT
0240 0E 20 00309  LD A,( )
0241 0E 00310  LD (DE),A
0242 0E 00311  INC DE
0243 00312  TARG. NEXPT ; FIND TARGET OF CALL OR JP
0244 0E 00313  POP HL
0245 0E 00314  PUSH HL
0246 0E 00315  INC HL
0247 0E 0608 00316  CALL NUMB16
0248 0E 0106 00317  JP INSRET
0249 00318  --(0)--
0250 00319
0251 00320

```

01160	BE	0000	008102	CALRET	LD	A, (BYTES)	/ DO CALLS & RETS.
01161	BE	0007	008103		LD	B, A	
01162	BE	0014	008104		LD	C, RETS	
01163	BE	001B	008105		LD	HL, CALLM	
01164	BE	0004	008106		LD	BC, 4	
01165	BE	00	008107		LD		
01166	BE	0742	008108		LD		
01167	BE		008109		LD		
01168	BE	00	008110		LD		
01169	BE		008111		LD		
01170	BE		008112		LD		
01171	BE	0F	008113		LD		
01172	BE		008114		LD		
01173	BE	007E	008115	RETS.	LD	HL, PLM	
01174	BE	0008	008116		LD	BC, 8	
01175	BE	80	008117		LDIR		
01176	BE	0786	008118		LD		
01177	BE	0116	008119		LD		
01178	BE		008120		LD		
01179	BE		008121		LD		
01180	BE	0480	008122		LD	DE, SPANDE	
01181	BE		008123		LD	TARG	
01182	BE		008124		LD		
01183	BE	0000	008125	PUSHB.	LD	A, (BYTES)	
01184	BE	0004	008126		LD	B, A	
01185	BE	0007	008127		LD	C, A	
01186	BE	0014	008128		LD	NZ, FLOOR.	
01187	BE	00	008129		LD	B, A	
01188	BE	0077	008130		LD	C, PIRB	
01189	BE	00	008131		LD	HL, PUSHM	
01190	BE	007E	008132		LD	PC, 7	
01191	BE	0004	008133		LD	HL, FORM	
01192	BE	80	008134		LDIR	BC, 4	
01193	BE	001E	008135		LD	DE, SPANDE	
01194	BE	80	008136		AND	BC	
01195	BE	00	008137		LD	B, 8	
01196	BE	00	008138		LD	C, 2	
01197	BE	8441	008139		LD	HL, PA	
01198	BE	0000	008140		LD	(RNAME), HL	
01199	BE	0000	008141		LD	HL, RNAMEE	
01200	BE	0000	008142		LD		
01201	BE	0000	008143		LD		
01202	BE		008144		LD		
01203	BE	80	008145	FLOOR.	AND	BC	
01204	BE		008146		AND		
01205	BE		008147		AND		
01206	BE	00	008148		LD	B, 0	
01207	BE		008149		LD	C, A	
01208	BE	007E	008150		LD	HL, PLM	
01209	BE	09	008151		ADD	HL, BC	
01210	BE	0008	008152		LD	BC, 8	
01211	BE	80	008153		LDIR		
01212	BE	0E	008154		LD	A, 14	

==(\*)\*==

/ GO LOOK IN TABLE.

/ SHIFT COUNT  
/ CHARACTER COUNT



00324	BB		00323	JF	(HL)
00325	BB		00323	JF	NZ, INCRET
00326	BB		00327	LD	DE, DEFRNDS
00327	BB		00328	LD	BC, 2
00328	BB		00328	LD	BC, 3
00329	BB		00329	ADD	HL, BC
00330	BB	00	00330	LD	HL, BC
00331	BB		00331	JF	(HL)
00332	BB	0106	00331	JF	INCRET
00333	BB		00333	LD	A
00334	BB		00334	LD	DE
00335	BB		00335	LD	(DE), A
00336	BB	0106	00336	JF	INCRET
00337	BB		00337	LD	A
00338	BB		00338	LD	DE
00339	BB		00339	LD	(DE), A
00340	BB	0106	00340	JF	INCRET
00341	BB	0000	00347	LD	A, (BYTE)
00342	BB	57	00348	BIT	Z, A
00343	BB	19	00349	JR	Z, JUNK
00344	BB	0049	00370	LD	HL, RSTW
00345	BB	0003	00371	LD	BC, 3
00346	BB	50	00371	LD	BC, 3
00347	BB	001E	00373	LD	DE, OFRND3
00348	BB	38	00374	AND	38
00349	BB	00B3	00375	LD	(DE), A
00350	BB	00B3	00375	LD	HL, DEFL
00351	BB	04E9	00377	CALL	NUMBER
00352	BB	0106	00378	JF	INCRET
00353	BB		00379	JF	(HL)
00354	BB	18	00381	JR	1, RSOFPND
00355	BB	0080	00382	LD	HL, INTERM
00356	BB	3F	00383	LD	Z, A
00357	BB	04	00384	JR	Z, DEAD
00358	BB	48	00385	LD	(HL), DE
00359	BB	02	00386	JF	INTEWR
00360	BB	44	00387	LD	(HL), D
00361	BB	0001	00388	LD	BC, 2
00362	BB	B0	00389	LD	BC, 2
00363	BB	0106	00390	JF	INCRET
00364	BB	03	00391	LD	BC, 2
00365	BB	03	00391	LD	BC, 2
00366	BB	20	00392	JR	NZ, NOTUP
00367	BB	0087	00393	LD	HL, JRM
00368	BB	01	00394	LD	BC, 2
00369	BB	ED	00395	LD	BC, 2
00370	BB	11	001E	LD	DE, OFRND3
00371	BB		00397	NEXPT	
00372	BB		00397	POP	HL
00373	BB		00397	PUSH	HL
00374	BB		00397	INC	HL
00375	BB	0408	00398	CALL	NUMB16
00376	BB	0106	00399	JF	INCRET
00377	BB	67	00400	LD	Z, A
00378	BB	4F	00401	JR	Z, EXS
00379	BB	5F	00402	BIT	Z, A
00380	BB	05	00403	JR	Z, OUTW
00381	BB	008E	00404	LD	HL, INM
00382	BB	03	00405	JR	INOUT
00383	BB	00A1	00406	LD	HL, OUTM
00384	BB	0003	00407	LD	BC, 3

.RESETS & OTHER JUNK

.OTHER JUNK

.SOME HAVE OPERANDS

RETURN

DISABL

INTEWR

RSOFPND

NOTLF

OUTW

INCW

0040	ED	BC	00410	LDIR	
0041	11	001E	00409	LD	DE, DFRNDS
0042	12	001E	00410	BIT	3, A
0043	11	00	00411	JR	DE, 0173
0044	11	00	00412	PUT	( )
0045	0E	00		LD	A, ( )
0046	12			LD	(DE), A
0047	13			INC	DE
			00413	NEXPT	
0048	E1			POP	HL
0049	0E			PUSH	HL
0050	0E			INC	HL
0051	ED	06EA	00414	CALL	NUMBS
			00415	PUT	( )
0052	0E	00		LD	A, ( )
0053	11			LD	(DE), A
0054	12			INC	DE
			00416	PUT	( )
0055	0E	00		LD	A, ( )
0056	11			LD	(DE), A
0057	13			INC	DE
0058	0E	00	00417	LD	A, (A)
0059	11		00418	LD	(DE), A
0060	12	0110	00419	JP	INSRET
0061	0E	00	00420	LD	A, (A)
0062	11		00421	LD	(DE), A
0063	12		00422	INC	DE
			00423	PUT	( )
0064	0E	00		LD	A, ( )
0065	12			LD	(DE), A
0066	13			INC	DE
			00424	PUT	( )
0067	0E	00		LD	A, ( )
0068	11			LD	(DE), A
0069	12			INC	DE
			00425	NEXPT	
0070	E1			POP	HL
0071	0E			PUSH	HL
0072	0E			INC	HL
0073	ED	06EA	00426	CALL	NUMBS
			00427	PUT	( )
0074	0E	00		LD	A, ( )
0075	12			LD	(DE), A
0076	13			INC	DE
0077	0E	0110	00428	JP	INSRET
0078	11	0010	00429	LD	HL, EXM
0079	01	0001	00430	LD	BC, 2
0080	ED	BC	00431	LDIR	
0081	11	001E	00432	LD	DE, DFRNDS
0082	0E	00	00433	BIT	3, A
0083	13	00	00434	JR	Z, SPHLI
0084	11	000E	00435	LD	HL, DEHLM
0085	01	0000	00436	LD	BC, 5
0086	ED	BC	00437	LDIR	
0087	13	00	00438	JR	EXRET
0088	E1	0004	00439	LD	HL, SPHLM

OUTD:

EXM.

SPHLI.

00440	LD	BC, 7	
00441	LDIR		
00442	JP	INSRET	
00443			
00444	LD	A, (BYTE)	
00445	AND	OF	/ FALL BETWEEN 00 & 3F.
00446	CP	01	/ THIS GROUP IS ONLY
00447	JP	Z, LOAD16	/ SLIGHTLY REGULAR.
00448	CP	03	
00449	JP	Z, INC16	
00450	CP	09	
00451	JP	Z, ADDHL	
00452	CP	0B	
00453	JP	Z, DEC16	
00454	AND	07	
00455	CP	07	
00456	JP	Z, LOOKA	
00457	CP	05	
00458	JP	Z, DECS	
00459	CP	04	
00460	JP	Z, INCS	
00461	CP	0	
00462	JP	Z, JRS	
00463			
00464			FALL THROUGH TO 8-BIT LOADS
00465			FELL THROUGH TO 8-BIT LOADS
00466	LD	HL, LDM	
00467	LD	BC, 2	
00468	LDIR		
00469	LD	DE, OPRNDS	/ DEST IS OPERAND FIELD
00470	LD	A, (BYTE)	
00471	BIT	Z, A	
00472	JR	Z, INDIS	/ FOR REGISTER INDIRECT
00473			
00474	CALL	REG38	/ GET REGISTER NAME
00475	PUT		/ PUT A COMMA ON "LINE"
00476	LD	A, 1, 1	
00477	LD	(DE), A	
00478	INC	DE	
00479	NEXPT		/ (REF COUNTER - 1) INTO HL PAIR
00480	POP	HL	
00481	PUSH	HL	
00482	INC	HL	
00483	CALL	NUMB3	/ WRITE IMMED N TO "LINE"
00484	JP	INSRET	/ TIDY UP AND RETURN.
00485			
00486			HERE DO REGISTER INDIRECT
00487	AND	OFO	/ ACCUMULATOR OR HL PAIR?
00488	CP	Z0	
00489	JR	Z, HLM	/ GO WRITE "HL"
00490	LD	A, "A"	
00491	LD	(DE), A	/ WRITE "A"
00492	INC	DE	
00493	JR	COMM	
00494	LD	HL, HLM3	

0400	01	0001	00480	LD	BC, 2	
0401	02	00	00481	LDIR		
0402	03	00	00482	PUT		, WRITE "HL"
0403	04	00		LD	A, "	, WRITE A COMMA
0404	05			LD	(DE), A	
0405	06			INC	DE	
0406	07		00483	PUT	"("	, WRITE A LEFT PARENTHESIS
0407	08	00		LD	A, "("	
0408	09			LD	(DE), A	
0409	10			INC	DE	
040A	11	0000	00484	LD	A, (BYTE)	, GET BACK INSTR BYTE
040B	12	00	00485	BIT	5, A	, IMMEDIATE OR REGISTER PAIR?
040C	13	00	00486	JR	Z, RG16	
040D	14		00487	NEXPT		, (REF COUNTER + 1) INTO HL PAIR:
040E	15			POP	HL	
040F	16			PUSH	HL	
0410	17			INC	HL	
0411	18	0010	00488	CALL	NUMB16	
0412	19	00	00489	JR	WCHWAY	
0413	20	0000	00500	CALL	RG16	, GET NAME OF REGISTER PAIR
0414	21	0000	00501	WCHWAY:	PUT	"")
0415	22			LD	A, ")"	, PUT A RIGHT PARENTHESIS
0416	23			LD	(DE), A	
0417	24	0000	00502	INC	DE	
0418	25	00	00503	LD	A, (BYTE)	
0419	26	0100	00504	BIT	3, A	, WHICH WAY DOES THE LOAD GO?
041A	27	00	00505	JP	NZ, INSRET	, TIDY UP & RETURN
041B	28		00506	LD	A, ","	, APPEND A COMMA
041C	29		00507	LD	(DE), A	
041D	30	0010	00508	INC	DE	
041E	31	00	00509	LDIR	HL, OPRNDS	
041F	32	00	00510	LDI		, LOAD A LETTER TO "LINE"
0420	33	00	00511	CP	(HL)	
0421	34	00	00512	JR	NZ, LTRLD	, NEXT CHAR A COMMA?
0422	35		00513	LD	A, "*"	
0423	36		00514	LD	(DE), A	
0424	37	0010	00515	INC	HL	
0425	38	00	00516	LD	DE, OPRNDS	
0426	39	00	00517	LDI		
0427	40	00	00518	CP	(HL)	
0428	41	00	00519	JR	NZ, REVRS	
0429	42		00520	LD	A, "	
042A	43	0000	00521	LD	(HL), A	
042B	44	00	00522	LD	BC, 8	
042C	45	0100	00523	LDIR		
			00524	JP	INSRET	, CLEAN UP & GO BACK
			00525			
			00526			
042D	46	0000	00527	LDM	HL, LDM	
042E	47	0000	00528	LD	BC, 2	
042F	48	00	00529	LDIR		
0430	49	0010	00530	LD	DE, OPRNDS	
0431	50	0000	00531	CALL	SS	, POINT AT OPERAND FIELD:
0432	51	00	00532	PUT	"	
0433	52	00		LD	A, "	

-00\*00-

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0450 12          LD      (DE), A
0451 13          INC     DE
                NEXPT   ; (REF COUNTER - 1) INTO HL PAIR
0452 E1          POP     HL
0453 E3          PUSH    HL
0454 E5          INC     HL
0455 0E 0603    00534 CALL    NUMB16
0456 03 0105    00535 JP      INSRET
                00536 ;
                00537 ;
                                ==(0)==
0457 21 0030    00538 INC16.  LD      HL, INCM      ; OPCODE IS "INC"
0458 13 03      00539          JR      GO
0459 21 0033    00540 DEC16.  LD      HL, DECM      ; OPCODE IS "DEC"
0460 01 0004    00541 GO:     LD      BC, 4
0461 ED 50      00542          LDIR     ; WRITE OPCODE
0462 11 001E    00543          LD      DE, OPRNDS  ; POINT AT OPERAND FIELD
0463 02 0605    00544          CALL   SS          ; WRITE OPERAND REG PAIR
0464 13 0105    00545          JP      INSRET
                00546 ;
                00547 ;
                                ==(0)==
                00548 ;
                00549 ; *****
00550 ; LOOKUP TABLES:
00551 ;
0465 3A 0000    00552 LOOKA.  LD      A, (BYTE)    ; GET BACK INSTR BYTE
0466 1E 0F      00553          BIT     3, A
0467 13 05      00554          JR      Z, LOOKA2
0468 21 0033    00555          LD      HL, LTAB1   ; LOOK IN TABLE 1
0469 13 03      00556          JR      LOOKA3
0470 21 0036    00557 LOOKA2: LD      HL, LTAB2   ; LOOK IN TABLE 2
0471 36 00      00558 LOOKA3: AND     OF0      ; MOST SIG. HEX DIGIT MATTERS
0472 1F        00559          RRA
0473 1F        00560          RRA      ; DIVIDE BY 4
0474 4F        00561          LD      C, A
0475 16 00      00562          LD      B, 0
0476 0F        00563          ADD     HL, BC      ; INDEX INTO TABLE
0477 01 0004    00564          LD      BC, 4      ; FOUR CHARACTERS PER ENTRY
0478 ED 50      00565          LDIR
0479 13 0105    00566          JP      INSRET    ; TIDY UP AND RETURN
                00567 ;
                00568 ;
                                ==(0)==
0480 21 0030    00569 ADDHL.  LD      HL, LOGTAB   ; OPCODE IS "ADD HL,"
0481 01 0004    00570          LD      BC, 4
0482 ED 50      00571          LDIR
0483 11 001E    00572          LD      DE, OPRNDS
0484 21 0033    00573          LD      HL, HLME3
0485 01 0003    00574          LD      BC, 3
0486 ED 50      00575          LDIR
0487 0E 0605    00576          CALL   SS          ; WRITE SOURCE-REGISTER PAIR
0488 03 0105    00577          JP      INSRET
                00578 ;
                00579 ;
                                ==(0)==
0489 21 0033    00580 DEC3.  LD      HL, DECM
0490 13 03      00581          JR      GO3
0491 21 0036    00582 INC3.  LD      HL, INCM
0492 01 0004    00583 GO3.   LD      BC, 4

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04C0	ED	B0	00584		LDIR		
04C2	11	001E	00585		LD		; WRITE "DEC" OR "INC"
04C5	3A	0000	00586		LD	DE, OPRNDS	
04C8	0D	06FF	00587		LD	A, (BYTE)	
04CB	03	0106	00588		CALL	REG38	; GET REGISTER FROM BITS 3-5
			00589		JP	INSRET	
			00590				
04CE	3A	0000	00591	JRS:	LD	A, (BYTE)	--(0)--
04D1	FE	00	00592		CP	00	
04D3	20	0E	00593		JR	NZ, JRS1	; HERE WE DEAL WITH
04D5	21	0040	00594		LD	HL, NOPM	; RELATIVE JUMPS.
04D8	01	0003	00595		LD	BC, 3	
04DB	ED	B0	00596		LDIR		
04DD	03	0106	00597		JP	INSRET	
			00598				
04E0	FE	08	00599	JRS1:	CP	08	
04E2	20	18	00600		JR	NZ, JRS2	
04E4	21	0043	00601		LD	HL, EXM	
04E7	01	0002	00602		LD	BC, 2	
04EA	ED	B0	00603		LDIR		
04EC	11	001E	00604		LD		; WRITE "EX"
04EF	01	0006	00605		LD	DE, OPRNDS	
04F2	ED	B0	00606		LD	BC, 6	
04F4	13	0106	00607		LDIR		; WRITE "AF, AF"
			00608		JP	INSRET	
04F7	FE	10	00609	JRS2:	CP	10	
04F9	20	14	00610		JR	NZ, JRS3	
04FB	21	004E	00611		LD	HL, DJNZM	
04FE	01	0005	00612		LD	BC, 5	
0501	ED	B0	00613		LDIR		
0503	11	001E	00614		LD		; WRITE "DJNZ"
			00615		NEXPT	DE, OPRNDS	
0508	E1				POP	HL	
0507	E5				PUSH	HL	
0508	E3				INC	HL	
050F	0D	06B8	00616		CALL	DISPLD	; FIGURE & WRITE TARGET
050C	13	0106	00617		JP	INSRET	
			00618				
050F	21	0050	00619	JRS3:	LD	HL, JRSM	
0512	01	0003	00620		LD	BC, 3	
0513	ED	B0	00621		LDIR		
0517	11	001E	00622		LD		; WRITE "JR"
051A	0B	3F	00623		LD	DE, OPRNDS	
051C	20	07	00624		BIT	3, A	; TEST SENSE OF CONDITION
			00625		JR	NZ, POSC	
051E	FE	4E			PUT	'N'	
0520	12				LD	A, 'N'	
0521	13				LD	(DE), A	
0522	3A	0000	00626		INC	DE	
0525	E6	30	00627	POSC:	LD	A, (BYTE)	; RESTORE A
0527	FE	10	00628		AND	30	
0529	28	10	00629		CP	10	
052B	FE	20	00630		JR	Z, JRDIS	
052D	28	04	00631		CP	20	
052F	FE	48	00632		JR	Z, JRZ	
0531	13	01	00633		LD	A, 'C'	
					JR	LDDE	; IT'S OR C...

0535	3E 3A	00634	JRZ:	LD	A, 'Z'	, IT'S JR Z...
0535	12	00635	LDDE:	LD	(DE), A	
0536	13	00636		INC	DE	
		00637		PUT	'Z'	
0537	3E 2C			LD	A, 'Z'	
0538	12			LD	(DE), A	
0538	13			INC	DE	
		00638	JRDIS:	NEXPT		
053E	E1			POP	HL	
053C	EE			PUSH	HL	
053D	23			INC	HL	
053E	2D 04B3	00639		CALL	DISPLC	, FIGURE & WRITE TARGET
0541	03 01C6	00640		JP	INSRET	
		00641				
0544	0B 6F	00642	INDEXD:	BIT	S, A	, HERE DO "INDEXED" INSTRUCTION?
0546	26 04	00643		JR	Z, XIND	, FIND OUT IF IT'S IX OR IY
0548	3E 39	00644		LD	A, 'Y'	
0549	1B 02	00645		JR	YIND	
054C	3E 33	00646	XIND:	LD	A, 'X'	
054E	3E 00E1	00647	YIND:	LD	(XBUF+1), A	, WRITE REGISTER NAME
		00648		NEXPT		
0551	E1			POP	HL	
0552	E3			PUSH	HL	
0553	13			INC	HL	
0554	7E	00649		LD	A, (HL)	
0555	36 F0	00650		AND	OFO	
0557	FE 20	00651		CP	Z0	, FIND OUT IF DISPLACEMENT BYTE
0559	13 11	00652		JR	Z, NODISP	, IS USED.
055B	FE E0	00653		CP	OEO	
055D	23 0D	00654		JR	Z, NODISP	
055F	7E	00655		LD	A, (HL)	
0560	E6 0F	00656		AND	OF	
0562	FE 09	00657		CP	O9	
0564	23 08	00658		JR	Z, NODISP	
0566	23	00659		INC	HL	, IF IT IS, IT'S THE 3D BYTE.
0567	7E	00660		LD	A, (HL)	
0569	3E 00E0	00661		LD	(DISPX), A	, REMEMBER DISPLACEMENT
056B	2B	00662		DEC	HL	, POINT AT 2ND BYTE.
		00663				
056C	7E	00664	NODISP:	LD	A, (HL)	
056E	FE 0B	00665		CP	0CB	, SEE IF IT'S A BIT TWIDDLE.
056F	20 09	00666		JR	NZ, NOTWID	, NOT A TWIDDLE
0571	23	00667		INC	HL	, IF IT IS, LOOK
0572	23	00668		INC	HL	, AT 4TH BYTE
0573	7E	00669		LD	A, (HL)	, THAT'S THE INSTRUCTION.
0574	3E 00E7	00670		LD	(CBBUF+1), A	
0577	21 00E6	00671		LD	HL, CBBUF	
		00672				
057A	32 0000	00673	NOTWID:	LD	(BYTE), A	
057D	0D 0187	00674		CALL	INSTR	, RECURSIVELY...
0580	3E 48	00675	AGAIN:	LD	A, 'H'	
0582	21 001E	00676		LD	HL, OPRNDS	
0585	01 0017	00677		LD	BC, 17	
0588	ED E1	00678	SEE:	CPIR		, LOOK FOR "HL."
058A	02 01C6	00679		JP	NZ, INSRET	, IF NO MATCH FOUND
058D	3E 4C	00680		LD	A, 'L'	

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058F BE          00681      CP          (HL)          ; MAKE SURE IT'S "HL"
0590 28 04      00682      JR          Z, WHOLE
0592 3E 48      00683      LD          A, "H"
0594 18 F2      00684      JR          SEE
0596 2B         00685      DEC        HL          ; WHOLE.
0597 E5         00686      PUSH       HL
0598 D1         00687      POP        DE
0599 21 00B1     00688      LD          HL, XBUF    ; POINT TO IT IN "LINE."
059C 01 0002     00689      LD          BC, 2
059F ED B0      00690      LDIR       ; REPLACE IT WITH "IX" OR "IY"
05A1 1B         00691      DEC        DE
05A2 D5         00692      PUSH       DE
05A3 3A 00B0     00693      LD          A, (DISPX)
05A6 FE 00      00694      CP          0          ; CHECK OUT INDEX DISPLACEMENT
05A8 C2 05AF     00695      JP         NZ, ONDEX
05AB E1         00696      POP        HL
05AC C3 0580     00697      JP         AGAIN      ; JUST IN CASE IT'S ADD IX, IX
                   00698
05AF 21 00B3     00699      LD          HL, DBUF    ; ONDEX:
05B2 F2 05BB     00700      JP         P, POS      ; IF DISPLACEMENT IS POSITIVE
05B5 ED 44      00701      NEG        ; ELSE GET ABSOLUTE VALUE
05B7 36 2D      00702      LD          (HL), MINUS ; AND PREFIX A "-"
05B9 18 02      00703      JR         BUMP
05BB 36 2B      00704      LD          (HL), PLUS  ; PREFIX A "+"
05BD CD 072D     00705      CALL      BUMP:
05C0 ED 53      00706      LD          (DBUF+1), DE
05C2 00B4
05C4 11 0034     00707      LD          DE, OPRNDS+16
05C7 21 0031     00708      LD          HL, OPRNDS+13
05CA C1         00709      POP        BC
05CB E5         00710      PUSH       HL
05CC 97         00711      SUB        A
05CD 32 00B0     00712      LD          (DISPX), A  ; ZERO FOR NEXT TIME
05D0 ED 42      00713      SBC        HL, BC      ; BYTE COUNT FOR LDDR
05D2 E5         00714      PUSH       HL
05D3 C1         00715      POP        BC
05D4 E1         00716      POP        HL
05D5 ED B8      00717      LDDR      ; THE VERY LDDR ITSELF
05D7 21 00E5     00718      LD          HL, DBUF+2
05DA 01 0003     00719      LD          BC, 3
05DD ED B8      00720      LDDR
05DF C3 01C6     00721      JP         INSRET
                   00722 ;
002B          00723      PLUS      EQU         "+/"
002D          00724      MINUS     EQU         "-/"
                   00725 ;
                   00726 ;
                   00727 ;
                   00728      TWIDL:   NEXPT     ; HERE DO THE BIT TWIDDLES
05E2 E1         00729      POP        HL
05E3 E5         00730      PUSH       HL
05E4 23         00731      INC        HL
05E5 7E         00732      LD          A, (HL)    ; (A VERY REGULAR GROUP)
05E6 32 0000     00733      LD          (BYTE), A
05E7 FE 40      00734      CP          40

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05EB	38 32	00732	JR	C, SHROTA	GO DO SHIFTS & ROTATE
05ED	E6 C0	00733	AND	OCO	
05EF	06 06	00734	LD	B, 6	
05F1	1F	00735 SH6T:	RRA		
05F2	10 FD	00736	DJNZ	SH6T	
05F4	3D	00737	DEC	A	
05F5	17	00738	RLA		
05F6	17	00739	RLA		
05F7	4F	00740	LD	C, A	
05FB	21 00B8	00741	LD	HL, TWITAB	
05FB	09	00742	ADD	HL, BC	
05FC	01 0003	00743	LD	BC, 3	
05FF	ED B0	00744	LDIR		
		00745			
0601	3A 0000	00746	LD	A, (BYTE)	
0604	E6 38	00747	AND	38	
0606	1F	00748	RRA		
0607	1F	00749	RRA		
060E	1F	00750	RRA		
060E	1F	00750	CALL	ACONVI	WHAT BITS ARE 11, PLEASE
060F	ED 072D	00751	LD	A, D	
060C	7A	00752	LD	DE, OPRNDS	
060D	11 001E	00753	LD	(DE), A	
0610	12	00754	LD	DE	
0611	13	00755	INC	DE	
		00756	PUT		
			LD	A, 7, 7	
0612	3E 2C		LD	(DE), A	
0614	12		LD	DE	
061E	13		INC	DE	
061E	3A 0000	00757 RSEND.	LD	A, (BYTE)	
061F	DE 0707	00758	CALL	REG38	WRITE REGISTER NAME
061F	13 0106	00759	JP	INSRET	
061F	E6 13	00760 SHROTA.	AND	38	
061F	1F	00761	RRA		
061F	1F	00762	LD	B, 0	
061F	1F	00763	LD	C, A	
061F	1F	00764	LD	HL, SHTAB	
061F	1F	00765	ADD	HL, BC	
061F	1F	00766	LD	BC, 3	
061F	1F	00767	LDIR		
061F	ED 2C	00768	LD	DE, OPRNDS	
061F	11 001E	00769	JR	RSEND	
061F	13 EB	00770			
061E	3A 0000	00771 LOADS.	LD	A, (BYTE)	
061E	FE 76	00772	CP	76	IS IT A HALT?
061E	20 0E	00773	JR	NZ, LDS	
061E	21 00E2	00774	LD	HL, HLTM	
061E	01 0004	00775	LD	BC, 4	
0640	ED B0	00776	LDIR		
0642	13 0106	00777	JP	INSRET	
		00778			
0645	21 008B	00779 LDS:	LD	HL, LDM	LOADS ARE EASY,
0648	01 0002	00780	LD	BC, 2	LOOK AT THE KARNAUGH MAP.
064B	ED B0	00781	LDIR		
064D	11 001E	00782	LD	DE, OPRNDS	
0650	CD 06FF	00783	CALL	REG38	DESTINATION REGISTER NAME
0653	3E 2C	00784	LD	A, 7, 7	

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0655' 12          00785      LD      (DE), A
0656' 13          00786      INC     DE
0657' 3A 0000'    00787      LD      A, (BYTE)
065A' CD 0707'    00788      CALL   REG8      ; SOURCE REGISTER NAME
065D' C3 01C6'    00789      JP      INSRET
                    00790 ;
                    00791 ;
                    00792 ;
                    00793 ;
                                \
                                --<O(*)O>--
                                /

0660' 3A 0000'    00794 LOGIC: LD      A, (BYTE)
0663' E6 38          00795      AND     38          ; LOOK AT BITS 3-5
0665' 06 00          00796      LD      B, 0
0667' 1F          00797      RRA
0668' 4F          00798      LD      C, A
0669' 21 00EC'    00799      LD      HL, LOGTAB
066C' 09          00800      ADD     HL, BC
066D' 01 0004      00801      LD      BC, 4
0670' ED B0          00802      LDIR                     ; WRITE THE OPCODE
0672' 11 001E'    00803      LD      DE, OPRNDS
0675' 3A 0000'    00804      LD      A, (BYTE)
0678' E6 BF          00805      AND     OBF
067A' FE 90          00806      CP      90
067C' DC 068F'    00807      CALL   C, EXPLCA      ; EXPLICIT ACCUMULATOR
067F' E6 F8          00808      AND     OF8
0681' FE 98          00809      CP      98
0683' CC 068F'    00810      CALL   Z, EXPLCA
0686' 3A 0000'    00811      LD      A, (BYTE)
0689' CD 0707'    00812      CALL   REG8      ; WRITE OPERAND REG
068C' C3 01C6'    00813      JP      INSRET
                    00814 ;
068F' 21 00EA'    00815 EXPLCA: LD      HL, AMES
0692' 01 0002      00816      LD      BC, 2
0695' ED B0          00817      LDIR                     ; WRITE EXPLICIT OPERAND
0697' C9          00818      RET
                    00819 ;
                    00820 ;
                    00821 ;
                    00822 ;
                                \
                                --<O(*)O>--
                                /
                    00823 ;
                    00824      PAGE

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0698

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00825 , #####
00826 ,
00827 , THIS MODULE COMPRISES SUBROUTINES CALLED BY THE
00828 , DISASSEMBLER DISASS.
00829 ,
00830 , #####

```

```

00831 ,
00832 35:    PUSH    HL          ;=====
00833        PUSH    BC          ;WRITES NAME OF REGISTER PAIR
00834        PUSH    AF          ;TO LINE BUFFER
00835        LD     A,(BYTE) ;=====
00836        AND    30
00837        LD     BC,0302
00838        LD     HL,"PS"
00839        LD     (RNAME),HL
00840        LD     HL,RNAME$
00841        CALL  LOOKUP
00842        POP   AF
00843        POP   BC
00844        POP   HL
00845        RET
00846        PAGE

```

```

0698  E5
0699  C5
069A  F5
069E  3A 0000
069E  E4 30
06A0  01 0302
06A3  21 3053
06A6  22 0112
06A9  21 0100
06AC  CD 0732
06AF  F1
06E0  C1
06E1  E1
06E2  CP

```

06B3					
06B3	7E	00847	DISPLO:	LD	A, (HL) ; =====
06B4	23	00848		INC	HL ; FIGURES DISPLACEMENT FOR
06B5	4F	00849		LD	C, A ; RELATIVE JUMPS; WRITES TARGET
06B6	06 08	00850		LD	B, 8 ; TO (DE). (TO "LINE")
06B9	0B 2F	00851	SHPROP.	BRA	A ; =====
06EA	10 FC	00852		DJNZ	SHPROP ; PROPAGATE SIGN BIT
06EC	47	00853		LD	B, A
06ED	09	00854		ADD	HL, BC
06EE	22 00E6	00855		LD	(NBUF), HL
06C1	21 00E6	00856		LD	HL, NBUF
06C4	CD 06C8	00857		CALL	NUMB16
06C7	C9	00858		RET	
		00859			
		00860			
06C8	25	00861	NUMB16.	PUSH	HL ; =====
06C9	05	00862		PUSH	BC ; WRITES NUMBER TO LINE BUFFER
06CA	F5	00863		PUSH	AF
06CB	D5	00864		PUSH	DE ; =====
06CC	7E	00865		LD	A, (HL)
06CD	CD 072D	00866		CALL	ACONVI
06D0	ED 53	00867		LD	(NBUF+2), DE
06D2	00E3				
06D4	28	00868		INC	HL
06D5	7E	00869		LD	A, (HL)
06D6	CD 072D	00870		CALL	ACONVI
06D9	ED 53	00871		LD	(NBUF), DE
06DB	00E6				
06DD	21 00E6	00872		LD	HL, NBUF
06E0	D1	00873		POP	DE
06E1	01 0004	00874		LD	BC, 4
06E4	ED 80	00875		LDIR	
06E6	F1	00876		POP	AF
06E7	01	00877		POP	BC
06E8	21	00878		POP	HL
06E9	C9	00879		RET	
		00880		PAGE	

```

06EA      E5      00881 NUMB8:  PUSH  HL      ;=====
06EB      7E      00882      LD    A,(HL) ;WRITES A SINGLE HEX BYTE
06EC      D5      00883      PUSH  DE      ;AS 2 ASCII CHARS TO (DE)
06ED      ED 072D  00884      CALL  ACONVI  ;=====
06F0      ED 53      00885      LD    (NBUF),DE
06F2      00E6
06F4      D1      00886      POP   DE
06F5      21 00E6  00887      LD    HL,NBUF
06F8      01 0002  00888      LD    BC,2
06FB      ED B0      00889      LDIR
06FD      E1      00890      POP   HL
06FE      C9      00891      RET
          00892 ;
06FF      F5      00893 REG38:  PUSH  AF      ;PICKS REGISTER CODE FROM
0700      26 38      00894      AND   38      ;BITS 3-5 OF ACCUMULATOR.
0702      1F      00895      RRA
0703      1F      00896      RRA
0704      1F      00897      RRA
0705      13 01      00898      JR    REG81
0707      F5      00899 REG8:   PUSH  AF      ;=====
0708      C5      00900 REG81:  PUSH  BC      ;EXPECTS 3-BIT CODE IN ACC
0709      E5      00901      PUSH  HL      ;WRITES REGISTER NAME TO (DE)
070A      21 412A  00902      LD    HL,"A*" ;=====
070D      22 0112  00903      LD    (RNAME),HL ;SET UP TABLE TAIL
0710      21 0100  00904      LD    HL,RNAME ;POINT TO TABLE
0713      E6 07      00905      AND   07
0715      4F      00906      LD    C,A
0716      06 00      00907      LD    B,0
0718      09      00908      ADD  HL,BC    ; INDEX INTO TABLE
0719      01 0001  00909      LD    BC,1
071C      3E 2A      00910      LD    A,"*"
071E      3E      00911      CP   (HL)
071F      10 06      00912      JR    NZ,REG8LD
0721      21 008F  00913      LD    HL,HLIND
0724      31 0004  00914      LD    BC,4
0727      ED B0      00915 REG8LD: LDIR
0729      E1      00916      POP   HL
072A      01      00917      POP   BC
072B      F1      00918      POP   AF
072C      C9      00919      RET
          00920 ;
          00921 ;
          00922 ;
          00923 ;
          00924 ;ACONVI TAKES BINARY IN ACCUMULATOR, RETURNS ASCII IN
          00925 ;THE DE PAIR IN LOW-HIGH ORDER.
          00926 ACONVI:  PUSH  HL
072E      21 0765  00927      LD    HL,ALOC
0731      77      00928      LD    (HL),A
0732      3E 33      00929      LD    A,33H
0734      ED 6F      00930      RLD
0736      ED 6F      00931      RLD
0738      FE 3A      00932      CP   3AH
073A      36 02      00933      JR    C,ANAJ11

```

WHERE "\*" IS REPLACED BY "(HL)"

--(0)--

0730	C6	07	00934		ADD	A, 7
073E	37		00935	ANAJI1:	LD	D, A
073F	7E		00936		LD	A, (HL)
0740	FE	3A	00937		CP	3AH
0742	38	02	00938		JR	C, ANAJI2
0744	C6	07	00939		ADD	A, 7
0746	5F		00940	ANAJI2:	LD	E, A
0747	E1		00941		POP	HL
0748	C9		00942		RET	
0749	E5		00943	ACONV:	PUSH	HL
074A	21	0765	00944		LD	HL, ALOC
074D	77		00945		LD	(HL), A
074E	3E	33	00946		LD	A, 33H
0750	ED	6F	00947		RLD	
0752	ED	6F	00948		RLD	
0754	FE	3A	00949		CP	3AH
0756	38	E6	00950		JR	C, ANAJI1
0758	C6	07	00951		ADD	A, 7
075A	57		00952	ANAJI1:	LD	D, A
075B	7E		00953		LD	A, (HL)
075C	FE	3A	00954		CP	3AH
075E	38	E6	00955		JR	C, ANAJI2
0760	C6	07	00956		ADD	A, 7
0762	5F		00957	ANAJI2:	LD	E, A
0763	E1		00958		POP	HL
0764	C9		00959		RET	
0765			00960	ALOC:	DEFS	1
			00961			
0766	11	001E	00962	CONDX:	LD	DE, OPRNDS
0769	E6	38	00963		AND	38
076B	1F		00964		RRA	
076C	1F		00965		RRA	
076D	C6	00	00966		LD	B, 0
076F	4F		00967		LD	C, A
0770	3E	2E	00968		LD	A, /
0772	21	0114	00969		LD	HL, CONDXM
0775	C9		00970		ADD	HL, BC
0776	CE	02	00971		LD	C, 2
0778	ED	B0	00972		LDIR	
077A	2E		00973		DEC	HL
077B	BE		00974		CP	(HL)
077C	C0		00975		RET	NZ
077D	1B		00976		DEC	DE
077E	3E	20	00977		LD	A, /
0780	12		00978		LD	(DE), A
0781	C9		00979		RET	
			00980			
			00981			
0782	1F		00982	LOOKUP:	RRA	--(0)--
0783	10	FD	00983		DJNZ	LOOKUP
0785	C5		00984		PUSH	BC
0786	4F		00985		LD	C, A
0787	C9		00986		ADD	HL, BC
0788	C1		00987		POP	BC
0789	ED	B0	00988		LDIR	
078B	C9		00989		RET	

00990 ;  
00991

PAGE

--(0)--

0780

00992 ;

\*\*\* BIT MANIPULATIONS HANDLED HERE \*\*\*

00993 ;

00994 TWIDDLE: INC

00995

00996

00997

00998 CBINF:

00999

01000

01001

01002 MEMCK:

01003

01004

01005

01006

01007 MEM:

01008

01009 ;

01010 BR:

01011

01012

01013

01014 SR:

01015

01016

01017

01018

01019

01020 OK:

01021

01022

01023 NDEXD:

01024

01025

01026

01027

01028

01029

01030

01031

01032

01033

01034

01035

01036

01037

01038

01039

01040

01041 JUST1:

01042

01043

01044 ;

01045 DDCB:

HL  
A, (HL)  
CALL  
JP  
CP  
JR  
CALL  
RET  
OR  
CP  
JR  
LD  
RET  
LD  
RET  
CP  
JR  
LD  
RET  
CP  
JR  
LD  
RET  
CP  
JR  
LD  
RET  
CALL  
OR  
RET  
INC  
LD  
LD  
CP  
JR  
CALL  
LD  
LD  
AND  
CP  
JR  
LD  
AND  
CP  
JR  
CP  
JR  
CP  
JR  
INC  
INC  
LD  
JP

SCOOT PAST REDUNDANT BYTE  
HERE'S THE INSTRUCTION INFO  
BACK  
OCC  
C, BR  
MEMCK  
OFS  
OFE  
Z, MEM  
A, 09  
A, 05  
40  
C, SR  
A, 41  
30  
C, OK  
38  
NC, OK  
A, 00  
MEMCK  
40  
HL  
A, (HL)  
B, A  
OCB  
Z, DDCB  
LOOK  
H, A  
A, B  
OF  
09  
Z, JUST1  
A, B  
OFO  
20  
Z, JUST1  
OEO  
Z, JUST1  
H  
H  
A, H  
BACK  
HL

GET PAST REDUNDANT BYTE  
SAVE A COPY  
INDEXED BIT TWIDDLE?  
SAME AS AN (HL) INSTRUCTION  
LOOK AT INSTRUCTION AGAIN  
LEAST SIGNIFICANT DIGIT COUNTS:  
SOME INDEXED INSTRUCTIONS DO NOT USE THE DISPLACEMENT BYTE.  
... MOST SIGNIFICANT DIGIT ...  
BUT ALL HAVE THE EXTRA PREFIX  
FIX UP LENGTH FIELD  
RESTORE INFO BYTE

07E3 23



```

07E4 23 01046 INC HL ; LOOK AT 4TH BYTE,
07E5 7E 01047 LD A, (HL) ; THAT'S THE REAL INSTRUCTION
07E6 CD 0794 01048 CALL CBINF
07E9 F6 03 01049 OR 03 ; LENGTH=4
07EB 03 01E9 01050 JP BACK
01051 ;
07EE 23 01052 SPCIAL: INC HL ; SECOND BYTE
07EF 7E 01053 LD A, (HL) ; IS THE INSTRUCTION
07F0 FE 40 01054 CP 40
07F1 38 13 01055 JR C, ZZO
07F4 FE 80 01056 CP 80
07F6 38 1E 01057 JR C, SOME0
07F8 FE A0 01058 CP 0A0
07FA 38 0E 01059 JR C, ZZO
07FC FE BC 01060 CP 0BC
07FE 30 0A 01061 JR NC, ZZO
0800 E4 0F 01062 AND 0F ; HERE WE DEAL WITH
0802 FE 04 01063 CP 04 ; BLOCK INSTRUCTIONS
0804 38 02 01064 JR C, BLKO
0806 FE 08 01065 CP 08
0808 30 05 01066 JR NC, BLKO
080A 3E 01 01067 ZZO: LD A, 01 ; INVALID INSTRUCTION; LENGTH=2
080C 03 01E9 01068 JP BACK
080F E6 03 01069 BLKO: AND 03
0811 21 080C 01070 LD HL, SPEC
0814 13 05 01071 JR C, LK
0816 21 08D0 01072 SOME0: LD HL, SPEC1
0819 D6 40 01073 SUB 40
081B CD 083A 01074 CALK: CALL LK
081E 03 01E9 01075 JP BACK
01076 ; LOOKUP ROUTINE; EXPECTS AN INSTRUCTION BYTE IN
01077 ; REGISTER A; RETURNS INFORMATION BYTE IN REGISTER A
01078 ;
0821 FE 40 01079 LOOK: CP 40 ;
0823 38 0B 01080 JR C, LOOK1
0825 FE 00 01081 CP 0C0
0827 30 0C 01082 JR NC, LOOK2
0829 FE 80 01083 CP 80
082B 38 15 01084 JR C, LOOK3
082D 3E 48 01085 LD A, 48 ; 8-BIT ARITHMETIC
082F 09 01086 RET
01087 ;
0830 21 084C 01088 LOOK1: LD HL, TABL1 ; 00-3F
0832 13 05 01089 JR LK
0835 21 088C 01090 LOOK2: LD HL, TABL2 ; C0-FF
0838 D6 C0 01091 SUB 0C0
083A 05 01092 LK: PUSH BC
083B 06 00 01093 LD B, 0
083D 4F 01094 LD C, A
083E 09 01095 ADD HL, BC ; INDEX INTO TABLE
083F 01 01096 POP BC
0840 7E 01097 LD A, (HL) ; BYTE FROM TABLE
0841 09 01098 RET
01099 ;
0842 FE 76 01100 LOOK3: CP 76 ; HALT INSTRUCTION?
0844 20 03 01101 JR NZ, LOOK4 ; ELSE 8-BIT LOAD

```

0846' 3E 00  
0848' C9

0849' 3E 08  
084B' C9

01102  
01103  
01104 ;  
01105 LOOK4:  
01106  
01107 \$EJECT

LD A, 0  
RET

LD A, 08 ; 8-BIT LOAD  
RET

```

08400
01108 ; TABLES FOR LOOKUP ROUTINES
01109 ;
08400 00 0A 04 01110 TABL1: DEFB 00, 0A, 04, 48, 48, 48, 09, 48 ; 0
084F0 48 48 48
08520 09 48
08540 48 48 08 01111 DEFB 48, 48, 08, 48, 48, 48, 09, 48 ;
08570 48 48 48
085A0 09 48
085C0 39 0A 04 01112 DEFB 39, 0A, 04, 48, 48, 48, 09, 48 ; 1
085F0 48 48 48
08620 09 48
08640 11 48 08 01113 DEFB 11, 48, 08, 48, 48, 48, 09, 48 ;
08670 48 48 48
086A0 09 48
086C0 31 0A 06 01114 DEFB 31, 0A, 06, 48, 48, 48, 09, 48 ; 2
086F0 48 48 48
08720 09 48
08740 31 48 0A 01115 DEFB 31, 48, 0A, 48, 48, 48, 09, 48 ;
08770 48 48 48
087A0 09 48
087C0 31 0A 06 01116 DEFB 31, 0A, 06, 48, 44, 44, 05, 40 ; 3
087F0 48 44 44
08820 05 40
08840 31 48 0A 01117 DEFB 31, 48, 0A, 48, 48, 48, 09, 40 ;
08870 48 48 48
088A0 09 40
088C0 38 08 32 01118 TABL2: DEFB 38, 08, 32, 12, 3E, 0C, 49, 1C ; C
088F0 12 3E 0C
08920 49 1C
08940 38 18 32 01119 DEFB 38, 18, 32, 00, 3E, 1E, 49, 1C ;
08970 00 3E 1E
089A0 49 1C
089C0 38 08 32 01120 DEFB 38, 08, 32, 31, 3E, 0C, 49, 1C ; D
089F0 31 3E 0C
08A20 49 1C
08A40 38 08 32 01121 DEFB 38, 08, 32, 31, 3E, 00, 49, 1C ;
08A70 31 3E 00
08AA0 49 1C
08AC0 38 08 32 01122 DEFB 38, 08, 32, 0C, 3E, 0C, 49, 1C ; E
08AF0 0C 3E 0C
08B20 49 1C
08B40 38 10 32 01123 DEFB 38, 10, 32, 08, 3E, 00, 49, 1C ;
08B70 08 3E 00
08BA0 49 1C
08BC0 38 08 32 01124 DEFB 38, 08, 32, 00, 3E, 0C, 49, 1C ; F
08BF0 00 3E 0C
08C20 49 1C
08C40 38 08 32 01125 DEFB 38, 08, 32, 00, 3E, 00, 49, 1C ;
08C70 00 3E 00
08CA0 49 1C
08CC0 4D 41 0D 01126 SPEC: DEFB 4D, 41, 0CD, 0C9
08CF0 09
08D00 09 31 49 01127 SPEC1: DEFB 0C9, 31, 49, 07, 49, 11, 01, 09 ; 4
08D30 07 49 11

```

08D6	01 09		
08D8	C9 81 49	01128	DEFB
08DB	0B 01 11		OC9, 81, 49, 0B, 01, 11, 01, 01
08DE	01 01		
08E0	C9 81 49	01129	DEFB
08E3	07 01 01		OC9, 81, 49, 07, 01, 01, 01, 49
08E6	01 49		
08E8	C9 81 49	01130	DEFB
08EB	0B 01 01		OC9, 81, 49, 0B, 01, 01, 01, 01
08EE	01 01		
08F0	C9 81 49	01131	DEFB
08F3	01 01 01		OC9, 81, 49, 01, 01, 01, 01, 4D
08F6	01 4D		
08F8	C9 81 49	01132	DEFB
08FB	01 01 01		OC9, 81, 49, 01, 01, 01, 01, 4D
08FE	01 4D		
0900	01 01 49	01133	DEFB
0903	07 01 01		01, 01, 49, 07, 01, 01, 01, 01
0906	01 01		
0908	C9 81 49	01134	DEFB
090B	0B 01 01		OC9, 81, 49, 0B, 01, 01, 01, 01
090E	01 01		
0010			

01135 . RADIX 16  
01136 ;

01137 . COMMENT>  
01138           HEREIN ARE DECODED THE SPECIAL Z80 INSTRUCTIONS,  
01139 THOSE PREFIXED BY "ED."  
01140 >

01141	SPECL:	POP	HL
01142		PUSH	HL
01143		INC	HL
01144		LD	A, (HL)
01145		LD	(BYTE), A
01146		CP	OAO
01147		JP	NC, BLOX
01148		CP	40
01149		JP	C, STARS
01150		CP	63
01151		JR	Z, STARS
01152		CP	6B
01153		JR	Z, STARS
01154		CP	70
01155		JR	Z, STARS
01156		CP	71
01157		JR	Z, STARS
01158		AND	07
01159		CP	00
01160		JP	Z, CINS
01161		CP	01
01162		JP	Z, COUTS
01163		CP	02
01164		JP	Z, ADSBC
01165		CP	03
01166		JP	Z, LDED
01167			

; GET INSTR BYTE  
; SAVE IT

0910	E1	
0911	E5	
0912	23	
0913	7E	
0914	32 0000	
0917	FE A0	
0919	D2 09ED	
091C	FE 40	
091E	EA 09A1	
0921	FE 63	
0923	28 7C	
0925	FE 6E	
0927	28 78	
0929	FE 70	
092B	28 74	
092D	FE 71	
092F	28 70	
0931	E6 07	
0933	FE 00	
0935	CA 0A67	
0938	FE 01	
093A	CA 0A87	
093D	FE 02	
093F	CA 0AA9	
0942	FE 03	
0944	CA 0AD9	

01168 ; THIS SECTION DISASSEMBLES THE MISCELLANEOUS

## 01169 ; ED-TYPE INSTRUCTIONS -- G. D. BUZZARD

0947	3A 0000	01170		LD	A, (BYTE)	
094A	11 0016	01171		LD	DE, OPCODE	; DESTINATION
094D	FE 44	01172		CP	44	; "NEG" OPCODE
094F	21 09B9	01173		LD	HL, MNEM	; POINT HL TO "NEG"
0952	28 50	01174		JR	Z, PUT	
0954	FE 45	01175		CP	45	; "RETN"
0956	21 09BD	01176		LD	HL, MNEM+4	
0959	28 49	01177		JR	Z, PUT	
095B	FE 4D	01178		CP	4D	; "RETI"
095D	21 09C1	01179		LD	HL, MNEM+8	
0960	28 42	01180		JR	Z, PUT	
0962	FE 67	01181		CP	67	; "RRD"
0964	21 09C5	01182		LD	HL, MNEM+0C	
0967	28 3B	01183		JR	Z, PUT	
0969	FE 6F	01184		CP	6F	; "RLD"
096B	21 09C9	01185		LD	HL, MNEM+10	
096E	28 34	01186		JR	Z, PUT	
0970	FE 46	01187		CP	46	; "IM 0"
0972	21 09CD	01188		LD	HL, MNEM+14	
0975	28 2D	01189		JR	Z, PUT	
0977	FE 56	01190		CP	56	; "IM 1"
0979	21 09D1	01191		LD	HL, MNEM+18	
097D	28 26	01192		JR	Z, PUT	
097E	FE 5E	01193		CP	5E	; "IM 2"
0980	21 09D5	01194		LD	HL, MNEM+1C	
0983	28 1F	01195		JR	Z, PUT	
0985	FE 47	01196		CP	47	; "LD I, A"
0987	21 09D9	01197		LD	HL, MNEM+20	
098A	28 20	01198		JR	Z, LDLD	
098C	FE 57	01199		CP	57	; "LD A, I"
098E	21 09DD	01200		LD	HL, MNEM+24	
0991	28 19	01201		JR	Z, LDLD	
0993	FE 5F	01202		CP	5F	; "LD A, R"
0995	21 09E1	01203		LD	HL, MNEM+28	
0998	28 12	01204		JR	Z, LDLD	
099A	FE 4F	01205		CP	4F	; "LD R, A"
099C	21 09E5	01206		LD	HL, MNEM+2C	
099F	28 0B	01207		JR	Z, LDLD	
09A1	21 09E9	01208	STARS:	LD	HL, MNEM+30	; ERROR CHECK
09A4	01 0004	01209	PUT:	LD	BC, 0004	
09A7	ED B0	01210		LDIR		; WRITE MNEMONIC
09A9	03 0B26	01211		JP	SPRET	; DONE
09AC	E5	01212	LDLD:	PUSH	HL	; WRITE "LD" (OPCODE)
09AD	EB	01213		EX	DE, HL	; MOVE POINTERS TO
09AE	36 4C	01214		LD	(HL), "L"	; OPRNDS SOURCE & DES
09B0	23	01215		INC	HL	; FIELDS
09B1	36 44	01216		LD	(HL), "D"	
09B3	11 001E	01217		LD	DE, OPRNDS	; NEW DESTINATION
09B6	E1	01218		POP	HL	; RESTORE SOURCE
09B7	18 EB	01219		JR	PUT	; WRITE OPRNDS
09B9	4E 45 47	01220	MNEM:	DEFM	"NEG RETNRETIRRD RLD IM OIM 1"	
09BC	20 52 45					
09BF	54 4E 52					
09C2	45 54 49					
09C5	52 52 44					

```

09C8' 20 52 4C
09CB' 44 20 49
09CE' 4D 20 30
09D1' 49 4D 20
09D4' 31
09D5' 49 4D 20
09D8' 32 49 2C
09DB' 41 20 41
09DE' 2C 49 20
09E1' 41 2C 52
09E4' 20 52 2C
09E7' 41 20 2A
09EA' 2A 2A 2A

```

```
01221
```

```
DEFM 'IM ZI, A A, I A, R R, A ****'
```

```

01222 ;
01223 ; THIS ROUTINE DISASSEMBLES THE ED-TYPE
01224 ; BLOCK TRANSFER INSTRUCTIONS. LDI, LDIR,
01225 ; LDD, LDDR, CPI, CPIR, CPD, CPDR.
01226 ; OBSERVE PRECEDING JUMP STATEMENT WHEN MODIFYING
01227 ; LINES DENOTED WITH '(***)' -- G. D. BUZZARD

```

```

09ED' 21 0A27'
09F0' 3A 0000'
09F3' CB 57
09F5' C2 09A1'
09F8' CB 77
09FA' C2 09A1'
09FD' 47
09FE' 3E 00
0A00' CB 48
0A02' 28 02
0A04' C6 20
0A06' CB 40
0A08' 28 02
0A0A' C6 10
0A0C' CB 38
0A0E' 28 02
0A10' C6 08
0A12' CB 60
0A14' 28 02
0A16' C6 04
0A18' 16 00
0A1A' 5F
0A1B' 19
0A1C' 11 0016'
0A1F' 01 0004
0A22' ED B0
0A24' C3 0B26'
0A27' 4C 44 49
0A2A' 20 4C 44
0A2D' 49 52 4C
0A30' 44 44 20
0A33' 4C 44 44
0A36' 52 43 50
0A39' 49 20 43

```

```

01228 BLOX: LD HL, BLMNEM ; BLOCK MNEMONICS
01229 LD A, (BYTE) ; GET OPCODE
01230 BIT 2, A
01231 JP NZ, STARS
01232 BIT 6, A
01233 JP NZ, STARS
01234 LD B, A ; USE REG B
01235 LD A, 0
01236 BIT 1, B
01237 JR Z, $+4
01238 ADD A, 20
01239 BIT 0, B ; TEST INST. TYPE
01240 JR Z, $+4 ; TRANSFER INSTRUCTION
01241 ; (SKIP 2 BYTES)
01242 ADD A, 10 ; SEARCH INSTR (***)
01243 BIT 3, B
01244 JR Z, $+4 ; INC HL INSTRUCTION
01245 ; (SKIP 2 BYTES)
01246 ADD A, 8 ; DEC HL INSTR (***)
01247 BIT 4, B
01248 JR Z, $+4 ; NON-REPEAT INSTR
01249 ; (SKIP 2 BYTES)
01250 ADD A, 4 ; REPEATING INSTR(***)
01251 LD D, 0
01252 LD E, A
01253 ADD HL, DE ; PICK RIGHT MNEMONIC
01254 LD DE, OPCODE ; DESTINATION
01255 LD BC, 4
01256 LDIR ; WRITE MNEMONIC
01257 JP SPRET ; DONE
01258 BLMNEM: DEFM 'LDI LDIRLDD LDDRCPI CPIRCPD CPDR'

```

```

0A3C 50 49 52
0A3F 43 50 44
0A42 20 43 50
0A45 44 52
0A47 49 4E 49 01259      DEFM      'INI INIRIND INDRROUTIOTIROUTDOTDR'
0A4A 20 49 4E
0A4D 49 52 49
0A50 4E 44 20
0A53 49 4E 44
0A56 52 4F 55
0A59 54 49 4F
0A5C 54 49 52
0A5F 4F 55 54
0A62 44 4F 54
0A65 44 52

```

```

01260 ;
01261 ; THIS ROUTINE HANDLES THE 'IN R,(C)' INSTRUCTION
0A67 21 0016 01262 CINS: LD HL,OPCODE ; WRITE 'IN'
0A6A 36 49 01263 LD (HL),'I'
0A6C 23 01264 INC HL
0A6D 36 4E 01265 LD (HL),'N' ; END WRITE 'IN'
0A6F 11 001E 01266 LD DE,OPRND5 ; DESTINATION
0A72 3A 0000 01267 LD A,(BYTE)
0A75 0D 06FF 01268 CALL REG38 ; WRITE 'R'
0A78 01 0004 01269 LD BC,4
0A7B 21 0A83 01270 LD HL,BRACK ; WRITE '(C)'
0A7E ED B0 01271 LDIR ; "
0A80 03 0B26 01272 JP SPRET ; DONE
0A83 2C 28 43 01273 BRACK: DEFM '(C)'
0A86 29

```

```

01274 ;
01275 ; THIS ROUTINE HANDLES THE 'OUT (C),R' INSTRUCTION
0A87 11 0016 01276 COUTS: LD DE,OPCODE ; DESTINATION
0A8A 21 0AA2 01277 LD HL,COMNEM ; SOURCE
0A8D 01 0003 01278 LD BC,3
0A90 ED B0 01279 LDIR ; WRITE 'OUT'
0A92 11 001E 01280 LD DE,OPRND5 ; NEW DESTINATION
0A95 0E 04 01281 LD C,4
0A97 ED B0 01282 LDIR ; WRITE '(C),'
0A99 3A 0000 01283 LD A,(BYTE) ; GET OPCODE
0A9C 0D 06FF 01284 CALL REG38 ; WRITE 'R'
0A9F 03 0B26 01285 JP SPRET ; DONE
0AA2 4F 55 54 01286 COMNEM: DEFM 'OUT(C),'
0AA5 28 43 29
0AA8 2C

```

```

01287 ;
01288 ; THIS ROUTINE HANDLES THE ADC, AND SBC
01289 ; INSTRUCTIONS.
0AA9 11 0016 01290 ADSBC: LD DE,OPCODE ; DESTINATION
0AAC 01 0003 01291 LD BC,3
0AAF 3A 0000 01292 LD A,(BYTE) ; GET OPCODE
0AB2 0B 5F 01293 BIT 3,A ; TEST FOR ADD/SUB
0AB4 20 05 01294 JR NZ,AD ; ADD INSTRUCTION
0AB6 21 0AD3 01295 LD HL,SEMNEM ; SUB MNEMONIC
0AB9 18 03 01296 JR N1
0ABE 21 0AD6 01297 AD: LD HL,ADMNEM ; ADD MNEMONIC

```

```

OABE ED B0          01298 N1:  LDIR
OACO 21 OAD0       01299      LD
OAC3 11 001E       01300      LD      HL, ASMNEM
OAC6 01 0003       01301      LD      DE, OPRNDS
OAC9 ED B0          01302      LD      BC, 3
OACB CD 0698       01303      LDIR
OACE 18 56         01304      CALL   SS
OADO 48 4C 2C      01305 ASMNEM: DEFM   SPRET
OAD3 53 42 43      01306 SBMNEM: DEFM  'HL,'
OAD6 41 44 43      01307 ADMNEM: DEFM  'SBC'
                                01308 ; 'ADC'
                                01309 ; THIS ROUTINE HANDLES THE ED-TYPE SIXTEEN BIT
                                01310 ; LOAD INSTRUCTIONS.
                                01311 LDED: INC   HL
                                01312      LD    A, (HL)
                                01313      LD    (ADDRL), A
                                01314      INC  HL
                                01315      LD    A, (HL)
                                01316      LD    (ADDRH), A
                                01317      LD    HL, OPCODE
                                01318      LD    (HL), 'L'
                                01319      INC  HL
                                01320      LD    (HL), 'D'
                                01321      LD    HL, OPRNDS
                                01322      LD    A, (BYTE)
                                01323      BIT  3, A
                                01324      JR   Z, N2
                                01325      EX  DE, HL
                                01326      CALL SS
                                01327      EX  DE, HL
                                01328      LD    (HL), ',,'
                                01329      INC  HL
                                01330 N2: LD    (HL), '(('
                                01331      INC  HL
                                01332      LD    A, (ADDRH)
                                01333      LD    B, 2
                                01334 N3: CALL  ACONV
                                01335      LD    (HL), E
                                01336      INC  HL
                                01337      LD    (HL), D
                                01338      INC  HL
                                01339      LD    A, (ADDRL)
                                01340      DJNZ N3
                                01341      LD    (HL), ',)'
                                01342      LD    A, (BYTE)
                                01343      BIT  3, A
                                01344      JR   NZ, SPRET
                                01345      INC  HL
                                01346      LD    (HL), ',,'
                                01347      INC  HL
                                01348      EX  DE, HL
                                01349      CALL SS
                                01350      JR   SPRET
                                01351 ADDR: DEFS  1
                                01352 ADDR: DEFS  1
                                01353 ;
OAD9 23
OADA 7E
OADB 32 0B24
OADE 23
OADF 7E
OAE0 32 0B25
OAE3 21 0016
OAE6 36 4C
OAE8 23
OAE9 36 44
OAEB 21 001E
OAEF 3A 0000
OAF1 CB 5F
OAF3 28 08
OAF5 EB
OAF6 CD 0698
OAF9 EB
OAFB 36 2C
OAFD 23
OAFD 36 28
OAFD 23
OAFD 23
OAFD 3A 0B24
OAFD 10 F4
OAFD 36 29
OAFD 3A 0000
OAFD CB 5F
OAFD 20 0C
OAFD 23
OAFD 36 2C
OAFD 23
OAFD EB
OAFD CD 0698
OAFD 18 02
OAFD 23
OAFD 23

```

```

; WRITE MNEMONIC
; NEW SOURCE
; NEW DESTINATION
; WRITE 'HL,'
; WRITE 'SS'
; POINT TO NEXT BYTE
; STORE IT
; NEXT BYTE
; STORE IT
; DESTINATION
; WRITE 'LD'
; NEW DESTINATION
; WRITE '(('
; WRITE ADDRESS
; CONVERT TO ASCII
; WRITE ADDRESS
; FINISH WRITING ADDRESS
; PUT DESTINATION IN DE
; WRITE 'SS'
; DONE

```



0026 03 0106

01354 SPRET.  
01355

JP  
END

INSRET

MACROS.  
NEXT PUT

SYMBOLS.

ACONV	0749	ACONVI	072D	AD	0ABB	ADDHL	049C	ADDRH	0B25
ADDRL	0B24	ADMNEM	0AD6	ADSB	0AA9	AGAIN	0580	ALOC	0765
AMES	00EA	ANAJ1	075A	ANAJ2	0762	ANAJI1	073E	ANAJI2	0746
ARITH	039E	ASMNEM	0AD0	BACK	01E9	BANKSW	01EE*	BLKO	080F
BLMNEM	0A27	BLOX	09ED	BR	07A8	BRACK	0A83	BUMP	05BD
BYTE	0000I	CALK	081B	CALLO	0274	CALLM	0073	CALRET	024E
CBBUF	00B6	CBINF	0794	CINS	0A67	CODE	000A	COMM	0403
CBMNEM	0AA2	CONDX	0766	CONDXM	0114	CORE	0168	COUT3	0A87
CBUF	00B3	DDCB	07E3	DEC16	046C	DEC8	04B5	DECM	0038
CEHLM	00AB	DISABL	030B	DISASS	0124I	DISPLC	06B3	DISPX	00B0
CUNZM	004B	EXM	0043	EXPLCA	068F	EXRET	039B	EXS	037A
CO	046F	SOB	04BD	HEX	0145I	HIQTR	01F2	HLIND	008F
HLM	03FB	HLMES	0035	HLTM	00E2	INC16	0467	INCS	04BA
INCM	003C	INDEXD	0544	INDI8	03EF	INFO	01CBI	INM	009E
INDUT	033D	INSRET	01C6I	INSTR	0187I	INTERM	009C	INTEWR	030D
INK	02FD	JPM	0087	JPS	0236	JRDIS	053B	JRS	04CE
JRS1	04E0	JRS2	04F7	JRS3	050F	JRSM	0050	JRZ	0533
JOST1	07DE	LDS	03CD	LDDE	0535	LDED	0AD9	LDLD	09AC
LEM	008B	LDS	0645	LENGTH	0001I	LINE	0003I	LK	083A
LEAD16	044C	LOADS	0633	LOCN	0004	LOGIC	0660	LOGTAB	00EC
LOOK	0821	LOOK1	0830	LOOK2	0835	LOOK3	0842	LOOK4	0849
LOOKA	047D	LOOKA2	0489	LOOKA3	048C	LOOKUP	0782	LTAB1	0053
LTAB2	0063	LTRLD	0430	MEM	07A5	MEMCK	079C	MINUS	002D
MNEM	09E9	N1	0ABE	N2	0AFD	N3	0B05	NBUF	00E6
NDEXD	07C0	NODISP	056C	NOPM	0040	NOTJP	032D	NOTWID	057A
NUMB16	06C8	NUMB3	06EA	OK	07BA	ONDEX	05AF	OPCODE	0016I
OPRND5	001EI	OUTM	00A1	OUTO	0361	OUTW	033A	PLM	007F
PLDCK	02B2	PLUS	002B	POPM	007B	POPS	0292	POPUSH	0282
POS	05EB	POSC	0525	PUSHM	0077	PUT	09A4	PUTS	0295
REG33	06FF	REG3	0707	REG81	0708	REG8LD	0727	RETS	0266
REVRS	043C	RG16	041A	RNAM	0112	RNAMES	010C	RSEND	0616
ROFND	0313	RSTJNK	02DC	RSTM	0099	SBMNEM	0AD3	SEE	0583
SGT	05F1	SHPROP	06B3	SHROTA	061F	SHTAB	00C3	SOME0	0813
SPI	0008	SPECIAL	07EE	SPEC	08CC	SPEC1	08D0	SPECL	0910
SFHLI	0393	SPLM	00A4	SPRET	0B26	SR	07AF	SS	0698
STARS	09A1	TABL1	084C	TABL2	088C	TARG	0245	TWIDL	078C
TWIDL	05E2	TWITAB	00B8	VIDEO	00ED	VIDOFF	0012	WCHWAY	041D
WGLE	0596	XBUF	00B1	XIND	054C	YIND	054E	ZZO	080A

(0 FATAL ERROR(S))

00001 , TRACK 80:VII:23

00002 , BY BILL MACLEOD  
00003 , & GREG BUZZARD  
00004 , LAST UPDATED 07-09-81  
00005 ,

00006 , THIS ROUTINE MAKES A TABLE OF ORIGINS AND END POINTS  
00007 , OF INSTRUCTION FIELDS IN A LOADED PROGRAM.

00008 , ORG = FIRST BYTE OF INSTRUCTION FIELD

00009 , END = FIRST BYTE OF NON-INSTR FIELD

00010 , ORG-END PAIRS ARE CREATED AS 4-BYTE

00011 , FIELDS IN THE TABLE AT "ORGEND."

00012 , IT IS ASSUMED THAT CODE IS NOT SELF-MODIFYING.

00013 , A RETURN TO THE OPERATING SYSTEM IS CONSIDERED AN

00014 , ENDPOINT; I. E., THE OPERATING SYSTEM IS NOT TRACED.

00015 ,

00016 , THE ROUTINE EXPECTS THE STARTING ADDRESS

00017 , TO BE PASSED IN REGISTER PAIR HL.

00018 ,

0100

00019 RADIX 16

00020 EXTRN INFO

00021 EXTRN CURSOR

00022 EXTRN CURSES

00023 EXTRN KEYIN

00024 EXTRN HEXIT

00025 EXTRN SYN

00026 EXTRN CICO

00027 ENTRY TRACK

00028 ENTRY ORGEND

0100

00029 CD EQU 0100

FEFC

00030 INMESS EQU 0F8FO

0000

00031 CURSR EQU 00

00FF

00032 CURSL EQU OFF

0032

00033 COML EQU 32

0005

00034 CGMH EQU 05

FBFF

00035 SCLOC EQU 0F8FF

00036 ;

00004 F5

00037 TRACK. PUSH AF ; SAVE CALLER'S REGISTERS

00014 B

00038 PUSH BC

00024 D5

00039 PUSH DE

00034 E5

00040 PUSH HL

00041 ;

00044 22 0326

00042 LD (ORGEND), HL ; START @ IS 1ST ORG

00074 21 0326

00043 LD HL, ORGEND

000A4 22 031C

00044 LD (CURRNT), HL

000D4 21 0000

00045 LD HL, 0000

00104 22 0328

00046 LD (ORGEND+2), HL

00134 22 032C

00047 LD (ORGEND+6), HL

00164 22 0319

00048 LD (INDFLG), HL

00194 2B

00049 DEC HL

001A4 22 032A

00050 LD (ORGEND+4), HL

001D4 21 032A

00051 LD HL, ORGEND+4 ; NEXT AVAILABLE

00204 22 031E

00052 LE (HEADER), HL ; ORG-END FIELD

00234 11 032E

00053 LD DE, ORGEND+8

00264 11 03F8

00054 LD BC, 03F8

00294 ED BC

00055 LDIR

002B4 2A 0326

00056 LD HL, (ORGEND) ; FIRST ORG

```

002E' CD 0000* 00057 ;
0031' F5 00058 FIRST: CALL INFO
0032' E6 03 00059 PUSH AF
0034' 3C 00060 AND 03
0035' 06 00 00061 INC A
0037' 4F 00062 LD B, 0
0038' ED 43 00063 LD C, A
003A' 031A' 00064 LD (LENGTH), BC
003C' F1 00065 POP AF
003D' CB 67 00066 BIT 4, A
003F' 20 03 00067 JR NZ, BRANCH
0041' 09 00068 ADD HL, BC ; REF PTR + LENGTH
0042' 18 EA 00069 JR FIRST
00070 ;
0044' CB 6F 00071 BRANCH: BIT 5, A ; IS IT CONDITIONAL?
0046' 23 06 00072 JR Z, NOFE
0048' 1D 0144' 00073 B4: CALL TARGET
004B' 09 00074 ADD HL, BC
004C' 18 E0 00075 JR FIRST
00076 ;
004E' 7E 00077 ; *** UNCONDITIONAL BRANCH HANDLED HERE:
004F' FE 0D 00078 NOFE: LD A, (HL)
0051' 28 F5 00079 CP 0CD ; CALL?
0053' FE 09 00080 JR Z, B4 ; JUST LIKE CONDITIONAL BRANCH
0055' 28 03 00081 CP 0C9 ; RETURN?
0057' CD 0144' 00082 JR Z, N9 ; IF SO, NO TARGET
0059' 0A 00083 CALL TARGET
005B' BD 2A 00084 N9: ADD HL, BC
005D' 0310' 00085 LD IX, (CURRENT)
005F' BD 75 02 00086 LD (IX-2), L ; END -- REF POINTER
0061' BD 74 02 00087 LD (IX-3), H
0063' BD E5 00088 PUSH BX
0067' E1 00089 POP HL ; POINT AT CURRENT ORG
006B' 01 1104 00090 LD BC, 0004
006B' 09 00091 ADD HL, BC
006D' 22 0310 00092 LD (CURRENT), HL ; CURRENT--CURRENT+4
006F' BD 86 05 00093 LD H, (IX+5) ; REF PTR -- ((CURRENT))
0071' BD 8E 04 00094 LD L, (IX+4) ; ((CURRENT)) IS NEXT ORG
0073' 0E 01 00095 LD C, 1
0077' ED 42 00096 ADC HL, BC
0079' 28 03 00097 JR Z, SORTT
00098 ;
00099 ; IF NEXT ORG-END IS FFFF-0000, YOU'RE DONE,
00100 ; GO SORT THE ORG-END LIST.
007B' 18 00101 ELSE GO BACK & CHECK ANOTHER INSTRUCTION.
007C' 18 E0 00102 DEC HL
00103 ; JR FIRST
00104 #EJECT

```

```

007E
007E 3A 0317 00105 ;
0081 FE 00 00106 SORTT. LD A, (INFLG) ; WAS 'OP (HL)' USED?
0083 CA 0136 00107 CP 0
0086 21 01B0 00108 CF Z, TRRET ; IF NOT USED
0089 CD 029D 00109 LD HL, MESS1
0090 ; CALL MESS
0090 11 FF00 00110 ; GET USER DEFINED ORG-END PAIRS
009F 21 030C 00111 LD DE, INMESS ; PRINT PROMPT
009Z 01 000D 00112 LD HL, MESS3
0095 ED 50 00113 LD BC, 0D
0097 DD 21 00114 ;
0099 0B23 00115 LD IX, ORGEND ; START OF TABLE
009E 5F 00116 ;
009C 01 01FF 00117 EXX
009F 5F 00118 LD BC, 1FF ; STORE OVERFLOW
0095 ; EXX ; COUNTER
009C 21 0000+ 00119 ;
00A3 23 00120 ;
00A4 3E 00 00121 ;
00A6 77 00122 T1: LD HL, CURSES ; MOVE CURSOR POSITION
00A7 23 00123 INC HL ; (OUR LINKER DOES
00A8 23 00124 LD A, CURSH ; NOT ALLOW EFFECTS ON
00A9 3E FF 00125 LD (HL), A ; EXTERNAL SYMBOLS
00AB 23 00126 INC HL
00AD 77 00127 INC HL
00AE 77 00128 LD A, CURSL
00AC CD 0000+ 00129 LD (HL), A
00AF 55 00130 CALL CURSOR
00B0 06 19 00131 ;
00B1 3E 20 00132 PUSH HL ; SAVE CURRENT VALUE
00B4 21 FF0F 00133 LD B, 19
00B7 77 00134 LD A, 20 ; ASCII BLANK
00B9 23 00135 LD HL, SCLOC ; SCREEN LOC'N
00BA 23 00136 T5: LD (HL), A ; BLANK OUT LINE
00BB 23 00137 INC HL
00BC 10 FC 00138 DJNZ T5
00BE 51 00139 POP HL
00BF ; 00140 ;
00C0 21 FF0F 00141 LD HL, SCLOC ; SCREEN LOC'N FOR
00C1 ; 00142 ; INPUT
00C2 CD 0000+ 00143 CALL CIOO ; GET INPUT
00C3 ; 00144 ;
00C4 DD E5 00145 PUSH IX ; POINT TO NEXT LOC'N
00C5 51 00146 POP HL ; IN TABLE
00C6 DD 23 00147 INC IX ; INCREMENT POINTER
00C7 DD 23 00148 INC IX
00C9 23 00149 INC HL ; POINT TO HIGH BYTE
00CA ; 00150 ; FIRST
00CA 06 02 00151 ; CHECK VALIDITY OF INPUT
00CB 11 0000+ 00152 LD B, 2
00CC ; 00153 LD DE, KEYIN ; POINT TO INPUT
00CD ; 00154 ;
00CF CD 0000+ 00155 T1: CALL HEXIT ; CONVERT TO HEX
00D2 0B 7F 00156 BIT 7, A ; ERROR ?
00D4 20 29 00157 JR NZ, SYN2 ; IF SO, JUMP

```

00D6	ED 6F	00158			
00D8	13	00159	RLD		
		00160	INC	DE	STORE VALUE
		00161			NEXT ASCII
00D9	CD 0000*	00162	CALL	HEXIT	
00DC	CB 7F	00163	BIT	7, A	CONVERT TO HEX
00DE	20 1F	00164	JR	NZ, SYN2	ERROR ?
		00165			IF SO, JUMP
00E0	ED 6F	00166	RLD		
00E2	13	00167	INC	DE	STORE VALUE
00E3	2B	00168	DEC	HL	NEXT ASCII
00E4	10 E9	00169	DJNZ	T1	POINT TO LOW BYTE
		00170			RETURN FOR LOW
00E6	1A	00171	LD	A, (DE)	
00E7	FE 0D	00172	CP	0D	NEXT ASCII
00E9	20 14	00173	JR	NZ, SYN2	IS IT <CR> ?
		00174			IF NOT, JUMP
		00175	CHECK FOR OVERFLOW.		
00EB	D9	00176	EXX		
00EC	0B	00177	DEC	BC	DECREMENT COUNTER
00ED	21 0000	00178	LD	HL, 00	
00F0	BF	00179	CP	A	ZERO CARRY FLAG
00F1	ED 4A	00180	ADC	HL, BC	CHECK FOR OVERFLOW
00F3	D9	00181	EXX		
00F4	20 24	00182	JR	NZ, T3	JUMP IF OVERFLOW
00F6	21 02F4	00183	LD	HL, MESS2	PRINT MESSAGE
00F9	CD 029D	00184	CALL	MESS	
00FC	CB 0123	00185	JP	0123	RETURN TO SYSTEM
		00186			
00FF	DD E5	00187	SYNTAX ERROR HANDLER.		
0101	DD 21	00188	SYN2: PUSH	IX	ERROR HANDLER
0103	010D	00189	LD	IX, SYN3	RETURN ADDRESS
0105	21 0000*	00190	LD	HL, SYN	
0108	01 0004	00191	LD	BC, 04	IN EXEC ROUTINE
010E	09	00192	ADD	HL, BC	
010C	E9	00193	JP	(HL)	JUMP TARGET
		00194			
010D	DD E1	00195	SYN3: POP	IX	RESTORE IX
010F	DD E5	00196	PUSH	IX	
0111	E1	00197	POP	HL	GET HL
0112	2E	00198	DEC	HL	RESTORE TO PREVIOUS
		00199			VALUE
0113	06 02	00200	LD	B, 2	
0115	11 0000*	00201	LD	DE, KEYIN	
0118	13 B5	00202	JR	T1	TRY AGAIN
		00203			
011A	23	00204	CHECK FOR END OF INPUTS		
011B	7E	00205	T3: INC	HL	GET MSBYTE OF THIS
011C	FE 00	00206	LD	A, (HL)	INPUT
011E	20 80	00207	CP	00	IS IT 00
		00208	JR	NZ, T2	IF NOT, LOOP
		00209			
0120	2B	00210	DEC	HL	GET PREVIOUS INPUT
0121	2B	00211	DEC	HL	
0122	7E	00212	LD	A, (HL)	MSBYTE

0123	FE FF	00213	CP	OFF	, WAS IF OFF ?
0125	02 00A0	00214	JF	NZ, T2	, IF NOT, LOOP
		00215	,		
0126	21 0000*	00216	LD	HL, CURSES	, PUT CURSOR BACK
012E	23	00217	INC	HL	, TO COMMAND AREA
012C	3E 05	00218	LD	A, COMH	
012E	77	00219	LD	(HL), A	
012F	23	00220	INC	HL	
0130	23	00221	INC	HL	
0131	3E 32	00222	LD	A, COML	
0133	77	00223	LD	(HL), A	
0134	1B 09	00224	LR	T6	
		00225	,		
		00226	,		
0136	0D 023A	00227	TRRET.	CALL	SORT
0138	0D 01F3	00228		CALL	MERGE
013D	0D 023A	00229		CALL	SORT
		00230	,		
013F	E1	00231	T3:	POP	HL
0141	01	00232		POP	DE
0143	01	00233		POP	BC
0145	F1	00234		POP	AF
0148	0F	00235		RET	
		00236	#EJECT		

0144

00237 , TARGET FINDER EXPECTS HL TO POINT AT INSTRUCTION  
00238 , AND EXPECTS ACCUMULATOR TO HOLD INFO BYTE  
00239 ,

0144	FB	00240	TARGET: PUSH	AF	
0145	CS	00241	PUSH	BC	
0146	ES	00242	PUSH	HL	
0147	3A 031A	00243	LD	A, (LENGTH)	"LENGTH" IS 2 BYTES
014A	FE 01	00244	CP	01	
014C	28 10	00245	JR	Z, RSTS	IT'S A RESTART (OR UP (HL) )
014E	FE 02	00246	CP	02	
0150	28 28	00247	JR	Z, JRS	IT'S A RELATIVE JUMP
		00248			
		00249			
0152	23	00250	INC	HL	TO GET THIS FAR, IT MUST BE
0153	E5	00251	PUSH	HL	AN ABSOLUTE JUMP
0154	DD E1	00252	POP	IX	
0156	DD 66 01	00253	LD	H, (IX+1)	
0159	DD 6E 00	00254	LD	L, (IX+0)	
015C	18 2C	00255	JR	CHEKR	
		00256			
015E	7E	00257	RSTS:		
015F	FE E9	00258	LD	A, (HL)	
0161	28 0C	00259	CP	0E9	IS IT "UP (HL)"?
0163	FE 09	00260	JR	Z, HLIND	
0165	CA 01EF	00261	CP	0C9	RET?
0168	E6 38	00262	JP	Z, TARET	
016A	26 00	00263	AND	38	STRIP OUT ADDRESS BITS
016C	6F	00264	LD	H, 0	
016D	18 1B	00265	LD	L, A	
		00266	JR	CHEKR	
		00267	HLIND:		
016F	3E FF	00268	LD	A, OFF	
0171	32 0319	00269	LD	(INDFLG), A	
0174	E1	00270	POP	HL	
0175	C1	00271	POP	BC	
0176	B1	00272	POP	DE	
0177	03 007E	00273	JP	30RTT	
		00274	JRS:		
017A	23	00275	INC	HL	POINT AT DISPLACEMENT
017B	7E	00276	LD	A, (HL)	
017C	FE E9	00277	CP	0E9	
017E	28 EF	00278	JR	Z, HLIND	"UP (IX)"?
0180	23	00279	INC	HL	NOW POINT TO JUMP'S BASE
0181	4F	00280	LD	C, A	
0182	06 08	00281	LD	B, 8	
0184	0B 2F	00282	SRA	A	
0186	10 FC	00283	DJNZ	SIGN	COPY SIGN BIT THRU
0188	47	00284	LD	B, A	
0189	09	00285	ADD	HL, BC	NOW SEND HL TO CHEKR
			#EJECT		



```

018A
018A 22 0320 00286 ;
018A 22 0320 00287 CHEKR: LD (TEMP1), HL
018D 01 0123 00288 LD BC, 0123
0190 97 00289 SUB A
0191 ED 42 00290 SBC HL, BC
0193 28 5A 00291 JR Z, TARET ; TARGET = SP008 ?
0195 DD 21 00292 LD IX, ORGEND
0197 0326
0199 DD 46 01 00293 NEXT1: LD B, (IX+1)
019C DD 4E 00 00294 LD C, (IX+0)
019F 21 0000 00295 LD HL, 0
01A2 97 00296 SUB A
01A3 ED 42 00297 SBC HL, BC
01A5 28 20 00298 JR Z, NEW1 ; IS IT A NEW ONE?
01A7 2A 0320 00299 LD HL, (TEMP1)
01AA 97 00300 SUB A
01AE ED 42 00301 SBC HL, BC ; ORG>TARGET ==>CARRY
01AD 28 40 00302 JR Z, TARET ; IF ORG=TARGET DO NOTHING
01AF 30 07 00303 JR NC, CKEND ; GO CHECK END
01B1 01 0004 00304 NEXT2: LD BC, 0004
01B4 DD 09 00305 ADD IX, BC
01B6 13 E1 00306 JR NEXT1
01B8 2A 0320 00307 CKEND: LD HL, (TEMP1)
01BB DD 46 03 00308 LD B, (IX+3)
01BE DD 4E 02 00309 LD C, (IX+2)
01C1 ED 42 00310 SBC HL, BC
01C3 38 2A 00311 JR C, TARET
01C5 18 EA 00312 JR NEXT2
01C7 DD 2A 00313 NEW1: LD IX, (HEADER)
01C9 031E
01CB 2A 0320 00314 LD HL, (TEMP1)
01CE DD 74 01 00315 LD (IX+1), H
01D1 DD 75 00 00316 LD (IX+0), L
01D4 2A 031E 00317 LD HL, (HEADER)
01D7 01 0004 00318 LD BC, 0004
01DA 09 00319 ADD HL, BC
01DE 22 031E 00320 LD (HEADER), HL
01DE 01 0722 00321 LD BC, ORGEND+3FC
01E1 97 00322 SUB A
01E2 ED 42 00323 SBC HL, BC
01E4 38 09 00324 JR C, TARET
01E6 21 02F4 00325 LD HL, MESS2
01E9 CD 029D 00326 CALL MESS
01EC C3 0123 00327 JP 0123
01EF E1 00328 TARET: POP HL
01F0 C1 00329 POP BC
01F1 F1 00330 POP AF
01F2 C9 00331 RET
00332 $EJECT

```

01F3'								
01F3'	DD 21		00333	MERGE:	LD		IX, ORGEND	
01F5'	0326'							
01F7'	DD 66 03		00334	MERG1:	LD		H, (IX+3)	
01FA'	DD 6E 02		00335		LD		L, (IX+2)	
01FD'	DD 46 05		00336		LD		B, (IX+5)	; THIS END
0200'	DD 4E 04		00337		LD		C, (IX+4)	
0203'	97		00338		SUB		A	; NEXT ORG
0204'	ED 42		00339		SBC		HL, BC	
0206'	38 21		00340		JR		C, NEXPR	
0208'	DD 7E 06		00341		LD		A, (IX+6)	
			00342					
020B'	DD 77 02		00343		LD		(IX+2), A	; MERGE THIS PAIR
020E'	DD 7E 07		00344		LD		A, (IX+7)	WITH THE NEXT
0211'	DD 77 03		00345		LD		(IX+3), A	
0214'	DD 36 04		00346		LD		(IX+4), OFF	; STUFF NEXT PAIR
0217'	FF							
0218'	DD 36 05		00347		LD		(IX+5), OFF	
021B'	FF							
021C'	DD 36 06		00348		LD		(IX+6), 0	; WITH NULL VALUES
021F'	00							
0220'	DD 36 07		00349		LD		(IX+7), 0	
0223'	00							
0224'	CD 023A'		00350		CALL		SORT	
0227'	18 CE		00351		JR		MERG1	
0229'	01 0004		00352	NEXPR:	LD		BC, 0004	
022C'	DD 09		00353		ADD		IX, BC	; NEXT PAIR
022E'	DD E3		00354		PUSH		IX	
0230'	E1		00355		POP		HL	
0231'	01 0722'		00356		LD		BC, ORGEND+3FC	
0234'	97		00357		SUB		A	
0235'	ED 42		00358		SBC		HL, BC	
0237'	38 BE		00359		JR		C, MERG1	
0239'	C9		00360		RET			; IF NOT TO END YET
			00361	*EJECT				

```

023A'
023A' DD E5      00362 SORT:  PUSH  IX      ; BUBBLE SORT; MAX N=3FF *****
023C' 2A 031E'  00363      LD    HL, (HEADER) ; SORTS 4-BYTE FIELDS *
023F' 01 0326'  00364      LD    BC, ORGEND   ; ON 1ST 2 BYTES ****
0242' 97      00365      SUB   A           ; ZERO IT OUT & CLEAR CARRY FLAG
0243' ED 42      00366      SBC   HL, BC
0245' 3E 03      00367      LD    A, 03      ; NOW DIVIDE BY 4
0247' A4      00368      AND   H
0248' 06 06      00369      LD    B, 6
024A' CB 27      00370 SH6.  SLA   A
024C' 10 FC      00371      DJNZ  SH6
024E' CB 3D      00372      SRL  L
0250' CB 3D      00373      SRL  L
0252' B5      00374      OR   L
0253' 4F      00375      LD   C, A       ; #OF FIELDS (<100H)
0254' 21 0326'  00376 AGAIN: LD   HL, ORGEND
0257' 97      00377      SUB   A           ; CLEAR CARRY FLAG
0258' 08      00378      EX   AF, AF'    ; SAVE IT OUT OF SIGHT
0259' 41      00379      LD   B, C
025A' 05      00380      DEC  B
025B' 05      00381 LOOP:  PUSH  BC
025C' E5      00382      PUSH HL
025D' DD E1      00383      POP  IX
025F' DD 56 01  00384      LD   D, (IX+1)
0262' DD 5E 00  00385      LD   E, (IX+0)
0265' DD 66 05  00386      LD   H, (IX+5)
0268' DD 6E 04  00387      LD   L, (IX+4)
026B' 97      00388      SUB   A
026C' ED 52      00389      SBC  HL, DE
026E' 01 0004  00390      LD   BC, 0004
0271' 30 1D      00391      JR   NC, NOSWAP
0273' 11 0320'  00392      LD   DE, TEMP1
0276' DD E5      00393      PUSH IX
0278' E1      00394      POP  HL
0279' ED B0      00395      LDIR
027B' 01 0004  00396      LD   BC, 0004
027E' DD E5      00397      PUSH IX
0280' D1      00398      POP  DE
0281' ED B0      00399      LDIR
0283' 01 0004  00400      LD   BC, 0004
0286' 21 0320'  00401      LD   HL, TEMP1
0289' ED B0      00402      LDIR
028B' 37      00403      SCF                    ; SWAP FLAG, WE'LL CALL IT
028C' 08      00404      EX   AF, AF'    ; PUT IT AWAY FOR A WHILE
028D' 01 0004  00405      LD   BC, 0004
0290' DD E5      00406 NOSWAP: PUSH  IX
0292' E1      00407      POP  HL
0293' 09      00408      ADD  HL, BC
0294' C1      00409      POP  BC
0295' 10 C4      00410      DJNZ LOOP
0297' 08      00411      EX   AF, AF'    ; BRING BACK SWAP FLAG
0298' 38 BA      00412      JR   C, AGAIN
029A' DD E1      00413      POP  IX
029C' C9      00414      RET
00415 $EJECT

```

```

029D'
029D' 11 F990      00416 MESS:  LD      DE, OF990
02A0' 01 02B0'    00417      LD      BC, MESS1
02A3' 7D          00418      LD      A, L
02A4' B9          00419      CP      C
02A5' 01 0018    00420      LD      BC, 18
02A8' 20 03      00421      JR      NZ, N1
02AA' 01 0044    00422      LD      BC, 44
                00423 ;
                00424 N1:  LDIR
02AD' ED B0      00425      RET
02AF' C9
    
```

```

; SCREEN LOCKIN
; CHECK WHICH MESS
; ASSUME SHORT ONE
; JUMP IF SO
    
```

```

02B0' 20 59 4F
02B3' 55 20 48
02B6' 41 56 45
02B9' 20 55 53
02BC' 45 44 20
02BF' 43 4F 4D
02C2' 50 55 54
02C5' 45 44 20
02C8' 4A 55 4D
02CB' 50 52 20
02CE' 2D 2D 20
02D1' 50 4C 45
02D4' 41 53 45
02D7' 20 45 4E
02DA' 54 45 52
02DD' 20 4F 52
02E0' 47 2D 45
02E3' 4E 44 20
02E6' 54 41 42
02E9' 4C 45 20
02EC' 56 41 4C
02EF' 55 45 53
02F2' 2E 20
02F4' 20 4F 52
02F7' 47 2D 43
02FA' 4E 44 20
02FD' 54 41 42
0300' 4C 45 20
0303' 4F 56 45
0306' 52 46 4C
0309' 4F 57 20
030C' 54 41 42
030F' 4C 45 20
0312' 45 4E 54
0315' 52 49 45
0318' 53
    
```

```

00426 ;
00427 MESS1:  DEFM
    
```

YOU HAVE USED COMPUTED JUMPS --

00428

```

DEFM 'PLEASE ENTER ORG-END TABLE VALUES.'
    
```

00429 MESS2:

```

DEFM 'ORG-END TABLE OVERFLOW'
    
```

00430 MESS3:

```

DEFM 'TABLE ENTRIES'
    
```

```

00431 INDFLO: DEFS 1
00432 LENGTH: DEFS 2
00433 CURRNT: DEFS 2
00434 HEADER: DEFS 2
00435 TEMP1:  DEFS 4
00436 BUFF:   DEFS 2
00437 ORGEND: DEFS 400
    
```

```

0319'
031A'
031C'
031E'
0320'
0324'
0326'
    
```

00439

END

MACROS:

SYMBOLS:

AGAIN	0254	B4	0048	BRANCH	0044	BUFF	0324	CHEKR	018A
DICO	00C0*	CKEND	01B8	CO	010C	COMH	0005	COML	0082
CURRNT	0310	CURSES	0129*	CURSH	0000	CURSL	00FF	CURSOR	00AD*
FIRST	002E	HEADER	031E	HEXIT	00DA*	HLIND	016F	INDFLG	0319
INFO	002F*	INMESS	F8F0	JRS	017A	KEYIN	0116*	LENGTH	031A
LOOP	025B	MERG1	01F7	MERGE	01F3	MESS	029D	MESS1	02B0
MESS2	02F4	MESS3	030C	N1	02AD	N9	005A	NEW1	01C7
NEXPR	0229	NEXT1	0199	NEXT2	01B1	NOPE	004E	NOSWAP	0290
RGEND	0326	RST3	013E	SCLOC	F8FF	SH6	024A	SIGN	0184
SORT	023A	SORTT	007E	SYN	0106*	SYN2	00FF	SYN3	010D
T1	00CF	T2	00A0	T3	011A	T5	00B7	T6	013F
TARGET	01EF	TARGET	0144	TEMP1	0320	TRACK	00001	TRRET	0136

NO FATAL ERROR(3)

0010

```
00001 .RADIX 16
00002 ;
00003 ; THIS ROUTINE SIMULATES THE EXECUTION OF A Z-80
00004 ; INSTRUCTION. THE STATE OF THE SIMULATEE'S REGISTERS
00005 ; (WITH THE EXCEPTION OF THE REFRESH REGISTER, AND THE
00006 ; PROGRAM COUNTER) IS RESTORED BEFORE THE EXECUTION OF
00007 ; EACH (SIMULATED) INSTRUCTION, AND SAVED IMMEDIATELY
00008 ; AFTER. THE STATE OF IFF-1 IS TREATED SIMILARLY.
00009 ; CALLS TO THE OPERATING SYSTEM ROUTINES, AS WELL AS TO
00010 ; INTERRUPT SERVICING ROUTINES (VIA IM-2) ARE EXECUTED
00011 ; COMPLETELY BEFORE RETURNING CONTROL TO THE HOST
00012 ; PROGRAM.
00013 ;
00014 ; THE ABILITY TO TRACE INTERRUPT SERVICE ROUTINES
00015 ; DYNAMICALLY WITHIN A PROGRAM WILL BE ADDED AT A LATER
00016 ; DATE. PRESENTLY INTERRUPT SERVICE ROUTINES CAN BE
00017 ; TRACED INDEPENDENTLY BY FORCING ZIP TO REQUEST ORG-
00018 ; END TABLE VALUES AND THEN GIVING IT THE ORG-END FOR
00019 ; THE SERVICE ROUTINE. AT THAT POINT YOU ARE FREE TO USE
00020 ; THE SET INSTRUCTION TO CHANGE THE REGISTER CONTENTS TO
00021 ; WHATEVER VALUES THEY WOULD NORMALLY HOLD.
00022 ;
00023 ; THE SIMUL ROUTINE EXPECTS THE INSTRUCTION TO BE
00024 ; SIMULATED TO BE LOCATED IN THE LOCATION
00025 ; POINTED BY THE CONTENTS OF REGPC.
00026 ;
00027 ; BY GREG BUZZARD 3-81
00028 ; LAST UPDATED 08-08-81
00029 ;
00030 ; EXTRN INFO ; INSTRUCTION INFO ROUTINE
00031 ;
00032 ; ENTRY PRESAV
00033 ; ENTRY SIMUL
00034 ; ENTRY TARGET
00035 ; ENTRY REGSAV ; USER REGISTER SAVE AREA
00036 ; ENTRY REGAF
00037 ; ENTRY REGF
00038 ; ENTRY REGA
00039 ; ENTRY REGBC
00040 ; ENTRY REGC
00041 ; ENTRY REGB
00042 ; ENTRY REGDE
00043 ; ENTRY REGE
00044 ; ENTRY REGD
00045 ; ENTRY REGHL
00046 ; ENTRY REGL
00047 ; ENTRY REGH
00048 ; ENTRY REGIX
00049 ; ENTRY REGSP
00050 ; ENTRY REGI
00051 ; ENTRY REGIY
00052 ; ENTRY REGPC
00053 ; ENTRY REGR
00054 ; ENTRY TEMP
00055 ; ENTRY XRAF
00056 ; ENTRY XREB
```

0047		00057			
		00058	OPSYS	EQU	47
		00059			
0000	F3	00060	SIMUL:	PUSH	AF
0001	C5	00061		PUSH	BC
0002	D5	00062		PUSH	DE
0003	E5	00063		PUSH	HL
		00064			
0004	01 0007	00065		LD	BC, 7
0007	11 0174	00066		LD	DE, WORK
000A	21 0233	00067		LD	HL, ZERO
000D	ED B0	00068		LDIR	
		00069			
000F	ED 43	00070		LD	(REPT), BC
0011	0272				
0013	ED 43	00071		LD	(REG), BC
0015	0270				
		00072			
		00073	GET INSTRUCTION, INFO, AND LOAD INSTRUCTION INTO		
		00074	THE WORK AREA		
		00075	LD	HL, (REGPC)	LOAD USER'S REGPC
0017	2A 026D	00076	CALL	INFO	GET INSTR INFO
001A	CD 0000*	00077	LD	(SAVE), A	SAVE INFO BYTE
001D	32 023B	00078	AND	03	
0020	E6 03	00079	INC	A	# OF BYTES IN INSTR
0022	3C	00080	LD	(LEN), A	SAVE IT
0023	32 023A	00081			
		00082	LD	C, A	
0026	4F	00083	LD	B, 0	
0027	06 00	00084	PUSH	HL	SAVE HL
0029	E5	00085			
		00086	ADD	HL, BC	INCREMENT REGPC VALUE
002A	09	00087	LD	(REGPC), HL	STORE IT
002B	22 026D	00088			
		00089	POP	HL	GET HL BACK
002E	E1	00090	LD	DE, WORK	
002F	11 0174	00091	LDIR		MOVE INSTR TO WORK
0032	ED B0	00092			
		00093	CHECK FOR SOME OF THE SPECIAL CASE INSTRUCTIONS		
0034	3A 0174	00094	LD	A, (WORK)	CHECK FOR DI OR EI
0037	FE FB	00095	CP	OFB	
0039	20 08	00096	JR	NZ, S1	JUMP IF NOT EI
003B	3E FB	00097	LD	A, OFB	
003D	32 0173	00098	LD	(EINT), A	ENABLE INTERRUPT
0040	C3 01EC	00099	JP	D3	
		00100			
0043	FE F3	00101	S1: CP	OF3	
0045	20 08	00102	JR	NZ, S2	JUMP IF NOT DI
0047	3E 00	00103	LD	A, 0	
0049	32 0173	00104	LD	(EINT), A	DISABLE INTERRUPT
004C	C3 01EC	00105	JP	D3	
		00106			
004F	FE 08	00107	S2: CP	08	WAS IT EX AF ?
0051	CA 01FA	00108	JP	Z, EXAF	IF SO JUMP
0054	FE D9	00109	CP	0D9	WAS IT EXX ?
0056	CA 020A	00110	JP	Z, EXX	IF SO JUMP



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00111 ;
0059  FE ED    00112    CP      OED                ; FIRST BYTE ED ?
005B  20 0E    00113    JR      NZ, 99             ; JUMP IF NOT
005D  3A 0175  00114    LD      A, (WORK+1)      ; LOOK AT NEXT BYTE
0060  FE 4F    00115    CP      4F                ; IS IT A LD R, A ?
0062  20 07    00116    JR      NZ, 99             ; JUMP IF NOT
                                00117 ;
0064  3A 025F  00118    LD      A, (REGA)        ; GET A, PUT A+1
0067  3C      00119    INC     A                 ; INTO REGR, IT IS
0068  32 026F  00120    LD      (REGR), A        ; DECREMENTED LATER
                                00121 ;
                                00122 ; CHECK FOR REPEATING INSTRUCTIONS (I. E. LDDR, OTIR, ET
                                )
006B  3A 0174  00123 93:    LD      A, (WORK)        ; GET FIRST BYTE
006E  FE ED    00124    CP      OED                ;
0070  20 13    00125    JR      NZ, CONT         ; IF NOT OED, CONT
                                00126 ;
0072  3A 0175  00127    LD      A, (WORK+1)      ; GET 2ND BYTE
0075  E6 54    00128    AND     0F4              ;
0077  FE 50    00129    CP      0E0              ; IS IT GONE?
0079  20 0C    00130    JR      NZ, CONT         ; IF NOT, CONT
                                00131 ;
007B  1B 57    00132    BIT     2, A              ; CHECK BIT 2
007D  20 08    00133    JR      NZ, CONT         ; IF 1, -CONT
                                00134 ;
007F  ED 4E    00135    LD      BC, (REGBC)      ; SAVE REGBC
0081  0280  00136    LD      (REPT), BC
0083  ED 48    00137 ;
0085  0272  00138 ; CHECK IF THE INSTRUCTION AFFECTS THE PC
                                00139 ; IF IT DOESN'T, JUMP TO N1
0087  3A 021B  00140 CONT. LD      A, (SAVE)        ; GET INFO BYTE
008A  0B 67    00141    BIT     4, A              ; DOES IT CHANGE PC?
008C  0A 0151  00142    JP      Z, N1             ; IF NOT JUMP
                                00143 ;
008F  3E 1D    00144    LD      A, 0CD           ; 1ST BYTE OF CALL
0091  32 0178  00145    LD      (CATCH), A
0094  21 0128  00146    LD      HL, CAUGHT       ; ADDRESS OF CALL
0097  22 0179  00147    LD      (CATCH+1), HL
                                00148 ;
009A  3A 026D  00149    LD      HL, (REGPC)      ; SAVE IT
009D  22 0231  00150    LD      (OLDPC), HL     ; FOR NON-JUMP CALLS
                                00151 ;
00A0  3A 0174  00152    LD      A, (WORK)        ; FIRST INSTR BYTE
00A3  0B 7F    00153    BIT     7, A              ; IS IT A RELATIVE JUM
00A5  20 1C    00154    JR      NZ, NZ           ; IF NOT JUMP
                                00155 ;
                                00156 ; HERE WE DEAL WITH RELATIVE JUMPS
00A7  3A 0175  00157    LD      A, (WORK+1)      ; GET DISPLACEMENT
00AA  4F      00158    LD      L, A
00AB  26 00    00159    LD      H, 0
00AD  0B 7F    00160    BIT     7, A              ; CHECK FOR NEG.
00AF  29 02    00161    JR      Z, CNT1          ; JUMP IF POS.
                                00162 ;
00B1  26 FF    00163    LD      H, OFF           ; PROPAGATE THE 1

```

```

00B3' ED 5B      00164 CNT1: LD      DE, (REGPC)      ; GET USER REGPC
00B5' 026D'
00B7' 19         00165      ADD     HL, DE          ; ADD IN DISPLACEMENT
00B8' 22 026D'  00166      LD      (REGPC), HL      ; PUT IT BACK
00BB' 3E 05      00167      LD      A, 5
00BD' 32 0175'  00168      LD      (WORK+1), A
00C0' 03 0151'  00169      JP      N1          ; INSERT PSEUDO-DISPL.
                    00170 ;
                    00171 ; HERE WE DEAL WITH ABSOLUTE JUMPS
00C3' FE 03      00172 N2:   CP      0C3          ; IS IT ABS. JUMP?
00C5' 20 14      00173      JR      NZ, N2. 1      ; IF NOT, JUMP
00C7' 2A 0175'  00174      LD      HL, (WORK+1)    ; GET TARGET
00CA' 3E 47      00175      LD      A, OPSYS        ; IS IT IN OPSYS?
00CC' BC         00176      CP      H
00CD' F2 0151'  00177      JP      P, N1          ; IF SO, JUMP
00D0' 22 026D'  00178      LD      (REGPC), HL      ; ELSE, UPDATE REGPC
00D3' 21 017B'  00179      LD      HL, TARGET      ; INSTALL PSEUDO TARGET
00D6' 22 0175'  00180      LD      (WORK+1), HL
00D9' 18 76      00181      JR      N1
                    00182 ;
                    00183 ; HERE WE DEAL WITH COMPUTED JUMPS
00DB' 11 017B'  00184 N2. 1: LD     DE, TARGET      ; GET PSEUDO-TARGET
00DE' FE E9      00185      CP      0E9          ; IS IT JP(HL)?
00E0' 20 0C      00186      JR      NZ, C1          ; IF NOT JUMP
00E2' 2A 0264'  00187      LD      HL, (REGHL)     ; SAVE HL
00E5' 22 0270'  00188      LD      (REG), HL
00E9' ED 53      00189      LD      (REGHL), DE     ; INSERT PSEUDO-TARGET
00EA' 0264'
00EC' 18 63      00190      JR      N1
                    00191 ;
00EE' FE DD      00192 C1:   CP      ODD          ; IS IT JP(IX)?
00F0' 20 0C      00193      JR      NZ, C2          ; IF NOT JUMP
00F2' 2A 0266'  00194      LD      HL, (REGIX)     ; SAVE IX
00F5' 22 0270'  00195      LD      (REG), HL
00F8' ED 53      00196      LD      (REGIX), DE     ; INSERT PSEUDO-TARGET
00FA' 0266'
00FC' 18 53      00197      JR      N1
                    00198 ;
00FE' FE FD      00199 C2:   CP      OFD          ; IS IT JP(IY)?
0100' 20 0C      00200      JR      NZ, N3          ; IF NOT JUMP
0102' 2A 026B'  00201      LD      HL, (REGIY)     ; SAVE IY
0105' 22 0270'  00202      LD      (REG), HL
0108' ED 53      00203      LD      (REGIY), DE     ; INSERT PSEUDO-TARGET
010A' 026B'
010C' 18 43      00204      JR      N1
                    00205 ;
                    00206 ; HERE WE DEAL WITH RESETS, RETURNS, AND INSTRUCTIONS
010E' FE ED      00207 ; WHICH AFFECT THE STACK
0110' 28 2D      00208 N3:   CP      OED          ; RETURN INSTR?
0112' E6 C7      00209      JR      Z, CSTK        ; IF SO JUMP
0114' FE C7      00210 ;
0116' CA 0173'  00211      AND     OC7          ; CHECK FOR RST
0119' E6 07      00212      CP      OC7          ; IS IT RST?
                    00213      JP      Z, EINT        ; IF SO JUMP
                    00214 ;
                    00215      AND     07

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011B	FE 02	00216	CP	2	, TARGET IN STACK?
011D	FA 013F	00217	JP	M, CSTK	, IF SO JUMP
		00218			
0120	FE 05	00219	CP	5	, IS IT CALL?
0122	FA 012B	00220	JP	M, N4	, IF NOT JUMP
		00221			
0125	2A 026D	00222	LD	HL, (REGPC)	, CALL INSTR
0128	22 0270	00223	LD	(REG), HL	, SAVE RETURN ADDRESS
		00224			
012E	2A 0175	00225 N4:	LD	HL, (WORK+1)	, GET JUMP TARGET
012E	3E 47	00226	LD	A, OPSYS	
0130	BC	00227	CP	H	, IS IT CALL TO SPDS?
0131	F2 0151	00228	JP	P, N1	, IF SO LET IT THRU
		00229			
0134	22 026D	00230	LD	(REGPC), HL	, ELSE STORE IT IN REGP
0137	21 017B	00231	LD	HL, TARGT	
013A	22 0175	00232	LD	(WORK+1), HL	, INSERT PSEUDO-TARGET
013D	1B 12	00233	JR	N1	
		00234			
013F	ED 73	00235 CSTK.	LD	(STKPT), SP	, SAVE HOST'S SP
0141	0230				
0143	ED 7B	00236	LD	SP, (REGSP)	, GET USER'S SP
0145	0268				
0147	E1	00237	POP	HL	, GET RETURN ADDRESS
0148	22 026D	00238	LD	(REGPC), HL	, STORE IT
014B	21 017B	00239	LD	HL, TARGT	, GET PSEUDO-RET ADDR
014E	ES	00240	PUSH	HL	, STACK IT
014F	1B 03	00241	JR	#+5	, !!!RELATIVE JUMP!!!
		00242			
		00243	, LOAD THE USER'S REGISTERS AND SET UP THE SIMULATION		
0151	ED 73	00244 N1:	LD	(STKPT), SP	, SAVE HOST'S SP
0153	0230				
0155	3A 026A	00245	LD	A, (REGI)	
0158	ED 47	00246	LD	I, A	, RESTORE USER I REG
015A	3A 026F	00247	LD	A, (REGR)	
015D	D6 0F	00248	SUB	OF	, ADJUST FOR CORRECT P
				R	
					, IN WORK AREA
015F	00	00249	NOP		, RESTORE USER R REG
0160	ED 4F	00250	LD	R, A	, RETRIEVE USER REG'S
0162	31 025E	00251	LD	SP, REGSAV	
0165	F1	00252	POP	AF	
0166	C1	00253	POP	BC	
0167	D1	00254	POP	DE	
0168	E1	00255	POP	HL	
0169	DD E1	00256	POP	IX	
016B	FE 2A	00257	LD	IY, (REGIY)	
016D	026B				
016F	ED 7B	00258	LD	SP, (REGSP)	, LOAD USER'S SP
0171	0268				
		00259			
		00260	, SIMULATION AREA		
0173	00	00261	EINT:	DEFS 00	, EI OR DI
0174		00262	WORK:	DEFS 4	
0178		00263	CATCH:	DEFS 3	
017B	F3	00264	TARGT:	DI	
		00265			

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0170' ED 73      00266 ; RESAVE USER'S REGISTERS
017E' 0268'     00267      LD      (REGSP), SP      ; SAVE USER'S SP
0180' 31 0268'  00268      LD      SP, REGSP
0183' DD E5     00269      PUSH   IX      ; SAVE USER'S REGS
0185' E5       00270      PUSH   HL
0186' D5       00271      PUSH   DE
0187' C5       00272      PUSH   BC
0188' F5       00273      PUSH   AF
0189' FD 22    00274      LD      (REGIY), IY
018B' 026B'
018D' ED 57'    00275      LD      A, I
018F' 32 026A'  00276      LD      (REGI), A      ; SAVE USER'S I REG
0192' ED 7B    00277      LD      SP, (STKPT)    ; RESTORE HOST'S SP
0194' 023C'
                                00278 ;
                                00279 ; CHECK IF INSTRUCTION WAS A REPEATER (OTIR ETC.), IF
                                00280 ; SO ADJUST THE R-REGISTER ACCORDINGLY.
0196' 01 0000   00281      LD      BC, 00
0199' 2A 0272'  00282      LD      HL, (REPT)    ; CHECK FOR ZERO
019C' BF       00283      CP      A      ; CLEAR CARRY FLAG
019D' ED 4A    00284      ADC    HL, BC
019F' 23 11    00285      JR      Z, DOO      ; IF NOT ZERO, JUMP
                                00286 ;
01A1' BF       00287      CP      A      ; ZERO CARRY FLAG
01A2' ED 4B    00288      LD      BC, (REGBC)
01A4' 0260'
01A6' ED 42    00289      SBC    HL, BC      ; # TIMES EXECUTED
01A8' 3A 026F'  00290      LD      A, (REGR)    ; GET REGR
01AB' 95       00291      SUB    L      ; DECREMENT IT
01AC' 3C       00292      INC    A      ; ADD 1 (IT GETS
                                00293 ; RE-DECREMENTED
                                00294 ; AT D3)
01AD' CB EF    00295      RES    7, A      ; ZERO THE MSB
01AF' 32 026F'  00296      LD      (REGR), A
                                00297 ;
                                00298 ;
01B2' 2A 0270'  00299 DOO:   LD      HL, (REG)    ; GET SAVED REG
                                00300 ;
01B5' 3E 00    00301      LD      A, 0
01B7' BC       00302      CP      H      ; IS HIGH BYTE EMPTY?
01B8' 20 03    00303      JR      NZ, DO      ; IF NOT JUMP
                                00304 ;
01BA' BD       00305      CP      L      ; IS LOW BYTE EMPTY?
01BB' 28 2F    00306      JR      Z, D3      ; IF SO DONE
                                00307 ;
                                00308 ; RESTORE REGISTERS WHICH MAY HAVE BEEN MODIFIED
01BD' 3A 0174'  00309 DO:   LD      A, (WORK)    ; GET SIM-INSTR
01C0' FE E9    00310      CP      0E9      ; WAS IT JP(HL)?
01C2' 20 06    00311      JR      NZ, D1      ; IF NOT JUMP
                                00312 ;
01C4' 22 0264'  00313      LD      (REGHL), HL ; REPLACE REG
01C7' 22 026D'  00314      LD      (REGPC), HL ; UPDATE REGPC
                                00315 ;
01CA' FE DD    00316 D1:   CP      0DD      ; WAS IT JP(IX)?
01CC' 20 06    00317      JR      NZ, D2      ; IF NOT JUMP

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		00318 ,				
01CE	22 0266	00319	LD	(REGIX), HL		; REPLACE REG
01D1	22 026D	00320	LD	(REGPC), HL		; UPDATE REGPC
		00321 ,				
01D4	FE FD	00322 D2:	CP	OFD		; WAS IT JP(IY)?
01D6	20 06	00323	JR	NZ, D2, 5		; IF NOT JUMP
		00324 ,				
01D8	22 026B	00325	LD	(REGIY), HL		; REPLACE REG
01D9	22 026D	00326	LD	(REGPC), HL		; UPDATE REGPC
		00327 ,				
01DE	ED 73	00328 D2, 5:	LD	(STKPT), SP		; WAS CALL
01E0	01E0					
01E2	ED 7B	00329	LD	SP, (REGSP)		; GET USER SP
01E4	01E8					
01E6	D1	00330	POP	DE		; STRIP TOP VALUE
01E7	E3	00331	PUSH	HL		; PUSH RETURN ADDRESS
01E9	ED 7B	00332	LD	SP, (STKPT)		
01EA	023C					
		00333 ;				
		00334 ;				DECREMENT THE R REGISTER AND RETURN
01EC	3A 026F	00335 D3:	LD	A, (REGR)		; GET OLD R VALUE
01EF	3D	00336	DEC	A		; DECREMENT IT
01F0	0B EF	00337	RES	7, A		
01F2	32 026F	00338	LD	(REGR), A		; PUT R BACK
01F5	E1	00339	POP	HL		
01F8	D1	00340	POP	DE		
01F7	C1	00341	POP	BC		
01FB	F1	00342	POP	AF		
01F9	C9	00343	RET			
		00344 ;				
		00345 ;				THIS SECTION HANDLES THE ALTERNATE REGISTER SET
		00346 ;				SWAPS
01FA	2A 0256	00347 EXAF:	LD	HL, (XRAF)		; AF
01FD	ED 3B	00348	LD	DE, (REGAF)		; AF
01FF	025E					
0201	ED 33	00349	LD	(XRAF), DE		; EXCHANGE THEM
0203	0258					
0205	22 025E	00350	LD	(REGAF), HL		
0208	18 E1	00351	JR	D3		
		00352 ;				
020A	11 0250	00353 EXX:	LD	DE, TEMP		; TEMPORARY STORAGE
020D	21 0258	00354	LD	HL, XRBC		; SOURCE
0210	01 0006	00355	LD	BC, 6		
0213	ED E0	00356	LDIR			
0215	13	00357	INC	DE		; SKIP OVER XRAF
0216	13	00358	INC	DE		
0217	23	00359	INC	HL		
0218	23	00360	INC	HL		; SKIP OVER REGAF
0219	0E 06	00361	LD	C, 6		
021B	ED E0	00362	LDIR			
021D	21 0250	00363	LD	HL, TEMP		; SOURCE
0220	13	00364	INC	DE		; SKIP OVER REGAF
0221	13	00365	INC	DE		
0222	0E 06	00366	LD	C, 6		
0224	ED E0	00367	LDIR			
0226	18 C4	00368	JR	D3		

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00369 ;
00370 ; HANDLES JP CC'S WHEN JUMP WAS NOT TAKEN
0228' E5 00371 CAUGHT: PUSH HL ; REPLACE REGPC
0229' 2A 0231' 00372 LD HL,(OLDPC) ; WITH ITS NON-JUMP
022C' 22 026D' 00373 LD (REGPC),HL ; VALUE
022F' E1 00374 POP HL
0230' C9 00375 RET
00376 ;
0231' 00377 OLDPC: DEFS 2
0233' 00 00 00 00378 ZERO: DEFB 00,00,00,00,00,00,00
0236' 00 00 00
0239' 00
023A' 00379 LEN: DEFS 1
023B' 00380 SAVE: DEFS 1
023C' 00381 STKPT: DEFS 2
023E' 00382 PRESAV: DEFS 6 ; PREVIOUS REG AREA
0244' 00383 USERHL: DEFS 2
0246' 00384 USERIX: DEFS 2
0248' 00385 USESTK: DEFS 3
024B' 00386 USERIY: DEFS 5
0250' 00387 TEMP: DEFS 6
0256' 00388 XRAF: DEFS 2
0258' 00389 XRBC: DEFS 2
025A' 00390 XRDE: DEFS 2
025C' 00391 XRHL: DEFS 2
025E' 00392 REGSAV: DEFS 0 ; USER REGISTER SAVE AREA
025E' 00393 REGAF: DEFS 0
025E' 00394 REGF: DEFS 1
025F' 00395 REGA: DEFS 1
0260' 00396 REGBC: DEFS 0
0260' 00397 REGC: DEFS 1
0261' 00398 REGB: DEFS 1
0262' 00399 REGDE: DEFS 0
0262' 00400 REGE: DEFS 1
0263' 00401 REGD: DEFS 1
0264' 00402 REGHL: DEFS 0
0264' 00403 REGL: DEFS 1
0265' 00404 REGH: DEFS 1
0266' 00405 REGIX: DEFS 2
0268' 00406 REGSP: DEFS 2
026A' 00407 REGI: DEFS 1
026B' 00408 REGIY: DEFS 2
026D' 00409 REGPC: DEFS 2
026F' 00410 REGR: DEFS 1
0270' 00411 REG: DEFS 2
0272' 00412 REPT: DEFS 2
00413 END

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MACROS:

SYMBOLS:

C1	00EE'	C2	00FE'	CATCH	0178'	CAUGHT	0228'	CNT1	00B3'
CONT	0087'	CSTK	013F'	D0	01BD'	D00	01B2'	D1	01CA'
D2	01D4'	D2.5	01DE'	D3	01EC'	EINT	0173'	EXAF	01FA'
EXX	020A'	INFO	001B*	LEN	023A'	N1	0151'	N2	00C3'
NZ.1	00DB'	N3	010E'	N4	012B'	OLDPC	0231'	QFSYS	0047
PRESAV	023E1'	REG	0270'	REGA	025F1'	REGAF	025E1'	REGB	02611'
REGBC	02601'	REGC	02601'	REGD	02631'	REGDE	02621'	REGE	02621'
REGF	025E1'	REGH	02651'	REGHL	02641'	REGI	026A1'	REGIX	02661'
REGIY	026B1'	REGL	02641'	REGPC	026D1'	REGR	026F1'	REGSAV	025E1'
REGSP	02681'	REPT	0272'	S1	0043'	S2	004F'	S3	006B'
SAVE	023B'	SIMUL	0000I'	STKPT	023C'	TARGET	017B1'	TEMP	02501'
USERHL	0244'	USERIX	0246'	USERIY	024B'	USESTK	0248'	WORK	0174'
XRAF	02561'	XRBC	02581'	XRDE	025A'	XRHL	025C'	ZERO	0233'

NO FATAL ERROR(S)

